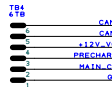
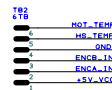
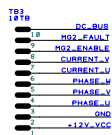


MCU BOARD			
IC2 BLUE_PILL			
M02_FWD_INHIBIT_13_P14	PB12	ONDI	P241 OND
M02_FPM1-N_34_P14	PB13	OND	P242 OND
M02_FPM2-N_35_P14	PB14	3.3V	P243 VCC_3V3_M02
M02_FPM3-N_36_P14	PB15	RESET	P244
M02_FPM4-P_41_P14	PA8	PB11	P245 M02_USART_RX_39
M02_FPM2-P_42_P14	PA9	PB10	P246 M02_USART_TX_28
M02_FPM3-P_43_P14	PA10	PB1	P247 M02_USAUX
M02_CAN_RX_44_P14	PA11	PB0	P248 M02_I2C_24
M02_CAN_TX_45_P14	PA12	PA7	P249 M02_ENG_4_29
M02_EMCIATOR-IN_28_P14	PA15	PA6	P250 M02_ENG_4_22
M02_ADCM_OUT_22_P14	PB3	PA11	P251 M02_I2C_11_1
M02_PRECHARGE_OUT_27_P14	PB4	PA4	P252 M02_TEMP_24
M02_CRUISE-IN_57_P14	PB5	PA3	P253 M02_TEMP_18
M02_START	PB6	PA2	P254 M02_USC_11
M02_FMD-IN_28_P14	PB7	PA1	P255 M02_REGEN_8
M02_REV-IN_37_P14	PB8	PA0	P256 M02_TEMP_11_9
M02_BRAKE-IN_14_P14	PB9	C15	P257 EPB_OUT
15V_VCC	PA16	PC14	P258 M02_EXC_OUT
OND	OND2	PC13	P259
VCC_3V3_M02	3.3V1	VBAT	P260

TO1 1070	
10	REV_IN
9	FOR_IN
8	START_IN
7	BRAKE_IN
6	GND
5	REGEN_IN
4	THROTTLE_IN
3	+5V_VCC
2	GND
1	+12VIN



The schematic diagram illustrates the power management section, featuring two 78K5A1 voltage regulators, IC4 and IC5. IC4, labeled 'IC4 BTB17BK5A1', is configured as a precharge regulator. Its input is connected to 'MO2_PRECHARGE_OUT_27_1' and its output is connected to 'PRECHARGE_7'. IC5, labeled 'IC5 BTB17BK5A1', is configured as the main power regulator. Its input is connected to 'MO2_DC5V_OUT_2_1' and its output is connected to 'MAIN_CON_7'. Both regulators have their ground pins connected to a common GND point.

The diagram shows a 1uF capacitor (C18) connected between the VCC-3V3-M02 supply and ground (GND). The TX, RX, and GND pins of the 7175B67-101LF module are connected to the same supply and ground lines.

CAN BUS

1uF
C17

15V_VCC

GND

MOS2_CAN_TX_451

MOS2_CAN_RX_444

5V_VCC

IC3
MCP2562-E-P

TXD
RXD
STBY
CANH
CANL
VDD
VIO

GND

CANH

CANL

VCC_3V3

MOS2

1uF
C21

GND

