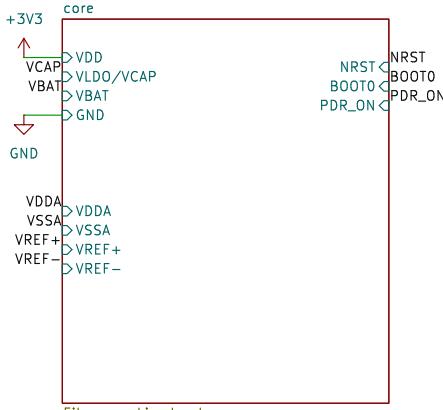
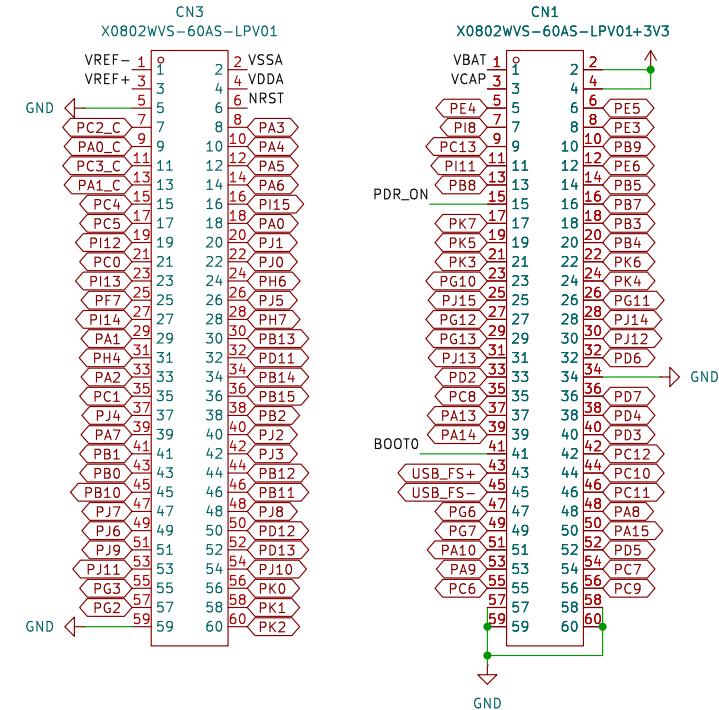
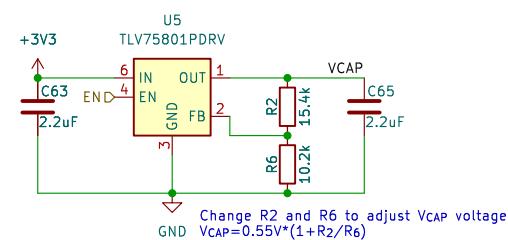


PDR_ON Pin: The PDR_ON pin is typically directly connected to VDD.
 BOOT0: BOOT0 has a 10K pull-down resistor.
 VSSA: VSSA must be connected to GND or AGND.
 VDDA: If VDDA is not externally supplied, it can be provided internally by soldering FB1. No external connection required.
 VBAT: VBAT must be connected to VDD or to a battery supply.
 VLDO/VCAP: If VLDO/VCAP is supplied externally, the LDO on the core board needs to be removed.



+3V3
TP1
GND
TP2



Sheet: /
 File: H750XB_V2.kicad_sch
Title:
 Size: A4 Date:
 KiCad E.D.A. 9.0.6-r1 Rev:
 Id: 1/2

