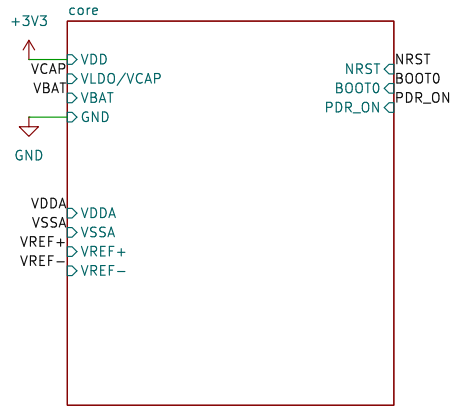
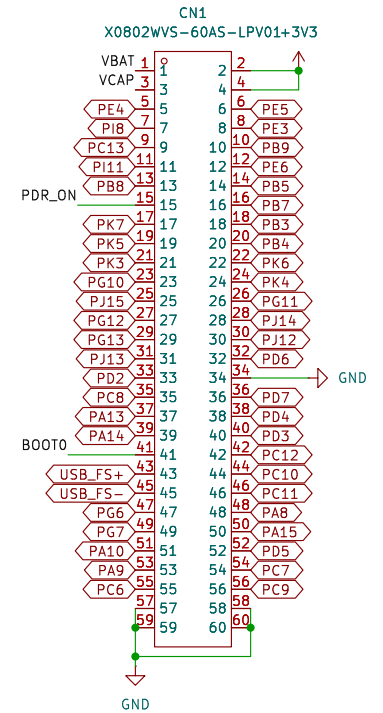
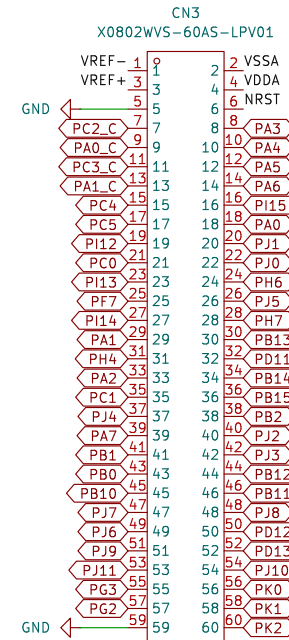
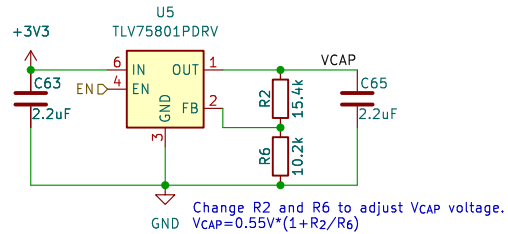
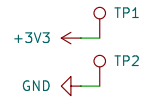


PDR\_ON Pin: The PDR\_ON pin is typically directly connected to VDD.  
BOOT0: BOOT0 has a 10K pull-down resistor.  
VSSA: VSSA must be connected to GND or AGND.  
VDDA: If VDDA is not externally supplied, it can be provided internally by soldering FB1. No external connection required.  
VBAT: VBAT must be connected to VDD or to a battery supply.  
VLDO/VCAP: If VLDO/VCAP is supplied externally, the LDO on the core board needs to be removed.



File: core.kicad\_sch



Sheet: /  
File: H750XB\_V2.kicad\_sch

**Title:**

Size: A4  
KiCad E.D.A. 9.0.6-r1

Date:

**Rev:**

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