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| G:\nsu-logo.png  **North South University**  Department of Electrical & Computer Engineering    **LAB REPORT**  Course Name:CSE332L  Experiment Number: **04**     |  | | --- | | Experiment Name: **Design of a 4-bit Binary Up-Down counter** |   Experiment Date: 31/03/2021  Report Submission Date: 08/03/2021  Faculty: SFM  Submitted to: Md Saidur Rahman  Section: 06 | |
| Student Name: **Koushik Banerjee** | Score |
| Student ID: **1812171642** |  |
| Remarks: |

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| --- | --- | --- | --- | --- |
| Clock pulse | A | B | C | D |
| P0 | 0 | 0 | 0 | 0 |
| P1 | 0 | 0 | 0 | 1 |
| P2 | 0 | 0 | 1 |  |
| P3 | 0 | 0 | 1 | 1 |
| P4 | 0 | 1 | 0 | 0 |
| P5 | 0 | 1 | 0 | 1 |
| P6 | 0 | 1 | 1 | 0 |
| P7 | 0 | 1 | 1 | 1 |
| P8 | 1 | 0 | 0 | 0 |
| P9 | 1 | 0 | 0 | 1 |
| P10 | 1 | 0 | 1 | 0 |
| P11 | 1 | 0 | 1 | 1 |
| P12 | 1 | 1 | 0 | 0 |
| P13 | 1 | 1 | 0 | 1 |
| P14 | 1 | 1 | 1 | 0 |
| P15 | 1 | 1 | 1 | 1 |