

EC/EE/CS & IT/IN



Digital Electronics

JK Flip Flop, D &
T Flip Flop,
Designing of Flip
flop





LECTURE NO. 8

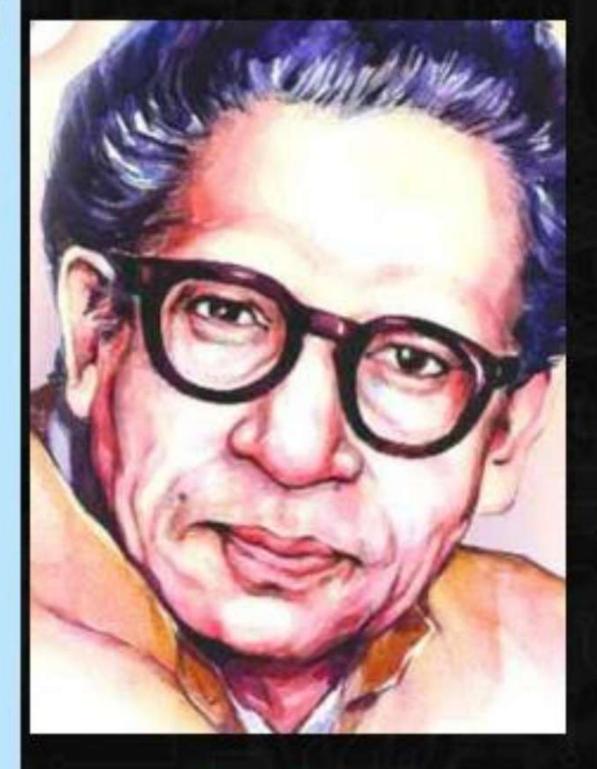
Chandan Jha Sir (CJ Sir)



कोशिश करने वालों की हार नहीं होती

कोशिश करने वालों की हार नहीं होती, लहरों से डरकर नौका पार नहीं होती। नन्हीं चीटी जब दाना लेकर चलती है, चढ़ती दीवारों पर, सौ बार फिसलती है। मन का विश्वास रगों में साहस भरता है, चढ़कर गिरना, गिरकर चढ़ना ना अख़रता है। आखिर उसकी मेहनत बेकार नहीं होती, कोशिश करने वालों की हार नहीं होती। डुबकियां सिंधु में गोताखोर लगाता है, जा जाकर खाली हाथ लौट आता है। मिलते नहीं सहज ही मोती गहरे पानी में, बढ़ता दुगुना उत्साह इसी हैरानी में। मुट्ठी उसकी खाली हर बार नहीं होती, कोशिश करने वालों की हार नहीं होती। असफलता एक चुनौती है, इसे स्वीकार करों, क्या कमी रह गयी, देखों और सुधार करों जब तक न सफल हो, नींद चैन से त्यागो तुम संघर्ष का मैदान छोड़कर मत भागो तुम। कुछ किये बिना ही जय जयकार नहीं होती, कोशिश करने वालों की हार नहीं होती।





RECAPE

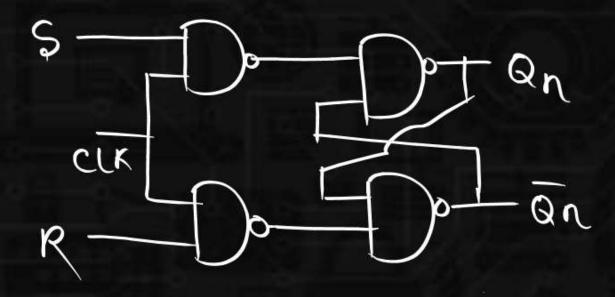


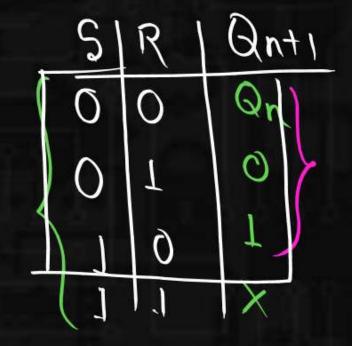
Latches.

SR. Latch

S D Q

S-R Flip-Flop.





Characteristic

Equation

anti= StROn

Excitation table.

Qn	Qn+1	S	R
9	9	0	X
0	Ţ	1	6
1	0	0	1
1	1	ر لا	^



TOPICS TO BE COVERED

01 JK Flip Flop

02 D Flip Flop

03 T Flip Flop

04 Desiging of Flip Flops

Jack Keilby.



(1) Symbol

(2) Truth Table

J	K	Q _{n+1}
0	0	Or
0	1	0
1	0	1
1	1	Qn



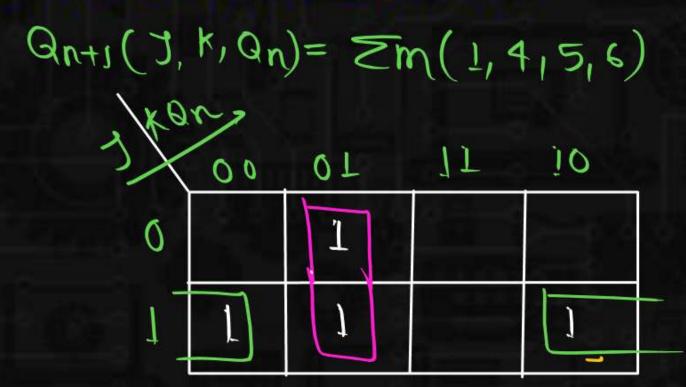
(3) Characteristic Table

ナ	K	Om+1
0	O	94
\mathcal{O}	1	0
L	b	L
1)	In

le	J	K	Q _n	Q _{n+1}
0	0	0	0	O
	0	0	1	1
2	0	1	0	0
3	0	1	1	0
9	1	0	0	1
(5)	1	0	1	1
(g)	1	1	0	1
7	1	1	1	0



(4) Characteristic Equation



$$Q_{n+1} = J\bar{Q}_n + \bar{K}Q_n$$

(5) Excitation Table

Qn	Q _{n+1}	J	K
0	0	0	X
0	1	1	X
1	0	X	1
1	1	X	0

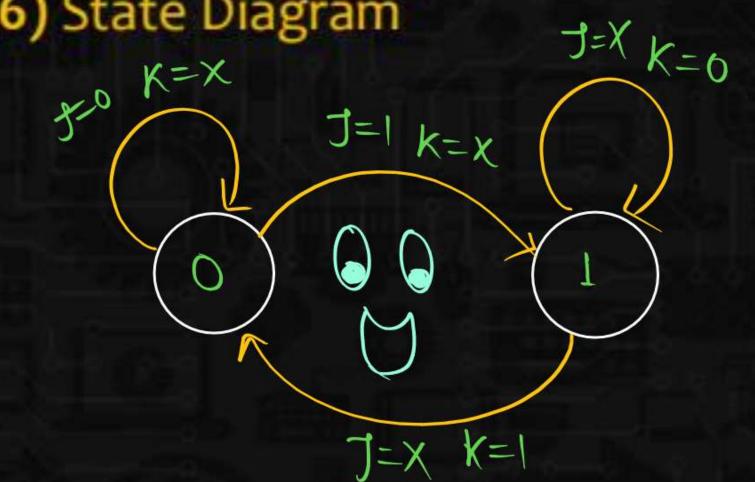


characteristic Table.

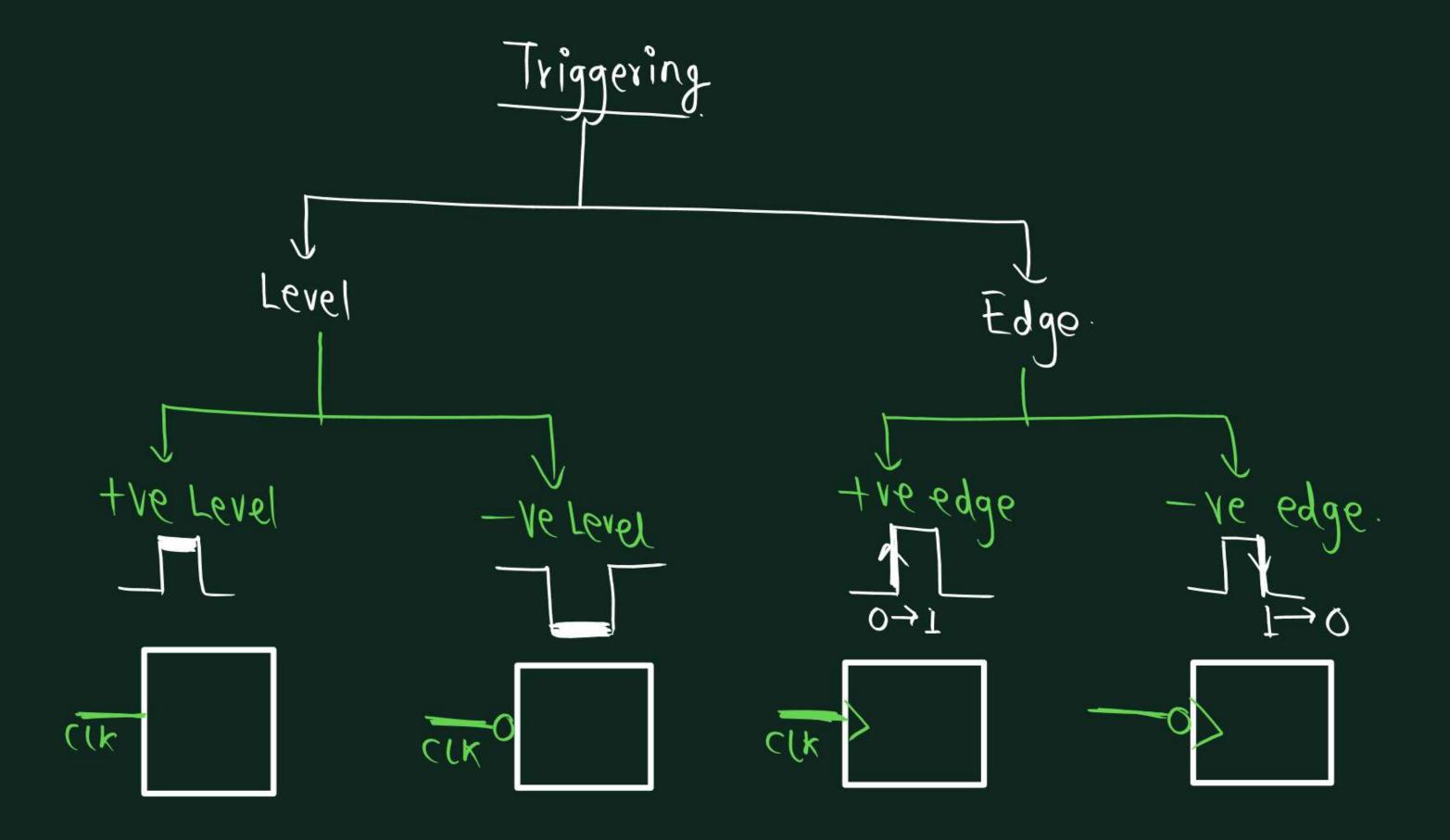
J	K	Q _n	Q _{n+1}
0	0	0	0
0	0	1 ←	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1 4	>1
1	1	0	1
1	1	1	0

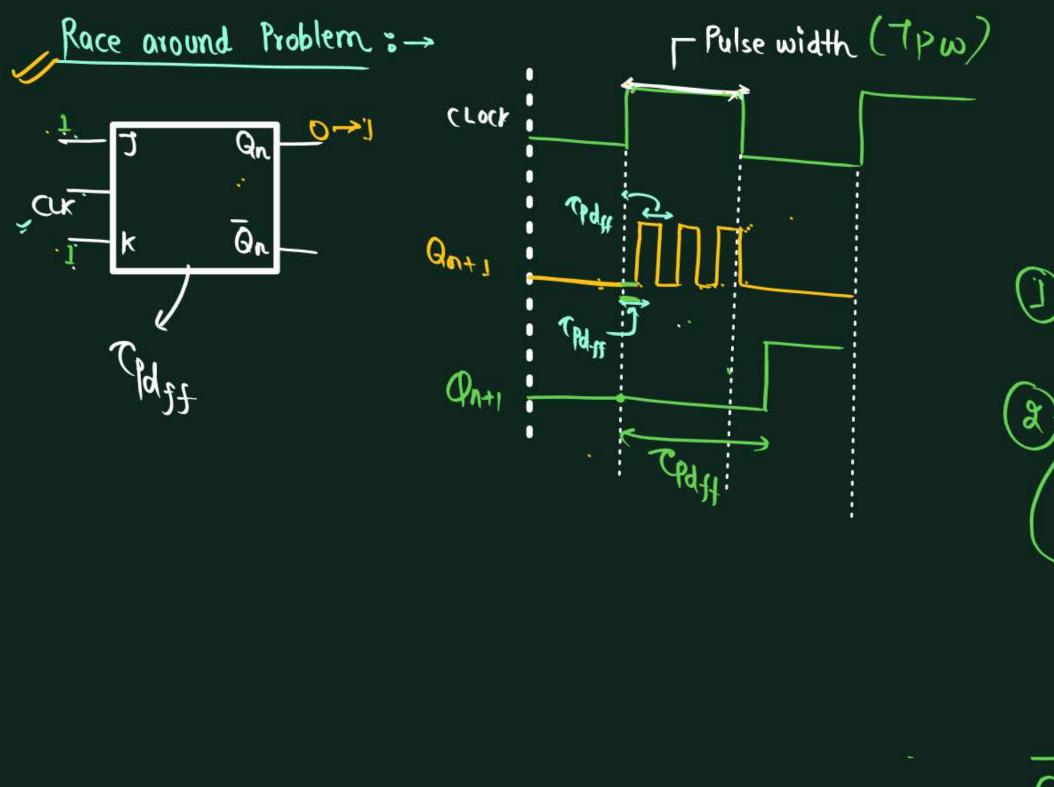






Q_n	Q _{n+1}	J	K
0	0	0	X
0	1	1	X
1	0	X	1
1	1	X	0





To avoide Race Around Problem

1) TPW < TPdff < TCLK

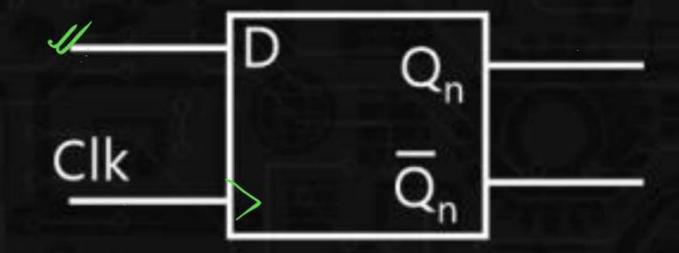
(2) Master-Slave chronit

DJ Wale Babu - Ashmit (t=10 min S Sp Rj DR Nishu Rahul. AR Unicorn

Auta, Fransparant

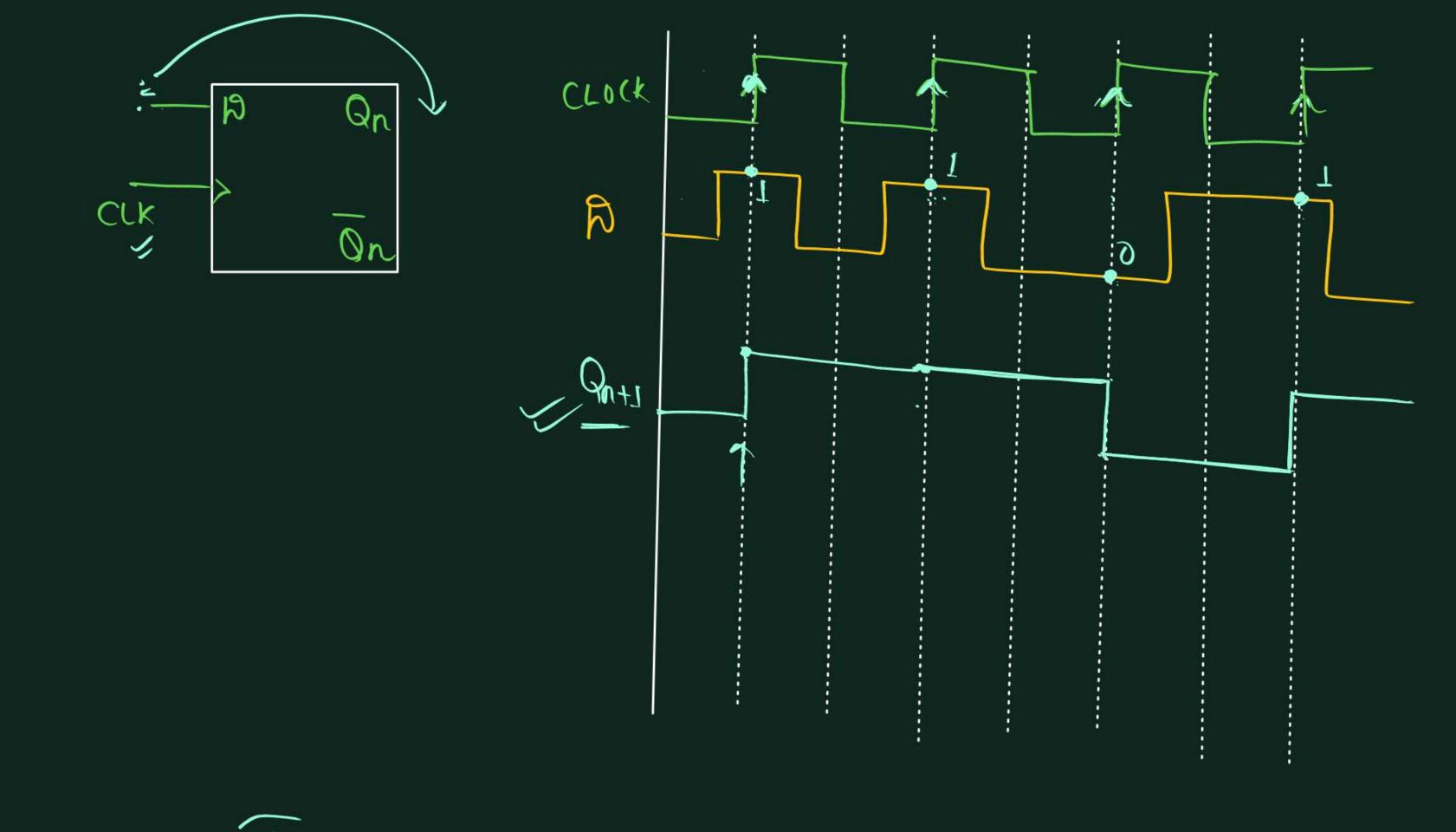


(1) Symbol



(2) Truth Table

D	Q _{n+1}
0	0 /
1	1.





(3) Characteristic Table

D	Qn	Q _{n+1}
• 0	0	0
0	1	0
1	0	1
1	1	1

$$Q_{n+1} = D \bar{Q}_n + D \bar{Q}_n$$

$$= D (\bar{Q}_n + \bar{Q}_n)$$



(4) Characteristic Equation

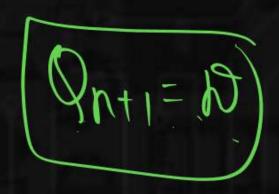
$$Q_{ht1} = \beta$$



(5) Excitation Table

60		
Qn	Q_{n+1}	D
0	0	0 ~
0	1	1 ~
1	0	0 ~
1	1	1

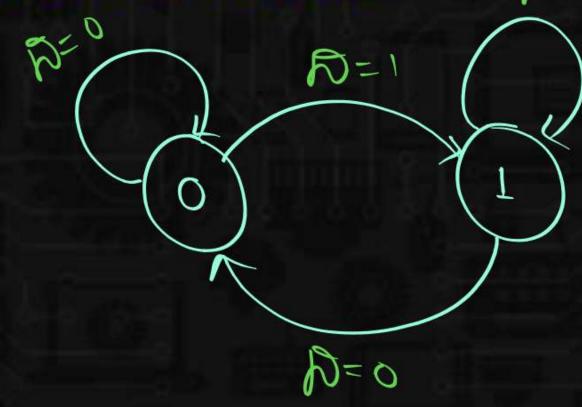
D	Q _n	Q _{n+1}
0 %	0	0
0	1.	0
1	0	1
1	1	1





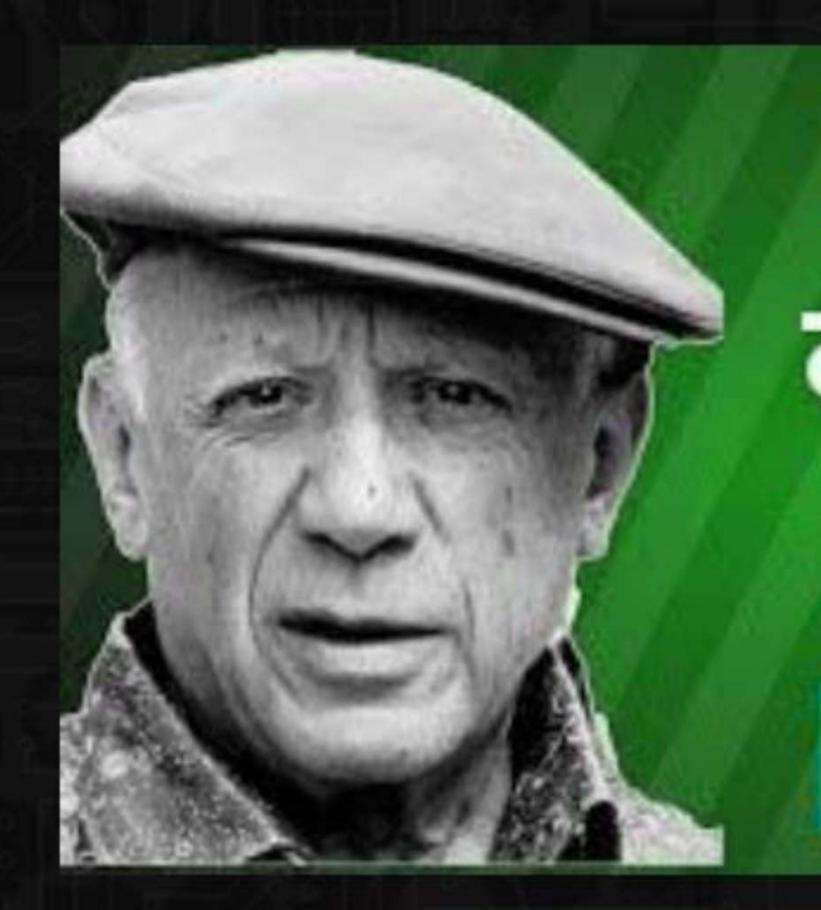
(6) State Diagram





Qn	Q _{n+1}	D
0	0	0
0	1	1
1	0	0
1	1	1



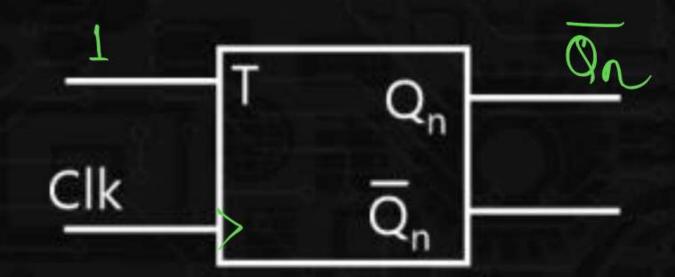


इतिहास का महान वित्रकार

Toggle FF



(1) Symbol



(2) Truth Table

T	Q _{n+1}
0	On
1	Qn

1-10

13nt1=13n ant1=3n



(3) Characteristic Table

101

ante = Tant Tan



(4) Characteristic Equation

$$Q_{n+1} = T \oplus Q_n$$

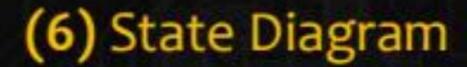


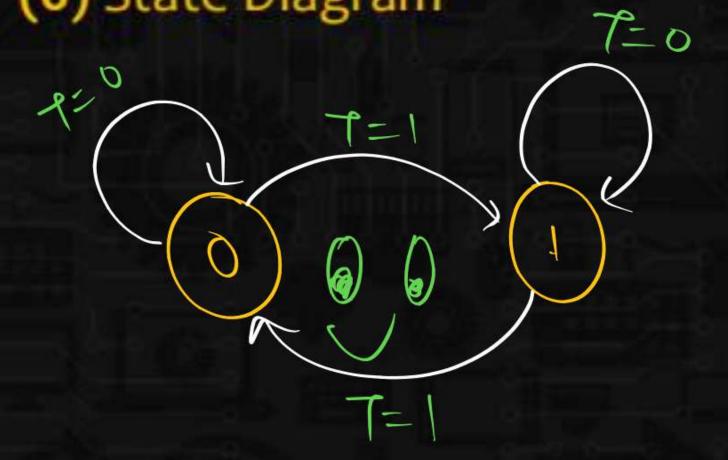
(5) Excitation Table

Qn	Q _{n+1}	T
0	0	0
0	1	1
1	0	1
1	1	0

T	Qn	Q _{n+1}
0 -	0	0
0	1	1
1	0	1
1	1	0







Qn	Q _{n+1}	P
0	0	0
0	1	1
1	0	Q
1	1	0

Q.1

The J-K FF shown below is initially cleared and then clocked for 5 pulses, the sequence at the Q output will be

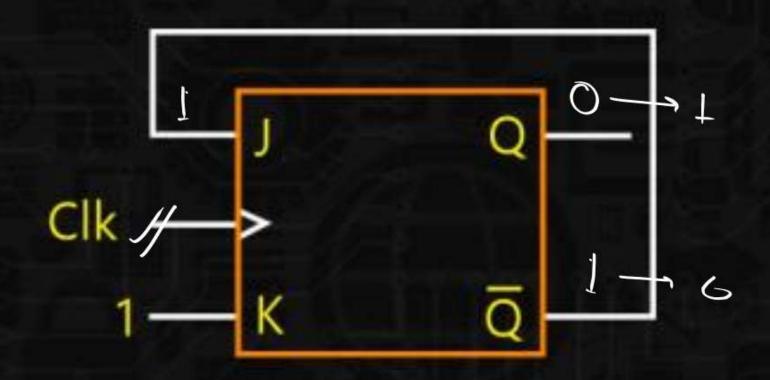


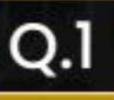
A. 010000

B. 011001

c. 010010

010101





For the circuit given below x & y condition will be-



- A. x stable y toggle
- B. x toggle y stable
- c. x & y both toggle
- D. x & y both stable

Designing of Flip Flops



avaliabliff - Aesired F

- Step (1): Write the characteristic table of desired Flip Flop.
- Step (2): Write the excitation table of available Flip Flop.

Step (3): Write the logical expression.

Step (4): Minimize the logical expression.

Step (5): Hardware implementation.

(CD)

thara table

Q.2

Design a D FF by using (T FF)

Excilation Table



Step 1

C Step 2

			✓
2	On	Q^{u+i}	T
0	0	Ō	0
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	1	0	1
	D	1	1
1		J	0

Besived Design a D FF by using SR Flip Flop?



Step. 1

A	On	Q _{n+j}	S	
0	0	0	0	X
0	1	0	0	1
1	0	1	1	0
1	1	1	X	0

ER. (JHA @ gmail com

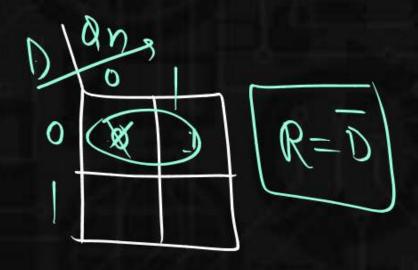
 $S(\tau,8n) = Zm(2) + Zd(3)$ Step3.

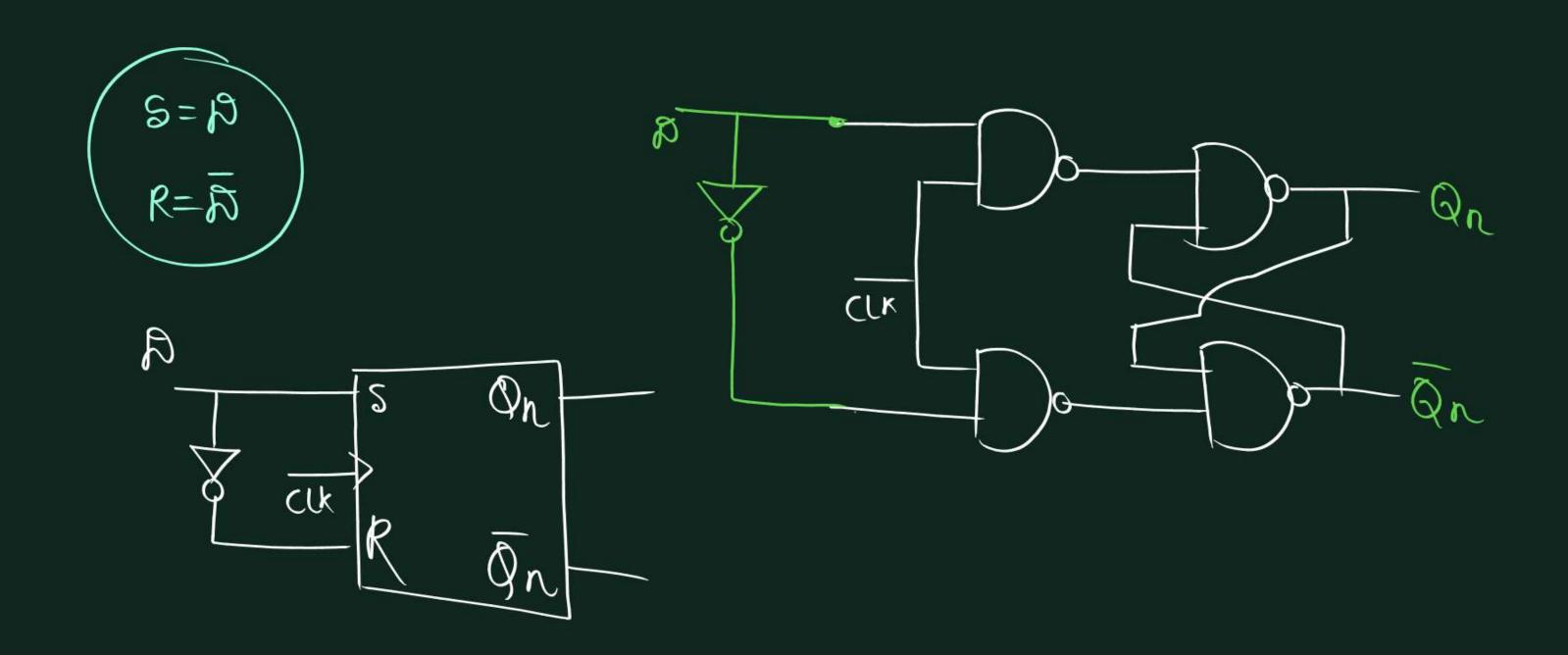
¿ Stepa

avaliable

$$\frac{1}{2} \frac{1}{2} \frac{1}$$

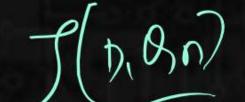
R(1) + 2I(0)





Q.4

Design a D FF by using JK FF?





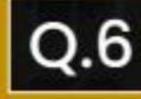
P	Bn	Bnt	(f)	K
0	0	6	310	
0		0		
	0			

HW



Q.5 Design a JK FF by using SR FF?



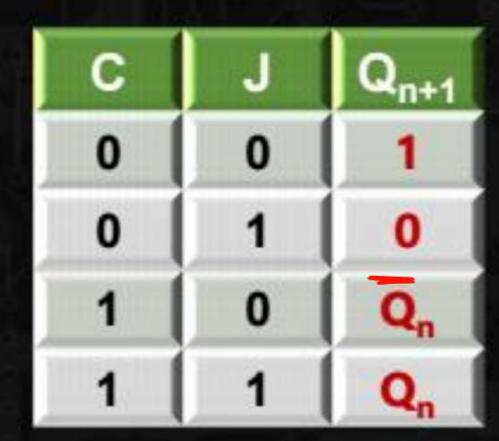


Q.6 Design a JK FF by using SR FF?

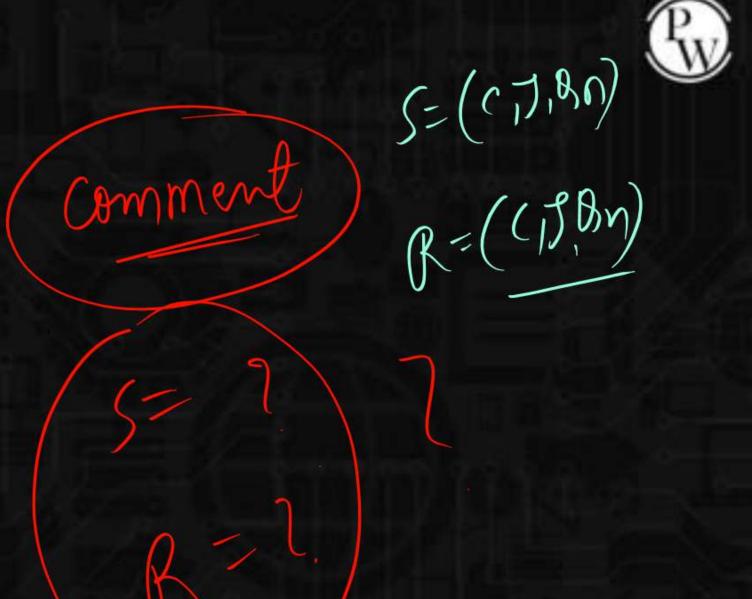


Q.7

Design a CJ by using SR FF?



(7-> chandan tha



Step (1)

С	J	Q _n	Q _{n+1}	S	R
0	0	0			
0	0	1			
0	1	0			
0	1	1	999		
1	0	0			
1	0	1			
1	1	0			
1	1	1			



С	J	Q _{n+1}
0	0	1
0	1	0
1 [0	Qn
1	1	Qn









