

Computer Science & IT Engineering

OS & Algo

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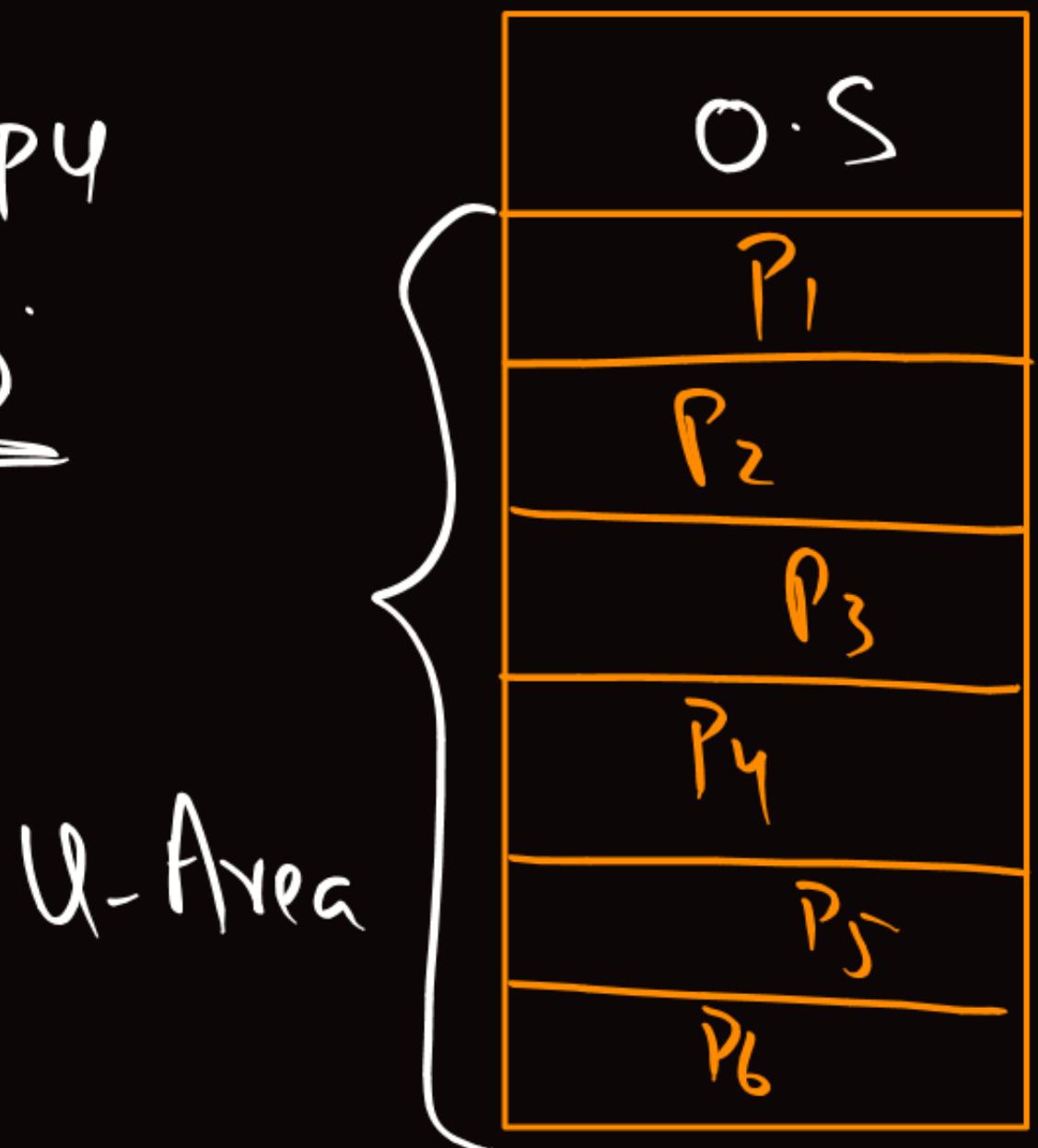
Important Terms & Formulas

Operating Systems

1. Introduction & Background

① U.R_r vs M.R_r

Show CPU
utiliz.

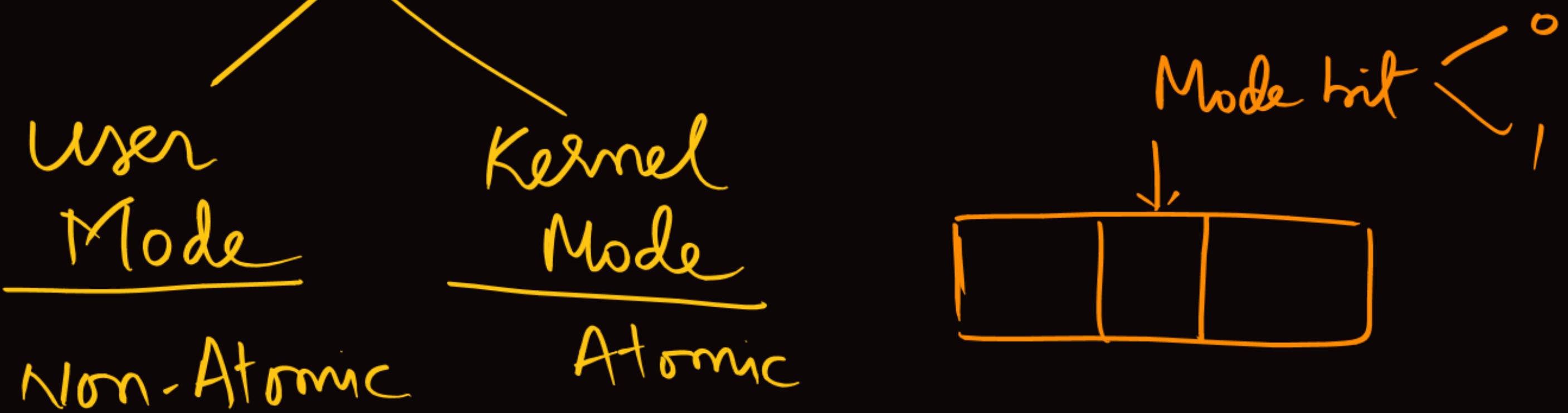


Memory (Max. CPU utiliz.)

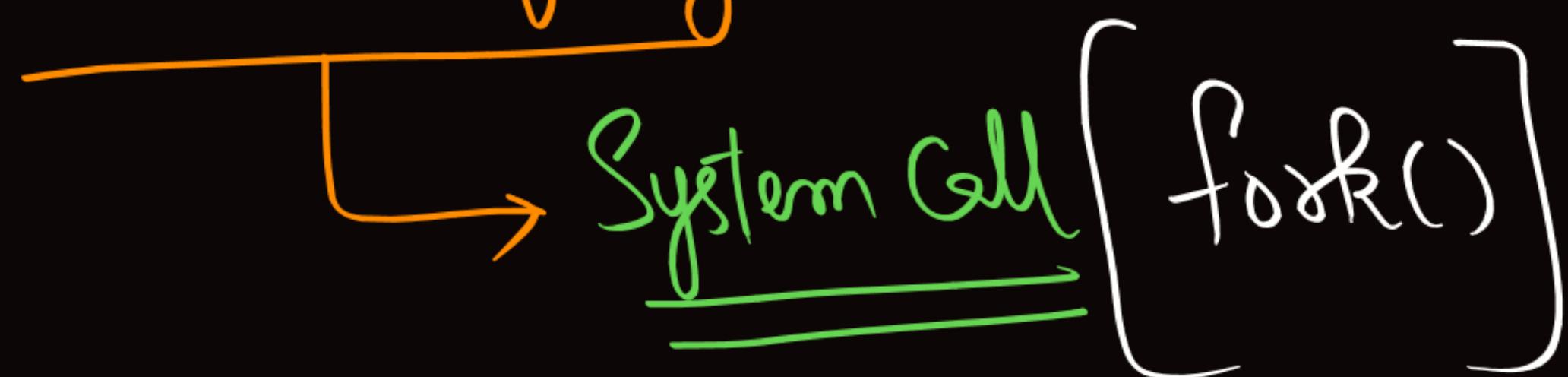
Throughput

$$\text{Throughput} = \frac{\text{No. of Programs/Processes}}{\text{Unit Time}}$$

\Rightarrow Dual Mode Support from CPU



Mode Shifting:



fork(): create a child/New Process

① n-fork() in Series :

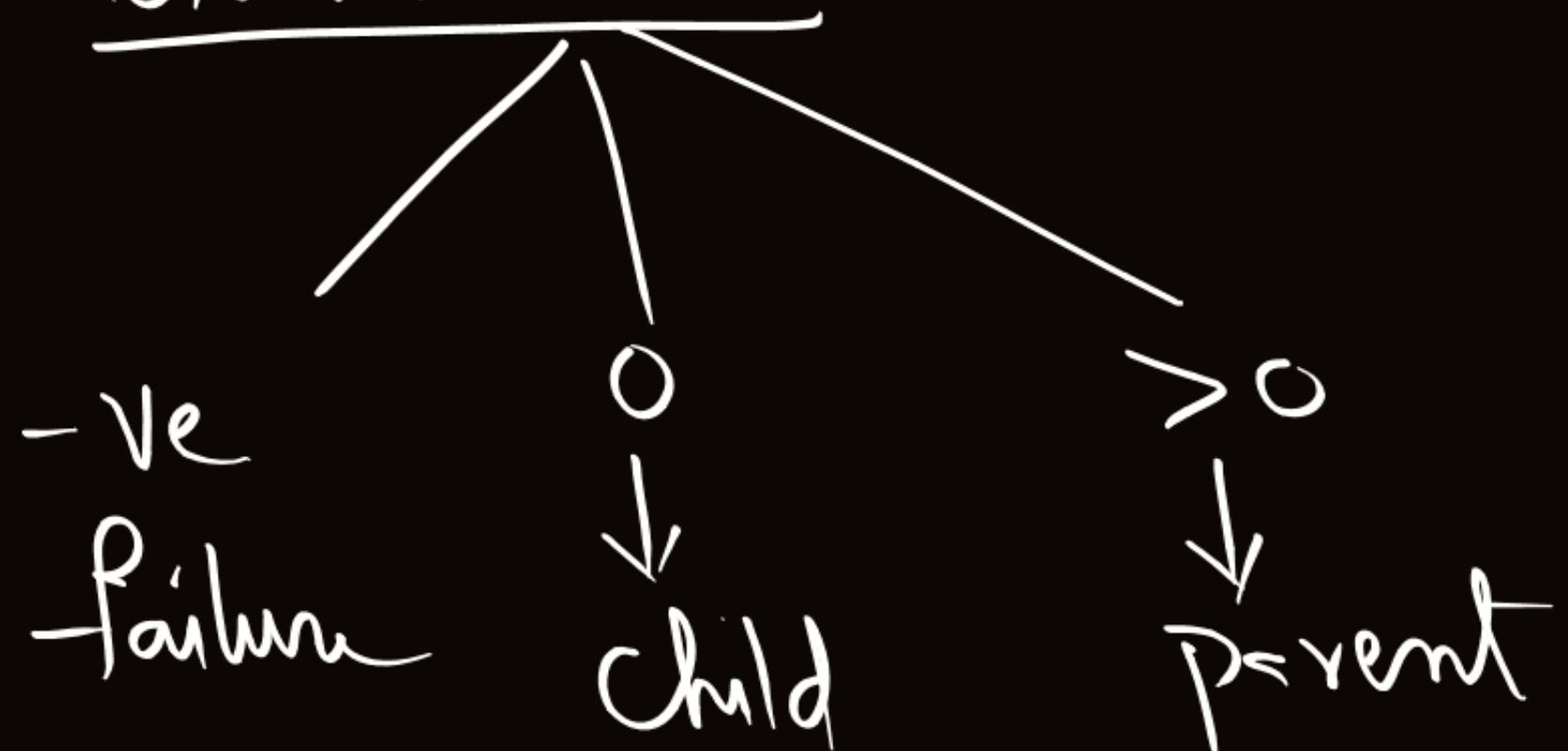
Total Processes

$$2^n$$

New Child

$$(2^n - 1)$$

② Return-value:



(ii) exec : execute / Run an application
in the Process

(iii) getpid()

(iv) wait()

→ All user defined
fns

→ All Library fns

→ O.S Routines
Calls

Process Management

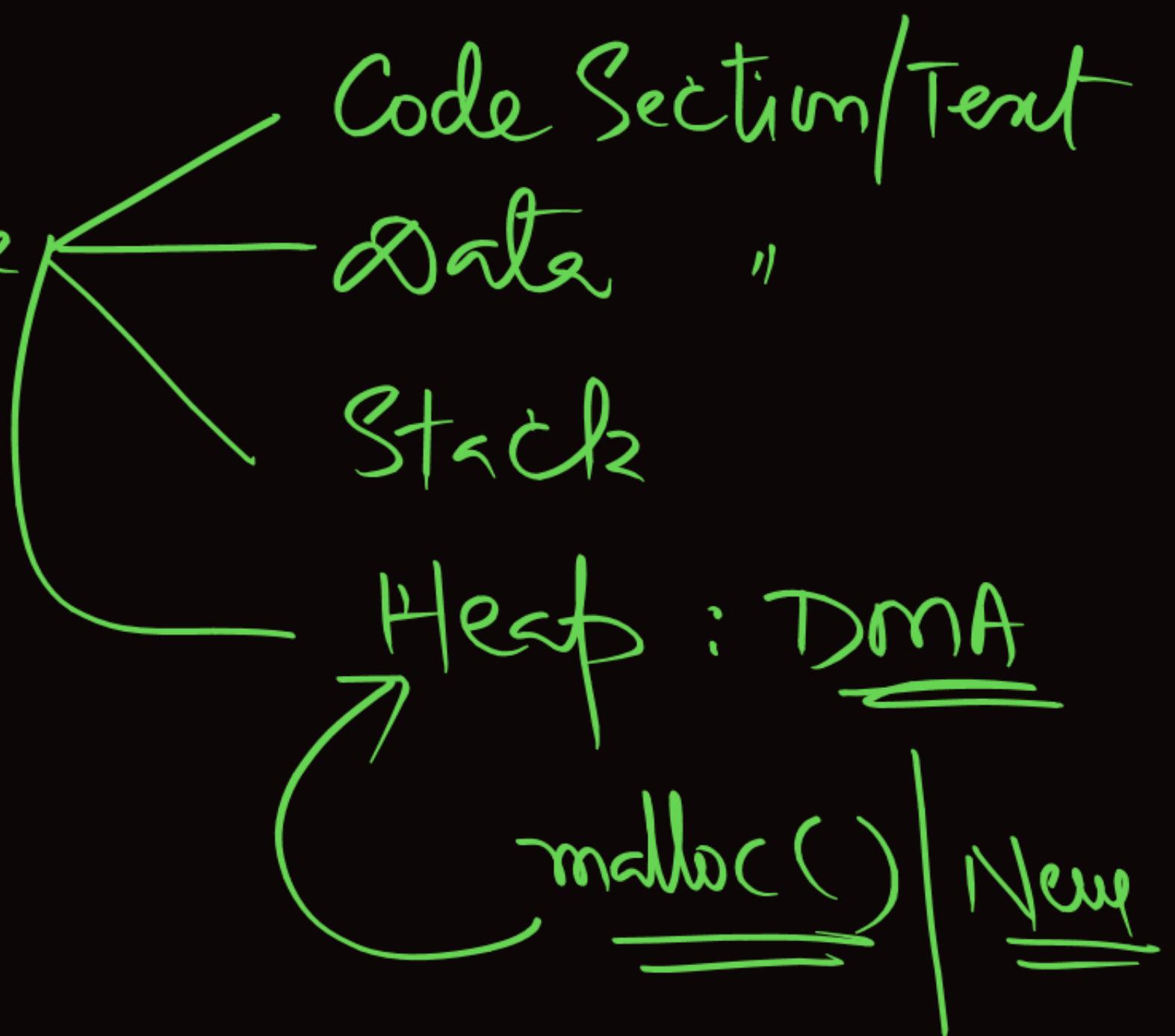
(i) Process Concepts

(ii) Process Structure

as an A.D.T

Operations:

Attributes:
State

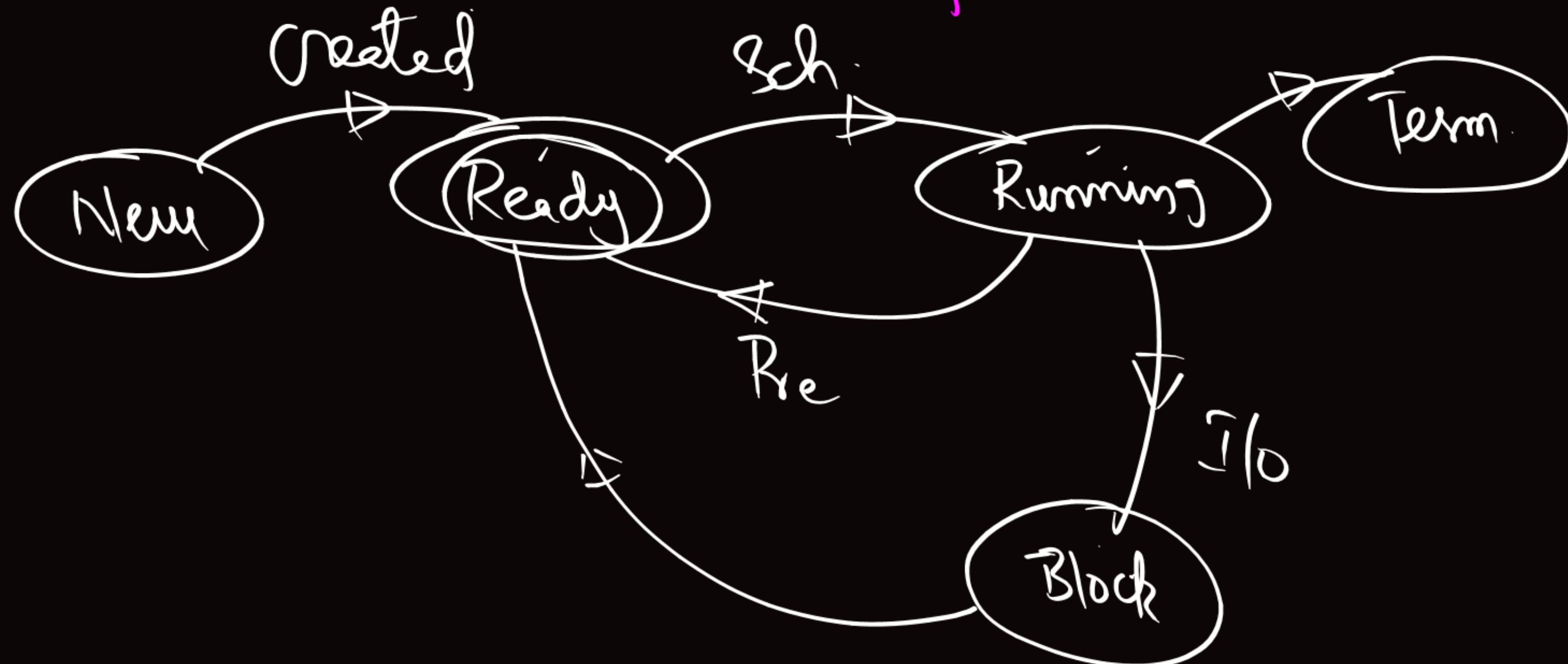


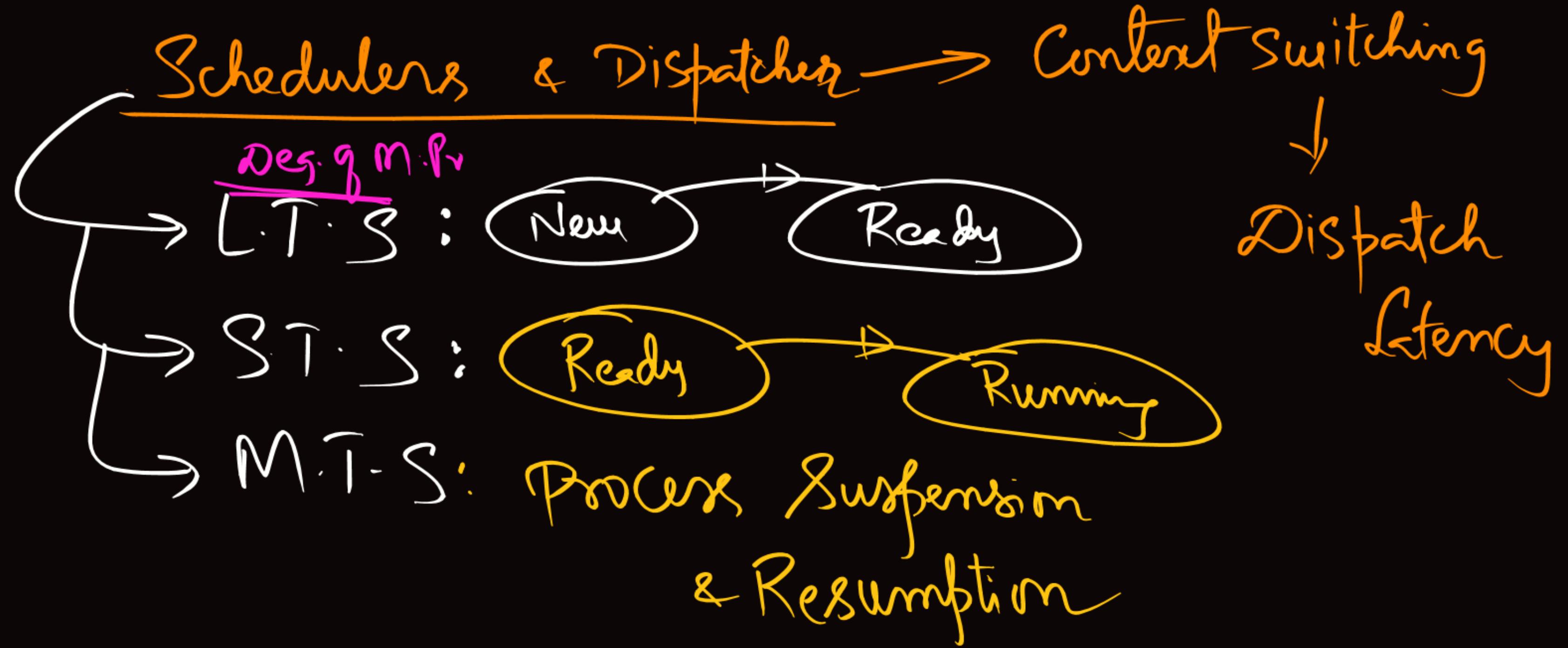
P.C.B
Process Descriptor } Process Content

States & Transition Diagram

< New, Ready, Running; Block; Terminate >

Pre Emptive





CPU Scheduling :

(i) Process Times:

a) Arrival Time (AT):

b) Burst Time (CPU BT):

c) IO Burst Time (IOTB)

d) Completion Time (CT)

e) Waiting Time (WT): $\bar{TA} - (\bar{BT} + \bar{IOTB})$ If $\bar{IOTB} = 0$

f) Turn Around Time (TAT): $CT - AT$

For a given Set of n-processes

$$\text{Schedule Length (L)} = \text{Max}(CT_i) - \text{Min}(AT_i)$$

$$\text{Throughput} = \frac{n}{L}$$

$$n - L \text{ units}$$
$$? - 1,$$

$$WT = TAT - BT$$

Response Time:



: The difference of
the time of
arrival to the
time @ which the
process gets onto CPU
for first time,
(Non-Preemptive + NO I/OB)

of Process Request



→ The time of Req. admission
to the time @ which it
generates its first result

- (D) FCFS
- (i) SJF
- (ii) SRTF
- (iv) HRRN
- (v) Round Robin
- (vi) Priority
- (vii) R.R + Priority
- (viii) Multi-level Q-Scheduling

Threads (MSOs)

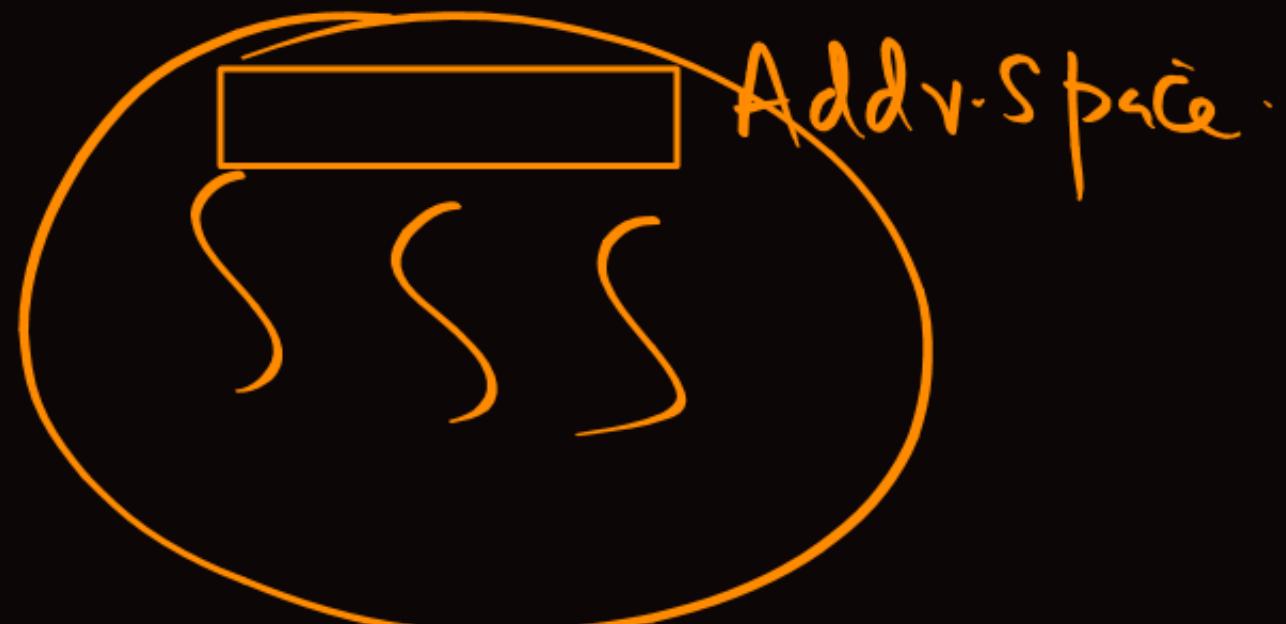
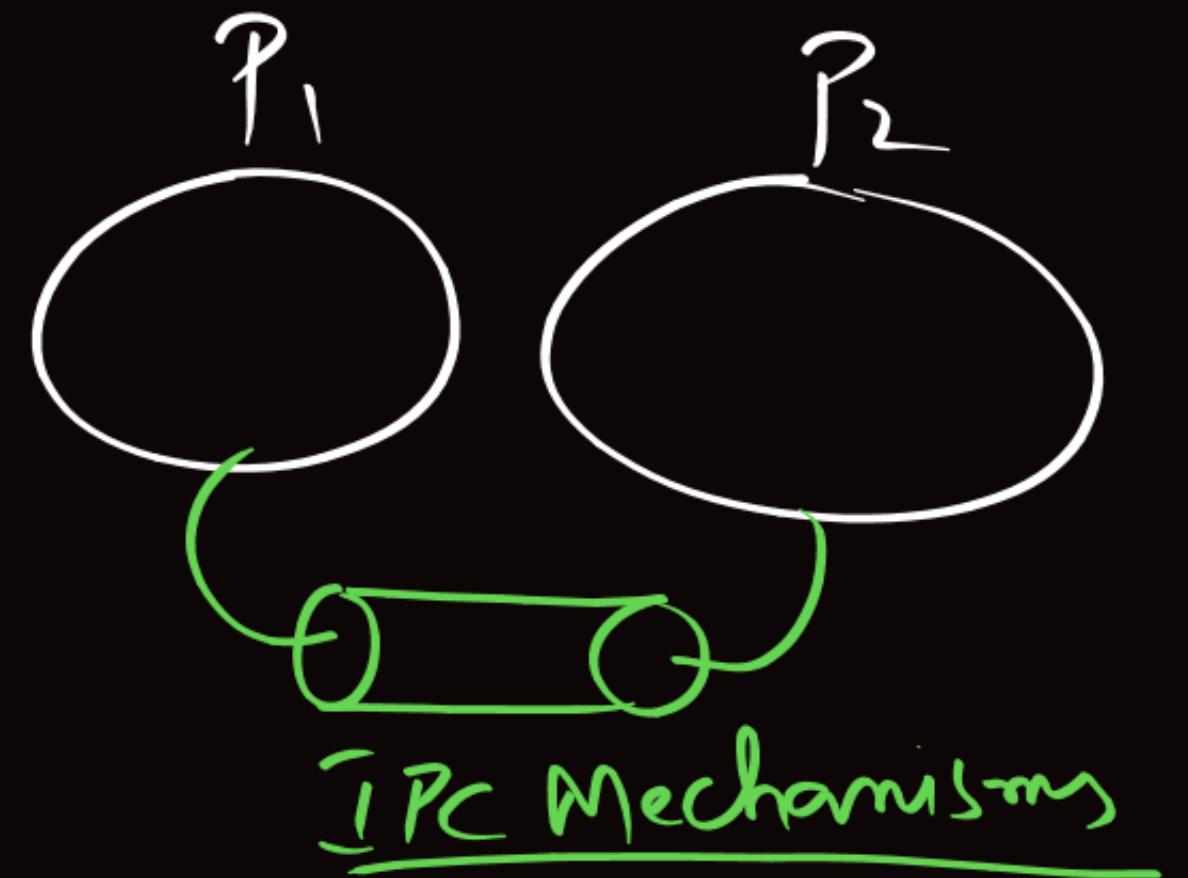
→ Process vs Threads

→ Benefits of Threads

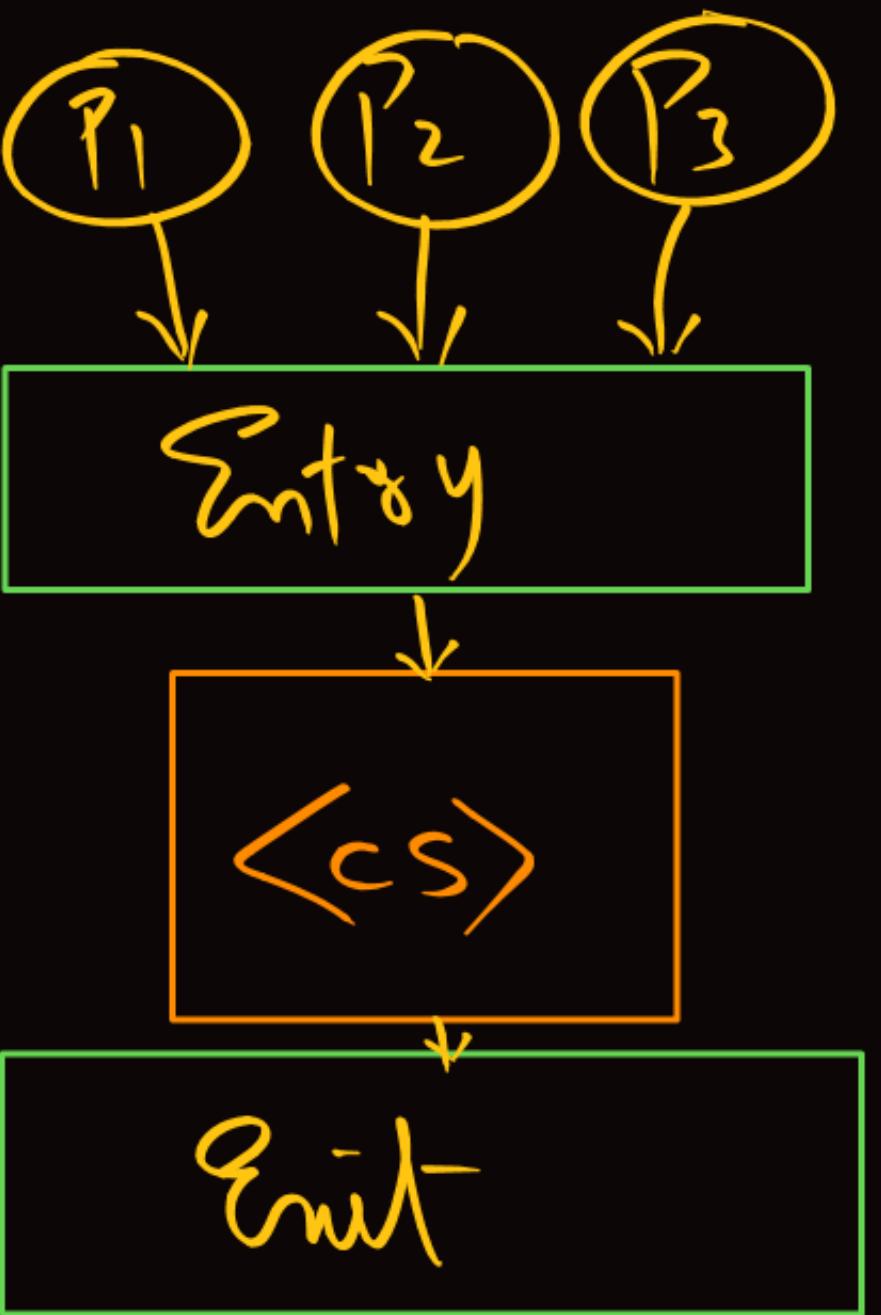
→ Types of Threads

U-LTs

K-LTs



Process Synchronization/Coordination



↳ Critical Sec
↳ Race Conditions
↳ Preemption

- (i) Mutual Exclusion
- (ii) Progress
- (iii) Bounded Wait
 - (Starvation) ↳ Fair-Shre

Monitor

Priority-Inversion

↳ Livelock/Deadlock

Priority-Inheritance

→ OS based

SEMAPHORES

BINARY

$\langle 0|1 \rangle$

COUNTING

$\langle -\infty \rightarrow +\infty \rangle$

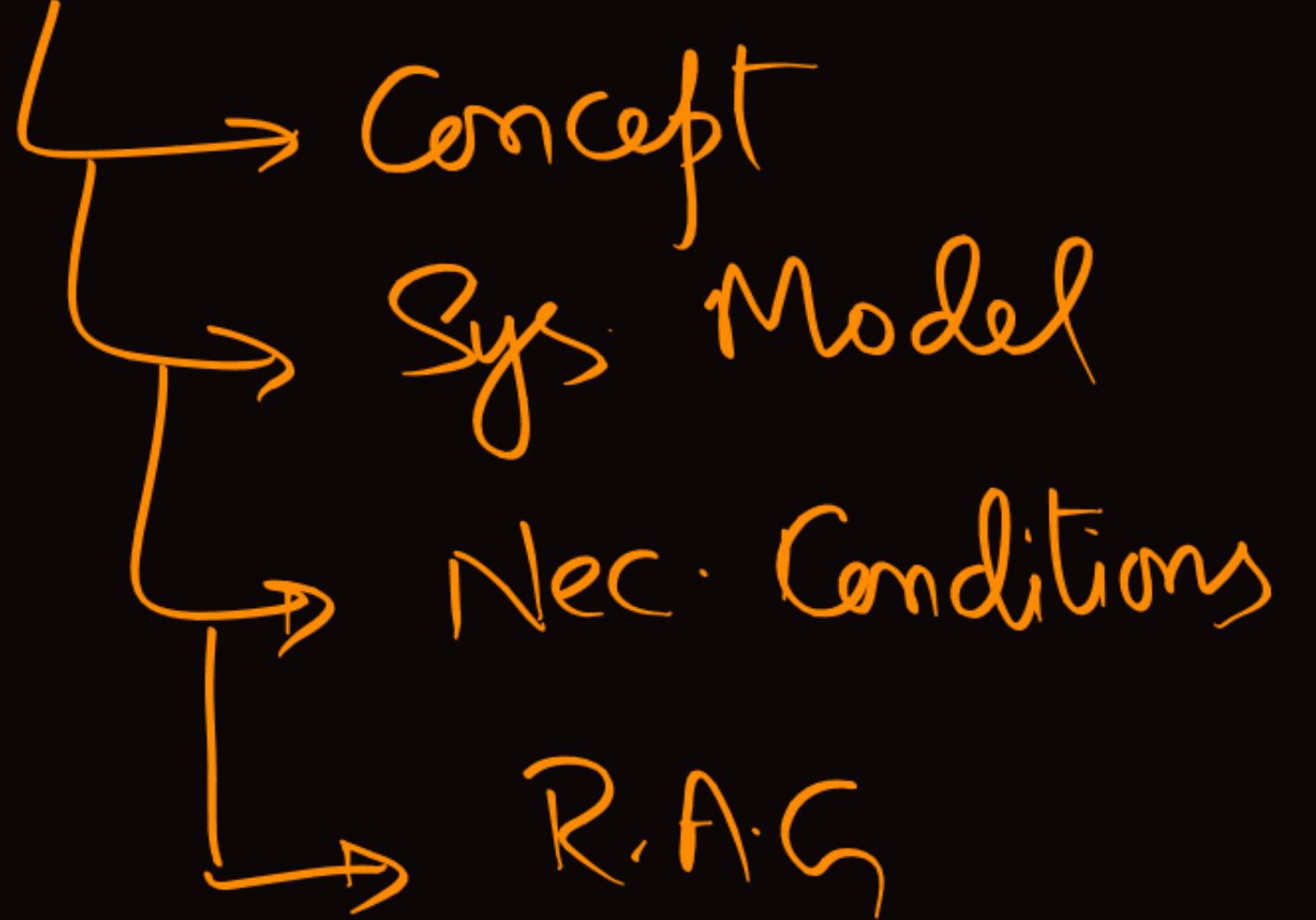
P | DOWN

V | UP

Concurrency Mechanisms



Deadlocks:



Strategies

Proactive

Prevention:

Avoidance
(Banker's)

Safe

unsafe

Reactive

Detection & Recovery

Ignorance
(Ostrich)

III. Memory Subsystem :

n = Addr. bits

N = No. of words

m = word length in
bits

$$N = 2^n \text{ words } (m\text{-bit})$$

$$n = \lceil \log_2 N \rceil \text{ bits}$$

$$N = 16 \text{ GB}$$

$$n = 30 + 4 = 34 \text{ bits}$$

$$N = \log_2 \times \text{ Bytes}$$

$$n = \log \log \times \text{ bits}$$

M. Mgmt. Techniques

CG

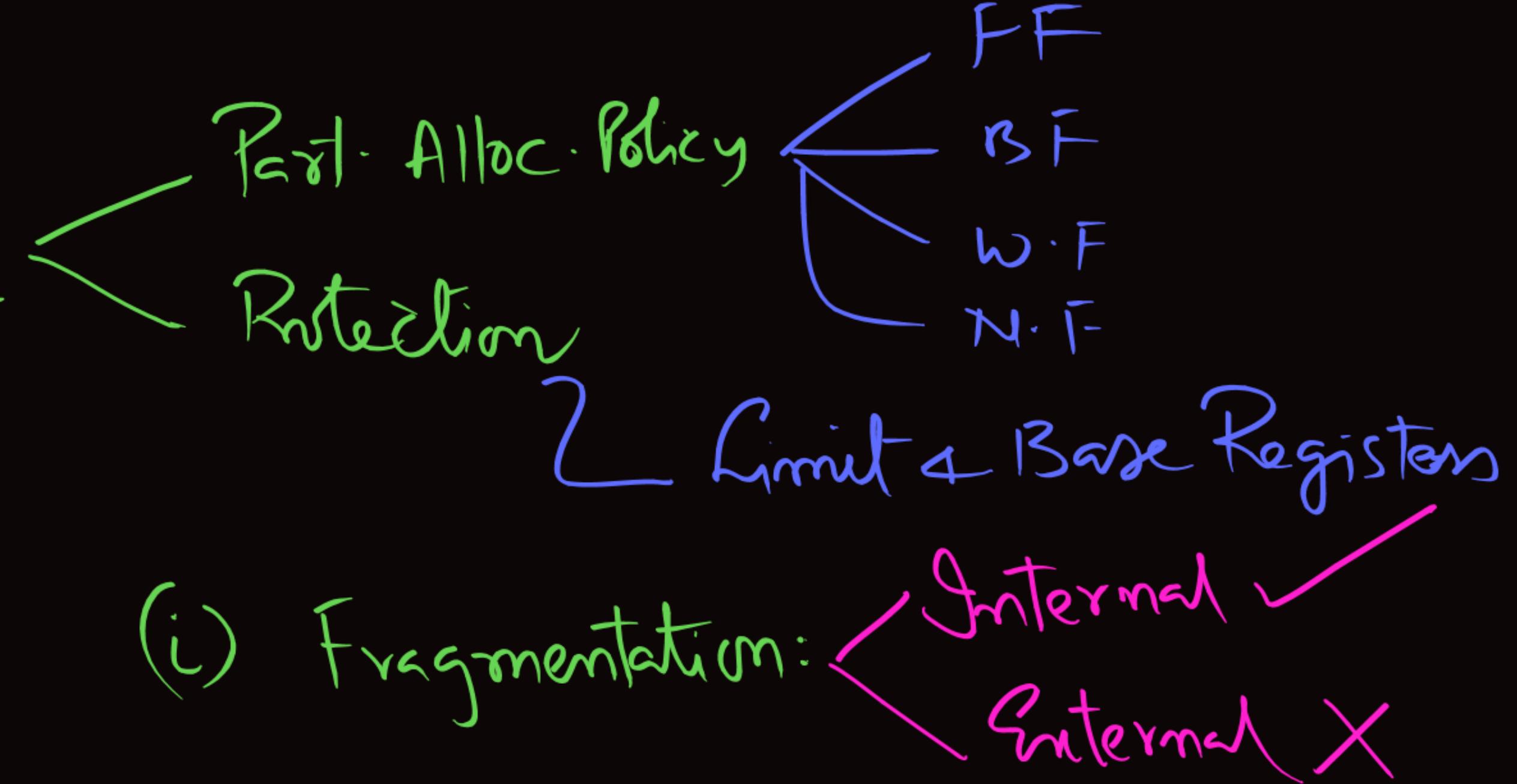
Non - CG

→ Partitions

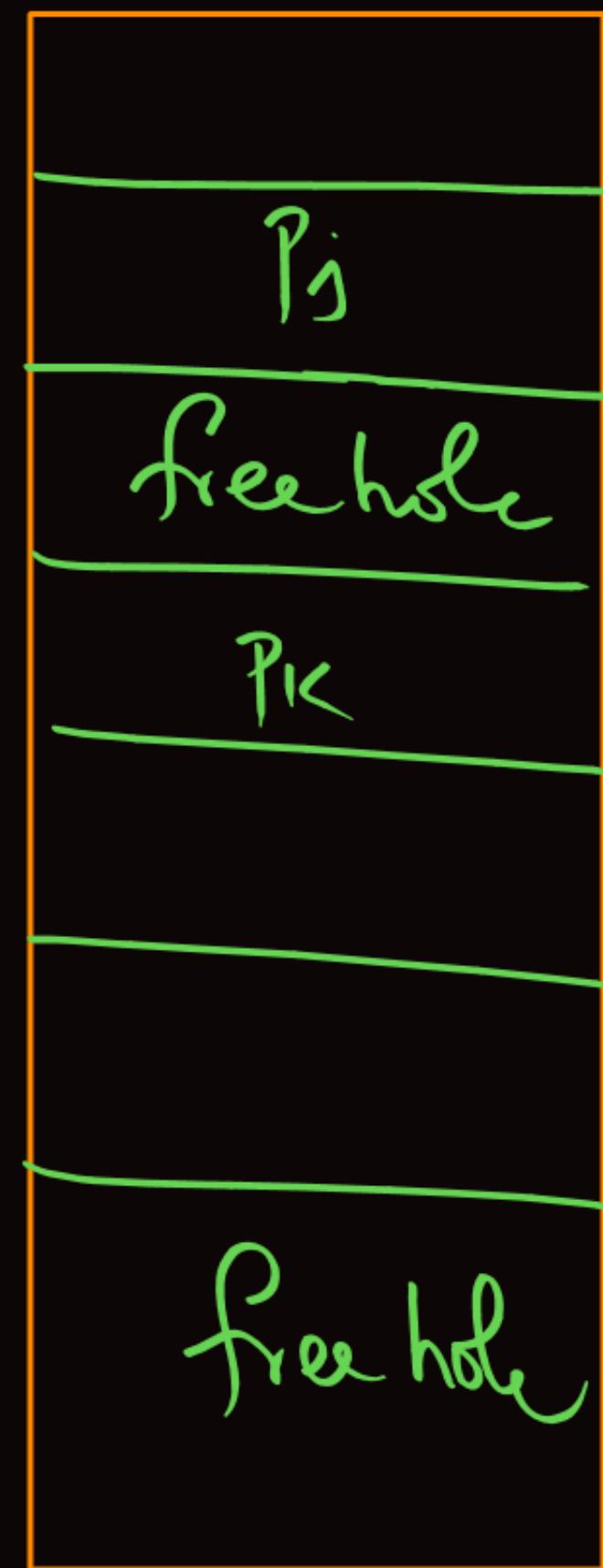
→ overlays

fixed (mFT)

variable (mVi)



Variable Positions:



→ No Int-Frag.

→ Ent-Frag:

Compaction

N.CG

Paging:

$$\frac{VA}{LA} : \boxed{P \quad d}$$

$$N_{\text{Pages}} = \frac{VA \cdot S}{PS}$$

$$PT\text{-Size} = N * e \text{ Bytes}$$

$$N = 2^P \iff P = \log_2 N_{\text{Pages}} \text{ bits}$$

$$PS = 2^d \quad [d = \text{Page offset}]$$

$$d = \log_2 PS$$

$$N = \text{No. of Pages}$$

$$e = PT\text{-E Size in Bytes}$$

Paging with TLB

$$\text{EMAT}_{\text{BP+TLB}} = x(c+m) + (-x)(c+2^m)$$

Virtual Memory (VM)



Demand Paging (PP) Pure Prefetch

$$EMAT = \frac{(1-P)m + P * \delta}{DP}$$

Gauvin

⇒ Page replacement LRU
Optimal
FIFO/LIFO

⇒ Thrashing

⇒ Working Set Model

File System & IO Mgmt

→ Physical Disk Structure

→ Logical " "



DBA :

DBS :



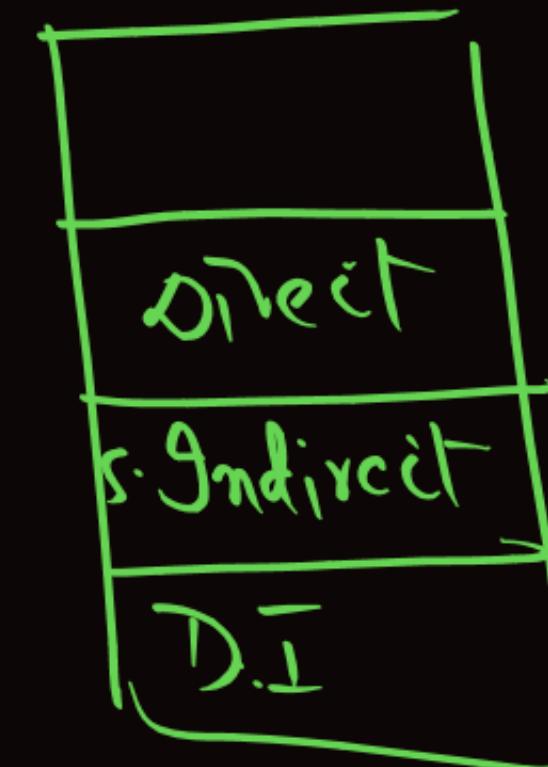
Max Possible : Max Possible
File Size Disk Size

(i) Alloc. Methods

→ C.S :
→ linked :
→ Index :

F.A.T

→ I-Node Structure*



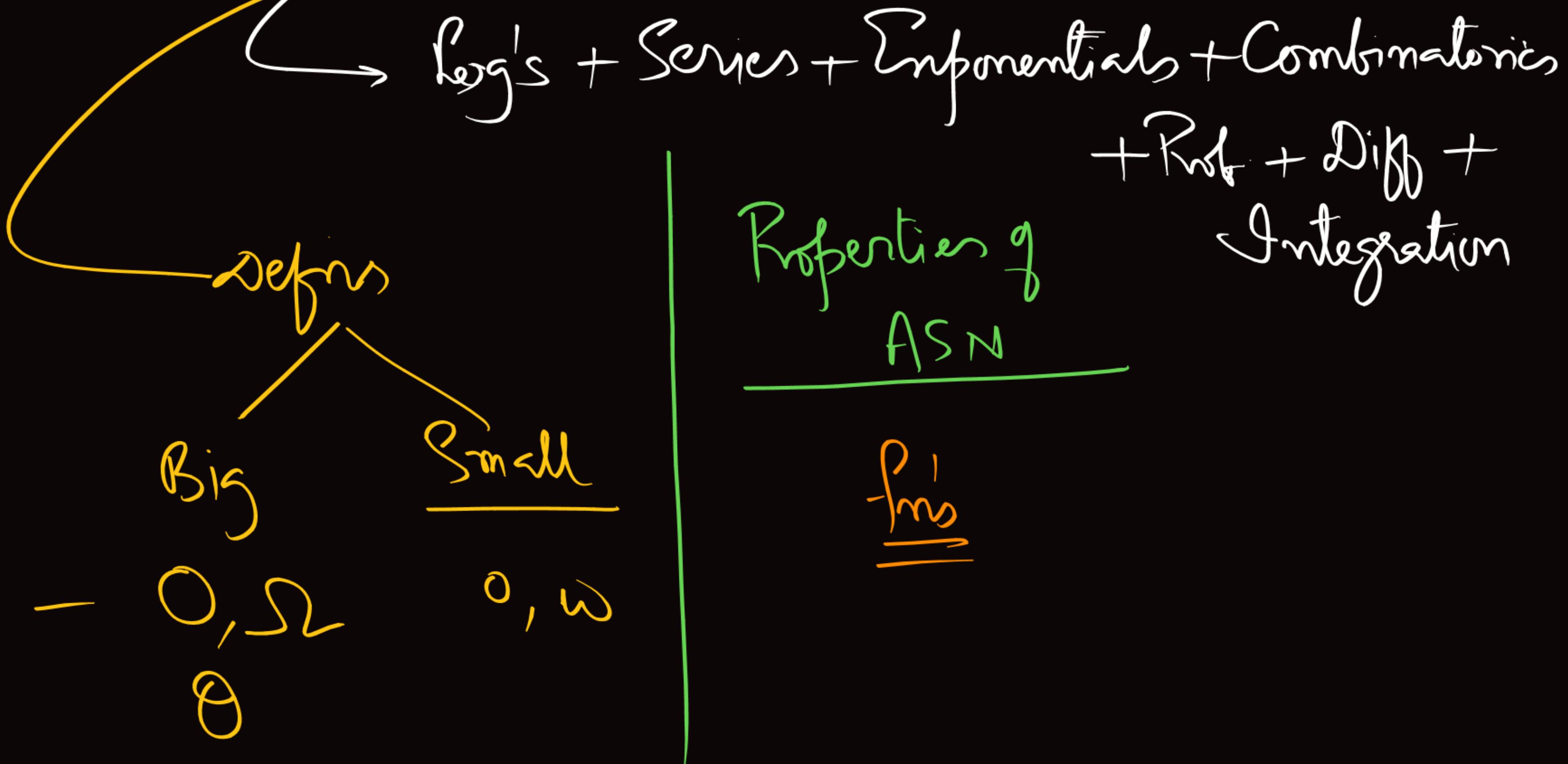
Disk Scheduling

(i) Seek Time

< FCFS; S.S.T.F; SCAN; LOOK
CSCAN; CLOOK >

ALGORITHMS

1. A Symptotic Notations [DS + ASN]



II. Divide & Conquer :

- (i) MaxMin
- *
 - (ii) MergeSort
 - (iii) Quicksort
- (iv) Matrix Multip.
- (v) Binary Search
- (vi) Power of n
- (vii) L.I.M

Recurrence Equations

$$\frac{3n}{2} - 2$$

(i) Merge Min : $T(n) = 2T(n/2) + 2 \Rightarrow O(n)$

(ii) Binary Search : $T(n) = T(n/2) + c \Rightarrow O(\log n)$

(iii) Merge Sort : $T(n) = 2T(n/2) + n \Rightarrow O(n \log n)$

(iv) Quick Sort : $\begin{cases} \text{W.C.} : T(n) = T(n-1) + n \Rightarrow O(n^2) \\ \text{B.C.} : T(n) = 2T(n/2) + n \Rightarrow O(n \log n) \end{cases}$

(v) Mat. Mult. : $T(n) = 8T(n/2) + n^2 : O(n^3)$
 $= 9T(n/2) + n^2 : O(n^{2.81})$

Master Method

$$T(n) = a \cdot T\left(\frac{n}{b}\right) + f(n)$$

Case I :

Case II :

Case III :

L.T.M :

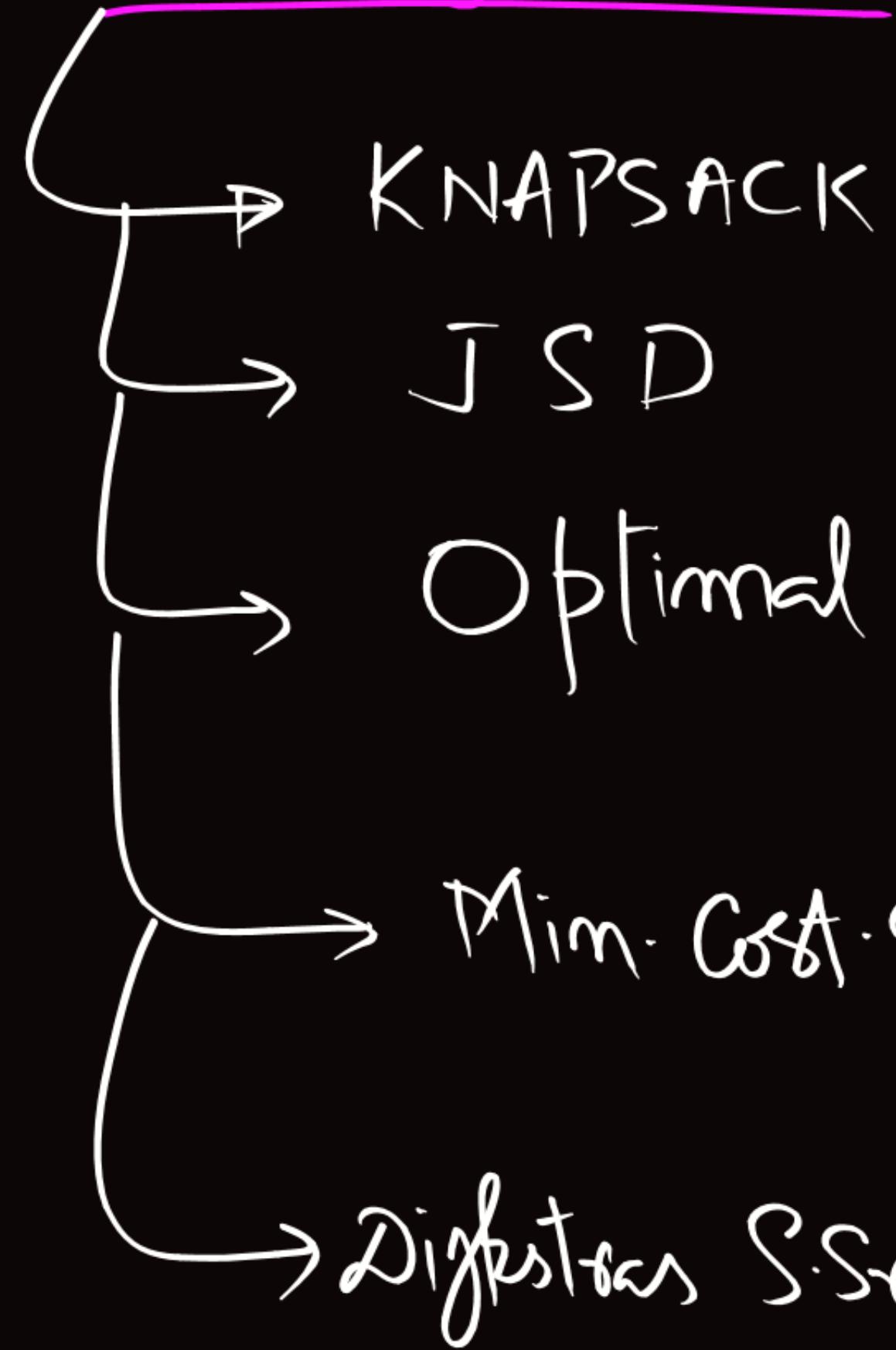
1) DC : $T(n) = 4T\left(\frac{n}{2}\right) + n$

2) AK : $T(n) = 3T\left(\frac{n}{2}\right) + n$

3) TC : $T(n) = 5 \cdot T\left(\frac{n}{3}\right) + n$



Greedy Method



Min. Cost SP Trees:

- Prim's : $O(n^2)$
- Kruskal's : $O(e \log e)$

Huffman Code

Dynamic Programming:

→ Opt. Substructure Property
→ overlapping

→ Fib

→ M. S. Graphs

→ T. S. Person

→ * Longest Common Subsequence

→ * Matrix - Chain Product

→ * Sum of Subsets

→ Opt. Binary Search Tree

(i) Soln (D. P. based Recurrence)

(ii) Numerical

(iii) Time & Space

Graph Techniques



→ B·F·S + D·FS

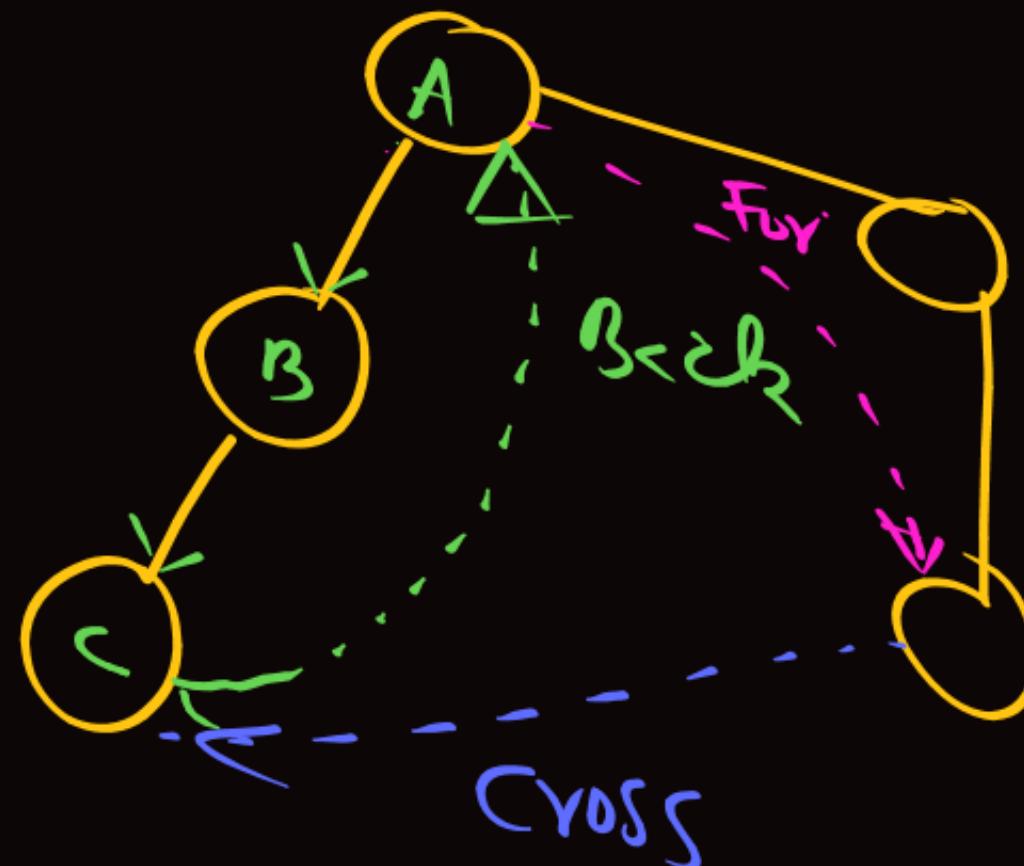
→ Applications

→ Components

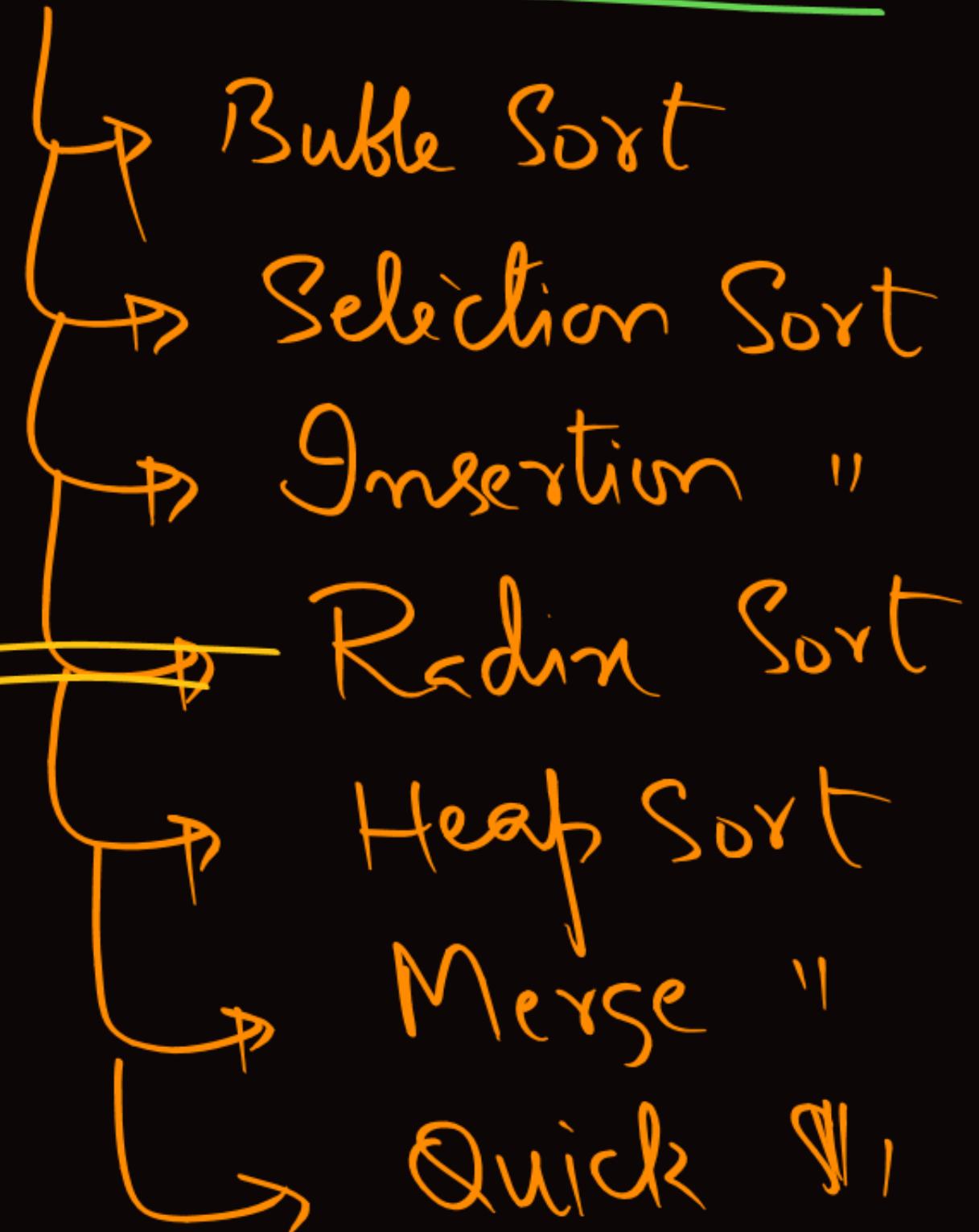
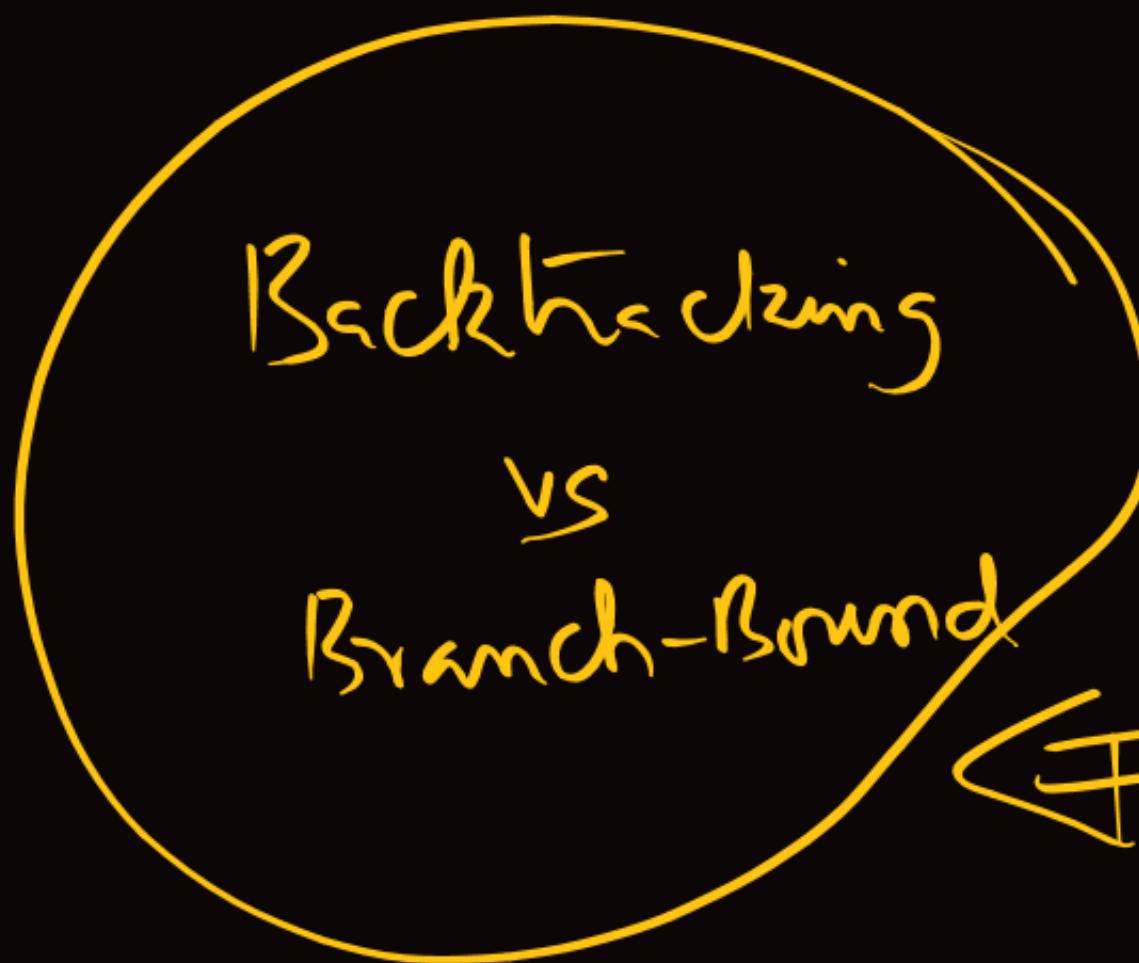
→ Topological Sort *

→ Heap Algo's:

↳ Heap Sort



SORTING METHODS



1) Best Case
Worst Case } Time

2) Space Complexity

3) Stable or not

4) Inplace or
outg Place