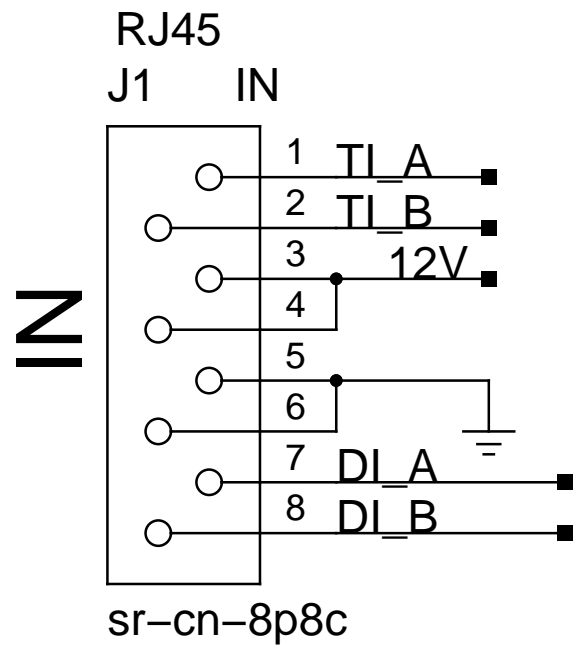


SR LVDS interconnect

TITLE				REVISION:
FILE:	interconnect.sch			DRAWN BY: rbarlow
PAGE	1	OF	1	

PIC powered through diode D1 to allow it to be programmed with the board powered down.

D2 allows Vpp to raise to 12V+ without damaging U3



TI_A

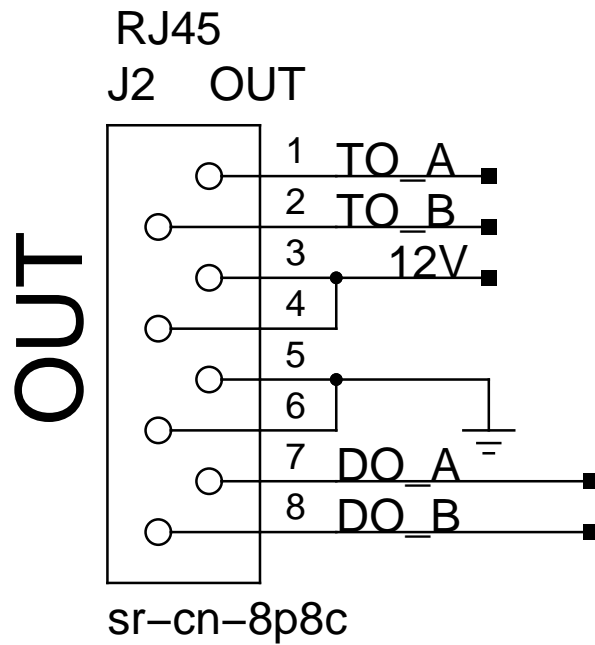
TI_B

TO_A

TO_B

DI_A

DI_B



DO_A

DO_B

12V

GND