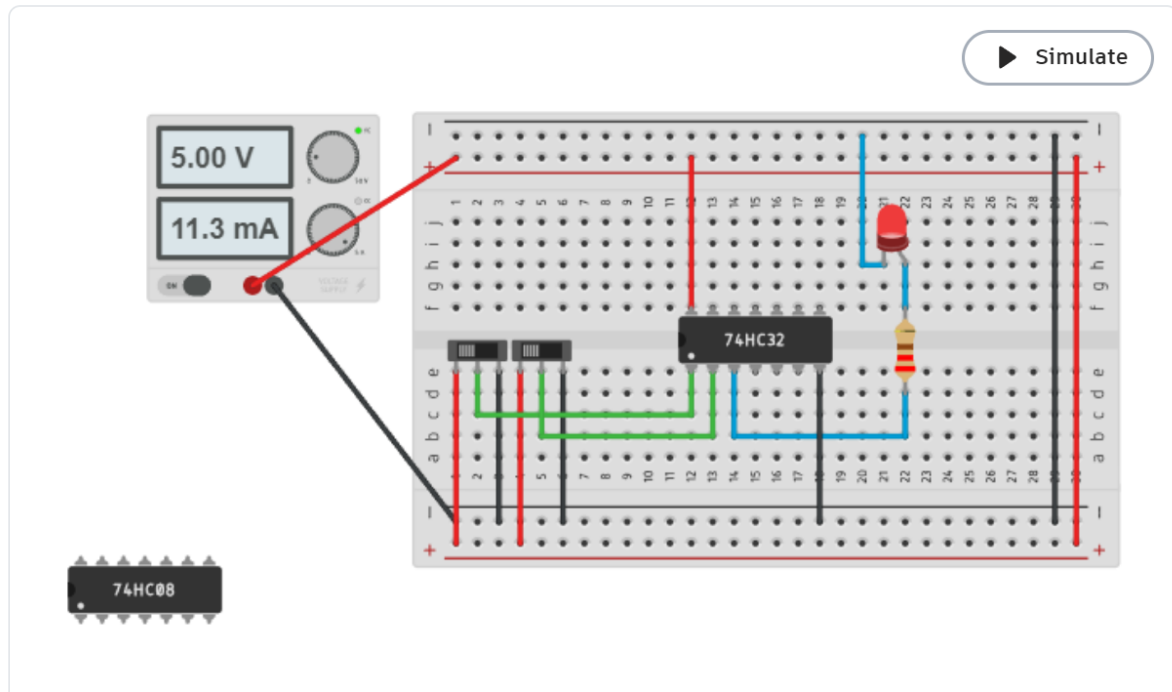


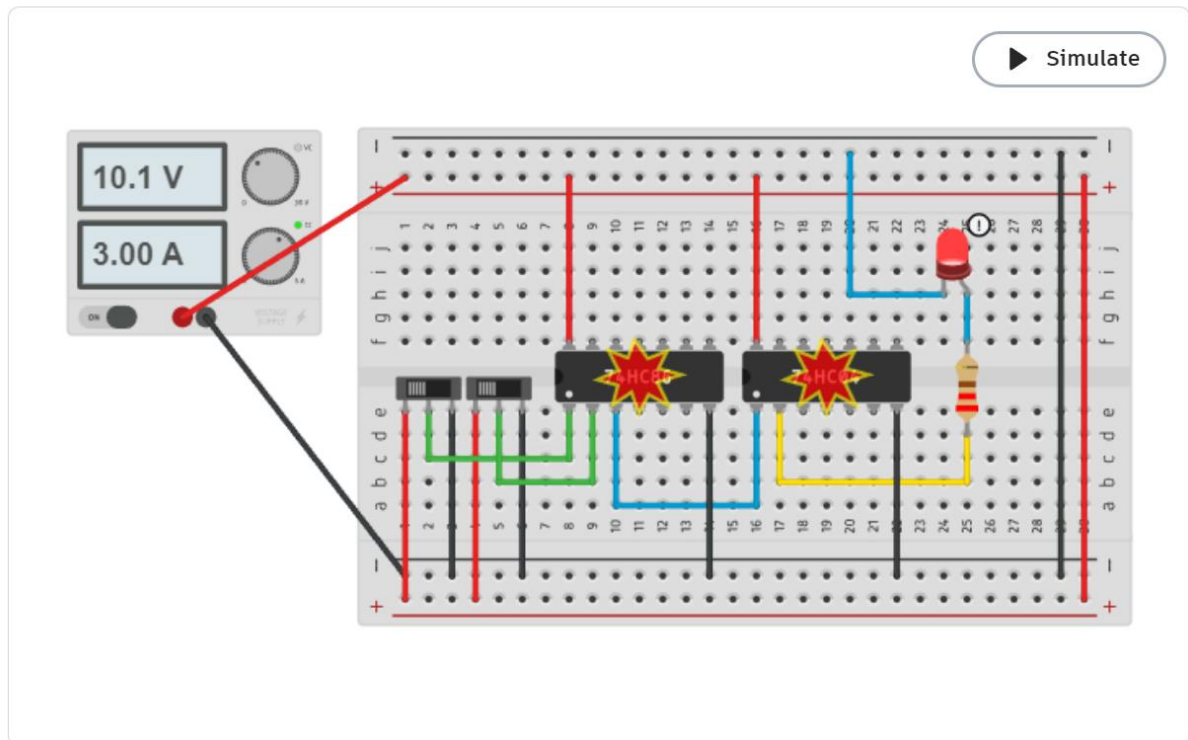
<https://www.tinkercad.com/things/87mITDRMQog?sharecode=6Om2BledtRy7oLpddaoOUWLRjzeV13QJMN-0Wsz2mOw>

## OR Gate by Advyth



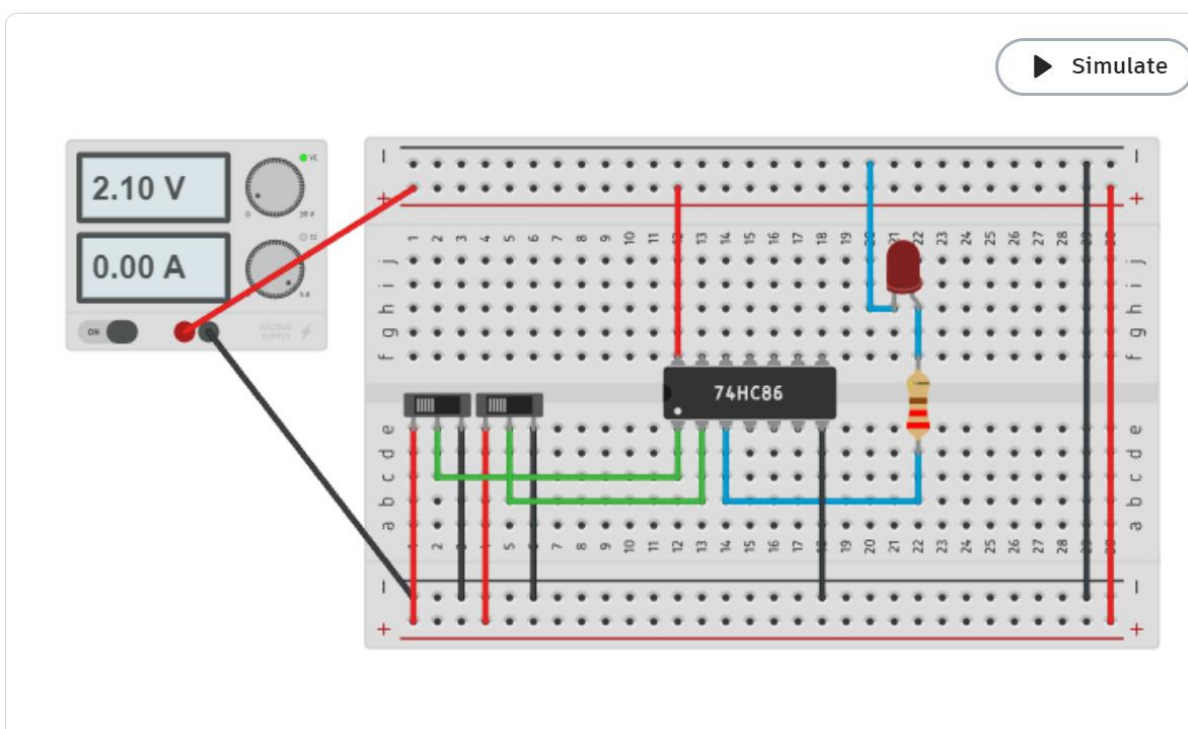
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## XNOR Gate by Advyth



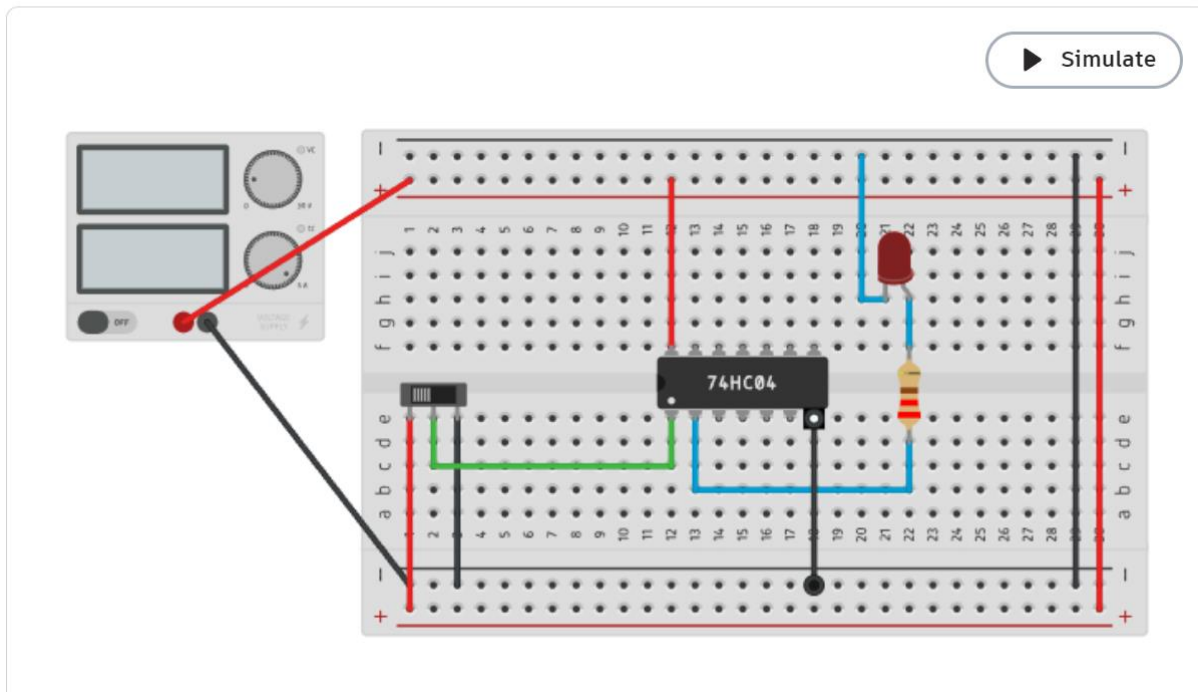
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## XOR Gate by Advyth



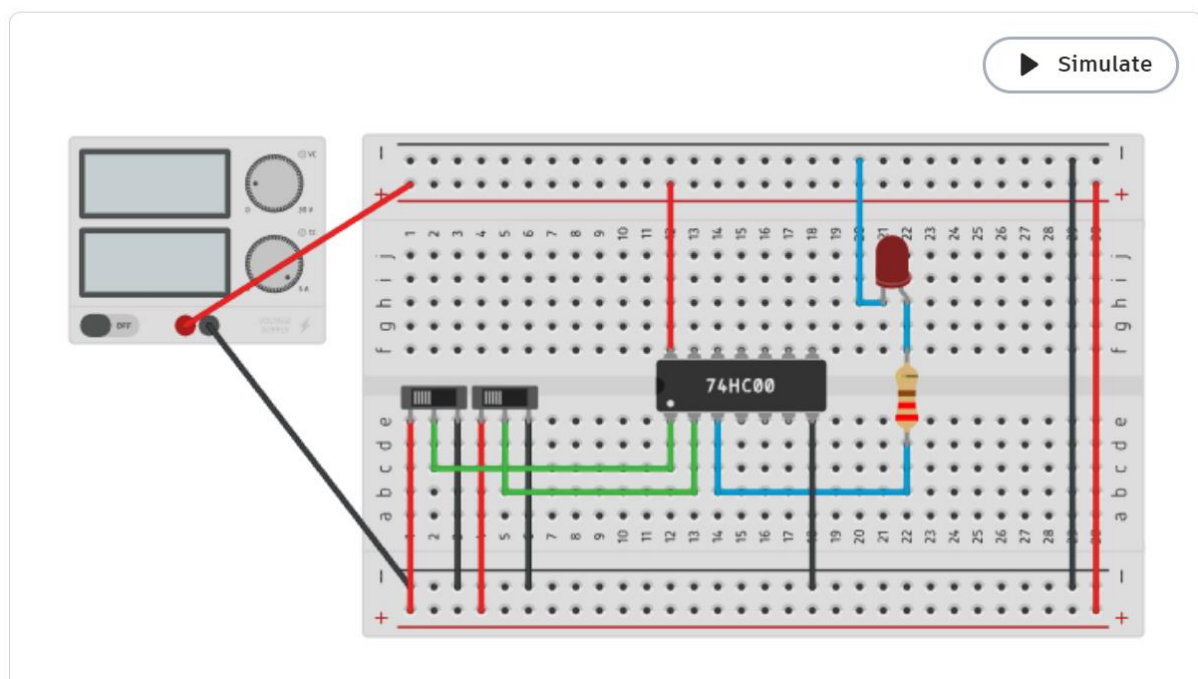
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## NOT Gate by Advyth



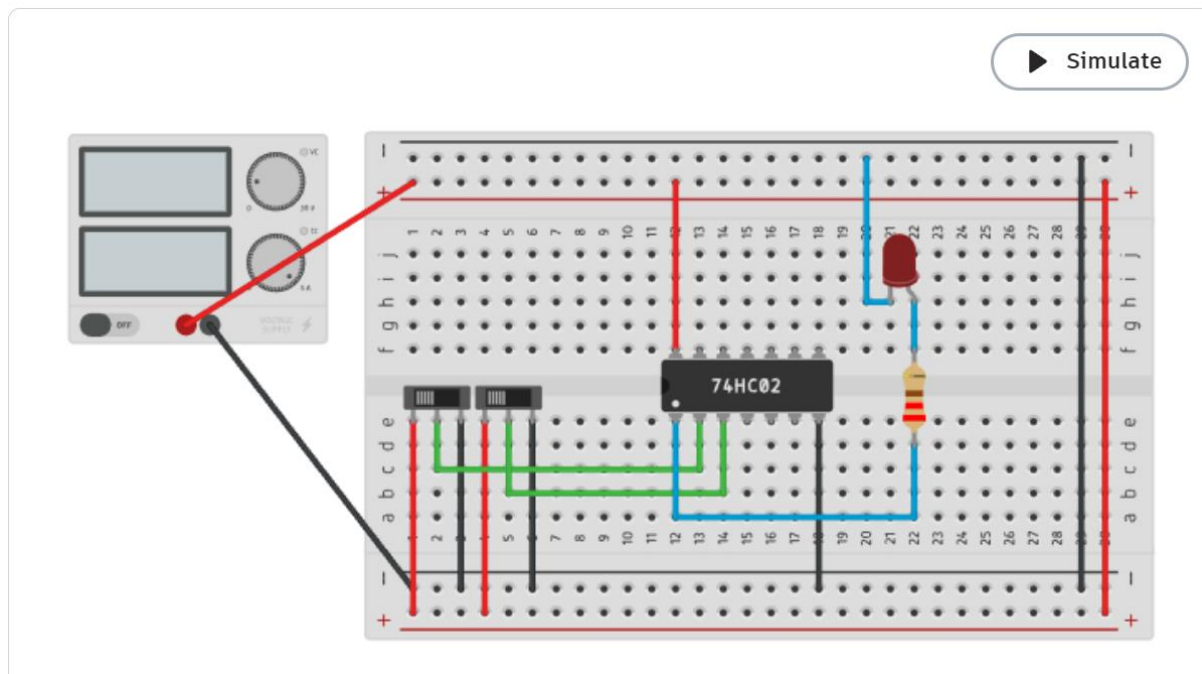
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## NAND Gate by Advyth



[https://www.tinkercad.com/things/2A6xoVRCdxk?sharecode=-ujAPqZCorjsoKH89c4gyW5ud5Vixhg-HPBk\\_oGP7MU](https://www.tinkercad.com/things/2A6xoVRCdxk?sharecode=-ujAPqZCorjsoKH89c4gyW5ud5Vixhg-HPBk_oGP7MU)

## NOR Gate by Advyth



<https://www.tinkercad.com/things/jW7dUyUFp56?sharecode=TMIC4rNjP9YMrrE2Tr4TZIj72v8VwaWC8BI5nilCY20>

## AND Gate by Advyth

