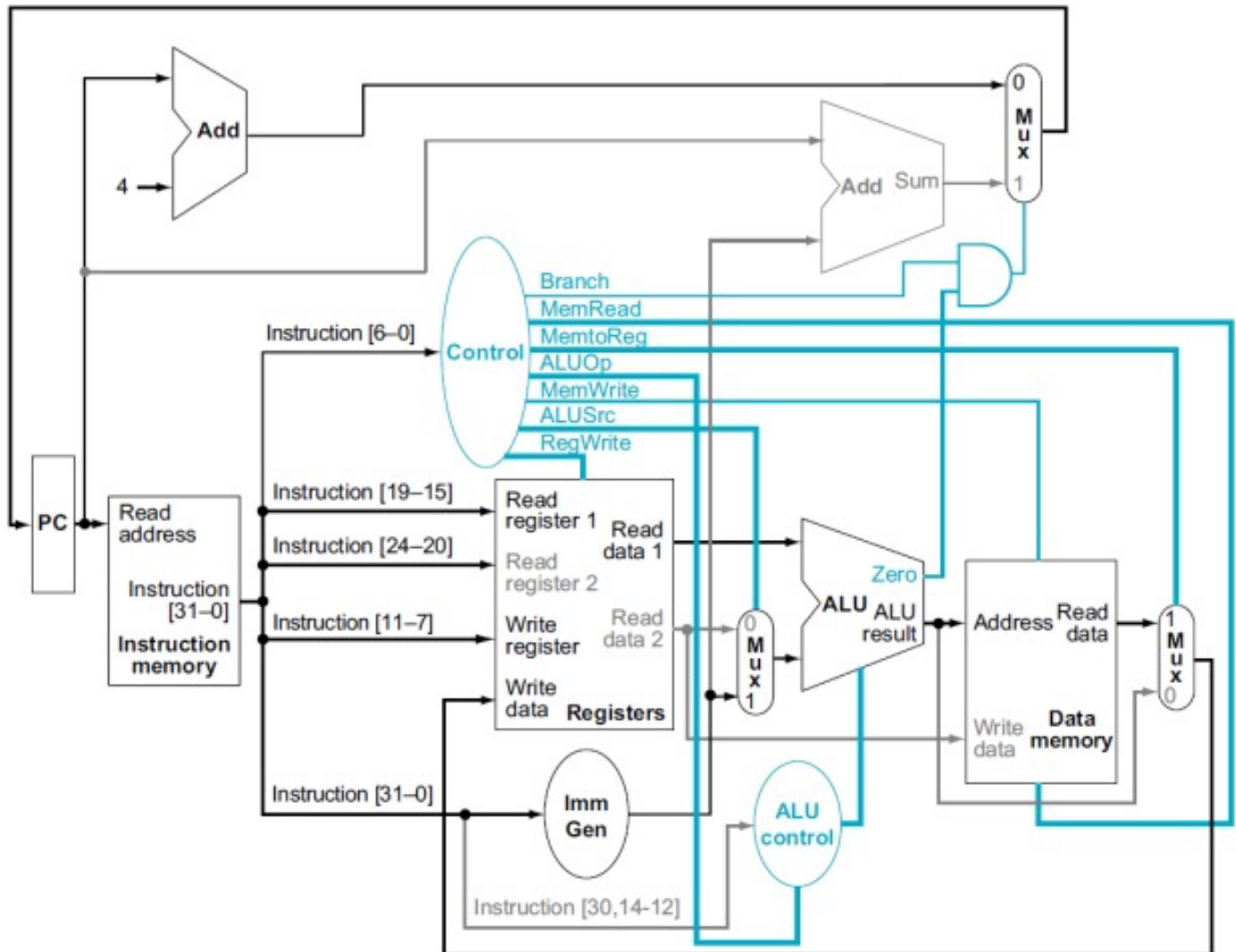Rohit Shidid

RRS6770

CSA Phase 1 Submission

1) **Draw the schematic for a single stage processor and fill in your code in the to run the simulator**

Testcase 0

```
PerformanceMetrics.txt                                    Open with TextEdit
Performance of Single Stage:
#Cycles -> 7
#Instructions -> 6
CPI -> 1.1666666666666667
IPC -> 0.8571428571428571
```

2)

Testcase 1

PerformanceMetrics.txt

Open with TextEdit

```
Performance of Single Stage:
#Cycles -> 40
#Instructions -> 39
CPI -> 1.0256410256410258
IPC -> 0.975
```

Testcase 3

```
Performance of Single Stage:
#Cycles -> 36
#Instructions -> 35
CPI -> 1.0285714285714287
IPC -> 0.9722222222222222
```