



**Department of Computer Science and Engineering**  
**Motilal Nehru National Institute of Technology Allahabad, Prayagraj**

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**Microprocessor Lab (CSN14400)**

**(B.Tech. IV Semester)**

**Assignment - 07**

Q1) ALP in 8085 to add six bytes of data stored in memory locations starting at 2050H and stores the sum in the Output Buffer memory in two consecutive memory locations. Using Assembler Drectives.

Data(H): A2, FA, DF, E5, 98, 8B