5G NR LDPC Encoder and Decoder

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1. Introduction

5G NR (New Radio) is the latest wireless communication standard that supports higher data rates, lower latency, and increased reliability. LDPC (Low-Density Parity-Check) is a forward error correction (FEC) technique used in 5G NR to improve the reliability of data transmission.

5G NR (New Radio) uses Low-Density Parity-Check (LDPC) codes for forward error correction (FEC) to improve the reliability of data transmission over wireless channels. LDPC codes are linear block codes that have a sparse parity-check matrix. The parity-check matrix is designed to have a low density of 1's, which means that only a small fraction of the bits in the matrix are 1's.

The 5G NR LDPC codes are designed to have a high coding gain and low error rates while maintaining low latency and complexity. The coding gain is the ratio of the signal-to-noise ratio (SNR) required for a non-coded system to achieve a certain bit error rate (BER) compared to the SNR required for a coded system to achieve the same BER. The higher the coding gain, the more robust the code is against channel noise and interference.

The 5G NR LDPC codes are specified by the 3GPP (Third Generation Partnership Project) and include several code rates ranging from 1/5 to 5/6. The code rates determine the amount of redundancy added to the original data to create the codeword. Higher code rates add more redundancy, which results in a more robust code but also increases the latency of the transmission.

The 5G NR LDPC codes have a block size of 8448 bits and are designed to be flexible and adaptable to different channel conditions. The code rate and the number of iterations used in the decoding process can be adjusted based on the channel conditions to optimize the trade-off between coding gain, latency, and complexity.

Overall, the 5G NR LDPC codes are a crucial component of the 5G NR communication system, and their performance

has a significant impact on the reliability and efficiency of data transmission over wireless channels.

2. 5G NR LDPC ENCODER

The 5G NR (New Radio) encoder is responsible for encoding information bits into a larger codeword to improve the reliability of data transmission. The 5G NR standard uses a specific type of forward error correction (FEC) technique called LDPC (Low-Density Parity-Check) for error correction. The 5G NR LDPC encoder adds redundant bits to the original information bits to create the codeword.

The 5G NR LDPC encoder uses a parity-check matrix to determine the redundant bits to add to the information bits. The matrix is designed to have a low-density of 1's, which means that only a small fraction of the bits in the matrix are 1's. This reduces the complexity of the encoder and decoder while still providing high coding gain and low error rates.

The 5G NR LDPC encoder can operate at different code rates, which determines the amount of redundancy added to the information bits. The higher the code rate, the more redundancy is added, which results in a more robust codeword but also increases the latency of the transmission.

The 5G NR encoder also includes other features such as rate matching and code block segmentation. Rate matching is used to adjust the size of the codeword to match the modulation and channel coding scheme used for transmission. Code block segmentation is used to divide the information bits into smaller code blocks, which can be processed and encoded separately.

Overall, the 5G NR encoder is a critical component of the communication system, and its performance impacts the reliability and efficiency of data transmission over 5G networks.

A. 5G-NR LDPC Channel coding

Based on my interpretation from 3GPP Specification 38.212 Rel 15 (Multiplexing and channel coding), I had put together the procedure on how LDPC Base Graph selection and coding happens.

- 1) For transmission of a DL transport block, a transport block CRC is first appended to provide error detection, followed by a LDPC base graph selection.
- NR supports two LDPC base graphs, one for small transport blocks and one for larger transport blocks.
- Then transport block is segmented into code blocks and code block CRC attachment is performed.

- 4) Each code block is individually LDPC encoded. The LDPC coded blocks are then individually rate matched.
- 5) Finally, code block concatenation is performed to create a codeword for transmission. Up to 2 code words can be transmitted simultaneously.

B. LDPC Base Graphs

There are two types of Base Graphs standardized in the specification, 3GPP Specification 38.212 (Multiplexing and channel coding).Base Graph is a Matrix where each of the entries can be further expanded based on the expansion factor 7c

1) Base Graph 1 (BG1): With Matrix size 46X68 entries For Large Transport Block.

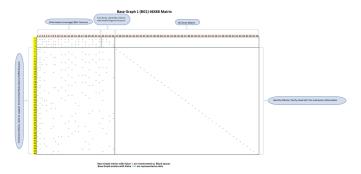


Fig. 1: High Level Visualization of fully Populated BG1 Matrix

2) Base Graph 2 (BG2): With matrix size 42X52 entries For Smaller Transport Block.

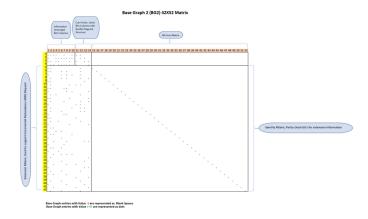


Fig. 2: High Level Visualization of fully Populated BG2 Matrix

C. LDPC Base Graph selection procedure

I have put together the following example of constructing the LDCP parity check matrix for a given information block size K and code rate R = K/N.

For simplicity I have considered a small TBS of size 20bits to illustrate below example, K=20 and R=0.25

- Obtain the base graph BG1 or BG2 for the given K (Transport Block)and R (Code Rate),Refer 3GPP Specification 38.212 for LDPC base graph selection. As per the specification
 - a) if $K \le 3824$ and $R \le 0.67$ then BG2 is selected.
 - b) If K<= 292 then BG2 is selected
 - c) if R<=0.25 then BG2 is selected.
 - d) Else BG1 is selected

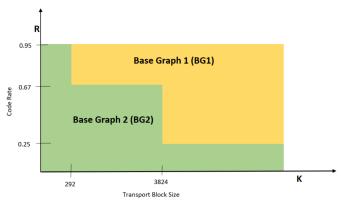


Fig. 3: Graphical Representation of Base Graph Selection

2) Determine the value of Kb for the given K (Transport Block) and R (Code Rate) Ref 3GPP Specification 38.212, 5.2.2

Kb denotes the number of information bit columns for the lifting size Zc.

As per the Specification

For LDPC BG1:

a) Kb = 22

For LDPC BG2:

- a) if K is between 640 < K <= 3824 then Kb = 10
- b) if K is between 560 < K <= 640 then Kb = 9
- c) if K is between $192 < K \le 560$ then Kb = 8
- d) If K is \leq 192 then Kb is = 6
- 3) Determine the base matrix expansion factor Zc by selecting the minimum Zc value in below Table, such that Kb*Zc >=K.Sets of LDPC lifting size Zc in the specification I have populated below Zc table.

					a				
	Zc	2	3	5	7	9	11	13	15
	0	2	3	5	7	9	11	13	15
	1	4	6	10	14	18	22	26	30
	2	8	12	20	28	36	44	52	60
j	3	16	24	40	56	72	88	104	120
	4	32	48	80	112	144	196	208	240
	5	64	96	160	224	288			
	6	128	192	320					
	7	256	384						

 $Zc=a*2^{j}$

For K=20, Zc = 4, this satisfies the condition Kb*Zc >= K, 6*4 = 24, 24>20 and this is the minimum Zc value from the above table that satisfies this condition.

4) After Zc is determined, the corresponding shift coefficient matrix set need to be selected from below Table., Ref for this table is 3GPP Specification 38.212,5.3.2

Set Index(iLS)	Set of Lifting Sizes(Zc)
0	2,4,8,16,32,64,128,256
1	3,6,12,24,48,96,192,384
2	5,10,20,40,80,160,320
3	7,14,28,56,112,224
4	9,18,36,72,144,288
5	11,22,44,88,196
6	13,26,52,104,208
7	15,30,60,120,240

Since Zc = 4, Set Index (iLS) "0" is considered.

5) Determine the entries values in the base matrix based on the Zc, Calculate the shifting coefficient value P(i,j) by the modular Zc operation.

$$P(i,j)= f(Vi,j, Zc) = mod(Vi,j, Zc)$$

3GPP TS 38.212 version 15.3.0 Release 15

Referral tables to calculate P(i,j) are available in the specification 3GPP Specification 38.212,5.3.2

ETSI TS 138 212 V15.3.0 (2018-10)

\mathbf{H}_{BG} $V_{i,j}$							\mathbf{H}_{BG} $V_{i,j}$						i.J								
Row index	Column index						Set index i_{LS}														
i	j	0	1	2	3	4	5	6	7	i	j	0	1	2	3	4	5	6	7		
	0	250	307	73	223	211	294	0	135	7 8 4 15	1	96	2	290	120	0	348	6	13		
ĺ	1	69	19	15	16	198	118	0	227		10	65	210	60	131	183	15	81	22		
	2	226	50	103	94	188	167	0	126		13	63	318	130	209	108	81	182	17		
[3	159	369	49	91	186	330	0	134		18	75	55	184	209	68	176	53	14		
	5	100	181	240	74	219	207	0	84		25	179	269	51	81	64	113	46	49		
i	6	10	216	39	10	4	165	0	83		37	0	0	0	0	0	0	0	0		
- [9	59	317	15	0	29	243	0	53	2225 2005 128 75 135 217 220 90	- 1	64	13	69	154	270	190	88	78		
	10	229	288	162	205	144	250	0	225		3	49	338	140	164	13	293	198	15		
ĺ	11	110	109	215	216	116	- 1	0	205		11	49	57	45	43	99	332	160	84		
0	12	191	17	164	21	216	339	0	128		20	51	289	115	189	54	331	122	- 5		
	13	9	357	133	215	115	201	0	75		22	154	57	300	101	0	114	182	20		
i	15	195	215	298	14	233	53	0	135		38	0	0	0	0	0	0	0	0		
i	16	23	106	110	70	144	347	0	217		0	7	260	257	56	153	110	91	18		
Ī	18	190	242	113	141	95	304	0	220		14	164	303	147	110	137	228	184	11		
ĺ	19	35	180	16	198	216	167	0	90		16	59	81	128	200	0	247	30	10		
	20	239	330	189	104	73	47	0	105		17	1	358	51	63	0	116	3	21		
	21	31	346	32	81	261	188	0	137		21	144	375	228	4	162	190	155	12		
	22	-1	- 1	1	1	1	1	0	- 1		39	0	0	0	0	0	0	0	0		
	23	0	0	0	0	0	0	0	0		1	42	130	260	199	161	47	1	18		

Fig. 4: LDPC base graph 1 (BG 1) and its parity check matrices

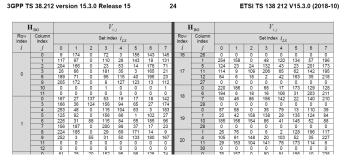


Fig. 5: LDPC base graph 2 (BG 2) and its parity check matrices

For K=20, Base Graph = 2, Zc= 4 and SetIndex iLS = 0, from above Table LDPC base graph 2 Using the equation P(i,j) = f(Vi,j, z) = mod(Vi,j, z) all the possible base graph matrix entries with the shifting coefficient are determined.

Below I have illustrated how the Base Matrix entries P(i.j) are populated, I have considered only the first row for below illustration, like wise the full matrix is built.

Н	I _{BG}				V_{i}	i, j			
Row index	Column index	Set index $i_{LS}^{}$							
i	j	0	1	2	3	4	5	6	7
	0	9	174	0	72	3	156	143	145
	1	117	97	0	110	26	143	19	131
	2	204	166	0	23	53	14	176	71
0	3	26	66	0	181	35	3	165	21
U	6	189	71	0	95	115	40	196	23
	9	205	172	0	8	127	123	13	112
	10	0	0	0	1	0	0	0	1
	11	0	0	0	0	0	0	0	0

Fig. 6: LDPC parity check matrix selection

From Step 3 and 4 Set Index iLS = 0 and Zc = 4

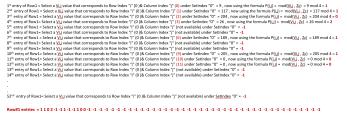


Fig. 7: modulus operation

Using the above principal remaining all entries of the matrix can be populated based on the Row index i and Column Index j and SetIndex(iLS)

I populated the below full base graph2 matrix with Zc=4

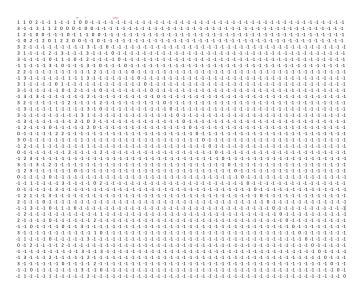


Fig. 8: Base Graph2 for iLS=0 and Zc=4