THE NAVIC STANDARD

Through Python

G. V. V. Sharma



Copyright ©2023 by G. V. V. Sharma.

https://creative commons.org/licenses/by-sa/3.0/

 $\quad \text{and} \quad$

 $\rm https://www.gnu.org/licenses/fdl-1.3.en.html$

Contents

Intro	roduction							iii
1 I	Design Parameters							1
1.1	The Frequency Bands			 	 	 	•	1
2 (Channel Modelling							3
2.1	Doppler shift			 	 	 		3
2.2	Delay			 	 	 		5
2.3	Power Scaling			 	 	 		6
2.4	Thermal noise			 	 	 		6
3 Т	Transmitter							9
3.1	Frame structure			 	 	 		9
	3.1.1 Cyclic Redundancy C	heck(CR	C)	 	 	 		9
3.2	Encoding			 	 	 	ě	10
	3.2.1 Interleaving			 	 	 		10
	3.2.2 Sync word and Tail b	its		 	 	 		10
3.3	Modulation			 	 	 		10
	3.3.1 Standard Positioning	Service .		 	 	 	•	10
	3.3.2 Pseudo Random Nois	e codes(PRNI)					11

	3.3.3	Baseband Modulation	11
4 F	Receiver		13
4.1	RTL SDR		13
	4.1.1	Radio block diagram for L5 band signal receiver	14
	4.1.2	Results	14
4.2	USRP SDI	R	15
	4.2.1	SDR specification	15
	4.2.2	Radio block diagram for L5 band signal receiver	16
	4.2.3	Results	17

Introduction

This book introduces the NAVIC communication standard through Python exercises

Design Parameters

1.1. The Frequency Bands

Channel Modelling

The phenomena modelled in the satellite communication channel are Doppler shift, delay, power scaling and thermal noise at the receiver.

2.1. Doppler shift

Due to relative motion between the satellites and the receiver, the transmitted signals undergo a frequency shift before arriving at the receiver. This shift in frequency is called Doppler shift and can be computed as

$$f_{shift} = f_d - f_c = \left(\frac{V_{rel}}{c - V_{S,dir}}\right) f_c \tag{2.1}$$

where,

 $f_{Shift} =$ Frequency shift due to Doppler

 f_d = Frequency observed at receiver

 $f_c = \text{Carrier frequency at transmitter}$

 V_{rel} = Relative velocity of transmitter and receiver

 $V_{S,dir}$ = Velocity of satellite along radial direction

c =Speed of light

 V_{rel} is given by

$$V_{rel} = V_{S,dir} - V_{R,dir} \tag{2.2}$$

where,

 $V_{R,dir}$ = Velocity of receiver along radial direction

 $V_{R,dir}$ and $V_{S,dir}$ are given by

$$V_{R,dir} = \mathbf{V}_R \cdot \hat{\mathbf{dir}} \tag{2.3}$$

$$V_{D,dir} = \mathbf{V}_S \cdot \hat{\mathbf{dir}} \tag{2.4}$$

where,

 $\hat{\mathbf{dir}} = \text{Unit vector from satellite to receiver i.e. radial direction}$

 $\mathbf{V_S} = \text{Velocity of satellite}$

 $\mathbf{V_R}$ = Velocity of receiver

dîr is given by

$$\hat{\mathbf{dir}} = \frac{\mathbf{P_S} - \mathbf{P_R}}{\|\mathbf{P_S} - \mathbf{P_R}\|} \tag{2.5}$$

where,

 $\mathbf{P_S} = \text{Position of satellite}$

 $\mathbf{P_R} = \text{Position of receiver}$

The Doppler shift is introduced by muliplying the satellite signal with a complex exponential,

$$x_{Shift}[n] = x[n] e^{-2\pi j \left(f_c + f_{Shift}\right) n t_s}$$
(2.6)

where,

 $x_{Shift}[n] = \text{Doppler shifted signal}$

x[n] = Satellite signal

 $t_s = \text{Sampling period}$

2.2. Delay

Since there is a finite distance between the satellite and the receiver, the signal at the receiver is a delayed version of the transmitted signal. This delay is given by

$$D_s = \frac{d}{c} f_s \tag{2.7}$$

where,

 $D_s = \text{Total delay in samples}$

d = Distance between satellite and receiver

c =Speed of light

 $f_s =$ Sampling rate

The total delay on the satellite signal is modeled in two steps. First, a static delay is modeled which does not change with time and it is always an integer number of samples. Then, a variable delay is modeled which can be a rational number of samples. While modelling the static delay, the entire delay is not introduced so that variable delay modelling handles the remaining delay.

To introduce the static delay, the samples are read from a queue whose size is the desired static delay length. When samples are read from the queue, an equal number of new samples are updated in the queue. To introduce the variable delay, the signal is passed through an all-pass FIR filter with an almost constant phase response. Its coefficients are calculated

using the delay value required.

2.3. Power Scaling

When a transmitting antenna transmits radio waves to a receiving antenna, the radio wave power received is given by,

$$P_r = P_t D_t D_r \left(\frac{1}{4\pi \left(f_c + f_{Shift} \right) D} \right)^2 \tag{2.8}$$

where,

 P_r = Received power

 $P_t = \text{Transmitted power}$

 $D_t = \text{Directivity of transmitting antenna}$

 D_r = Directivity of receiving antenna

D = Total delay in seconds

To scale the received signal as per the received power calculated,

$$x_{Scaled}[n] = \frac{\sqrt{P_r}}{\text{rms}(x[n])}x[n]$$
(2.9)

2.4. Thermal noise

The thermal noise power at the receiver is given by,

$$N_r = kTB (2.10)$$

where,

 N_r = Noise power in watts

k = Boltzmann's constant

T = Temperature in Kelvin

B = Bandwidth in Hz

AWGN (Additive White Guassian Noise) samples with zero mean and variance N_r are generated and added to the satellite signal to model thermal noise at receiver.

The functions necessary to model the channel are present in the below code,

codes/channelmodel.py

Transmitter

3.1. Frame structure

NavIC master frame consists of 2400 symbols, divided to four subframes. Each subframe is 600 symbols long. Subframes 1 and 2 transmit fixed navigation parameters. Subframe 3 and 4 transmit secondary navigation parameters in the form of messages. Each subframe is 292 bits long without FEC encoding and sync word. It starts with TLM word of 8 bits. Ends with 24 bit Cyclic Redundancy Check(CRC) followed by 6 tail bits. In subframes 1 and 2 navigation data is alloted 232 bits, starting from bit 31. In subframe 3 and 4, 220 bits are alloted starting from bit 37. For detailed structure of subframes, refer to chapter 5.9 in the doc

3.1.1. Cyclic Redundancy Check(CRC)

The parity coding of data signal follows 24Q polynomial for each subframe. 24 bits of CRC parity will provide protection against burst as well as random errors with undetected eroor probability of 2^{-24} for all channel bit error probabilities 0.5

$$g(X) = \sum_{i=0}^{24} g_i X^i \quad g_i = 1 \text{ for } i = 0, 1, 3, 4, 5, 6, 7, 10, 11, 14, 17, 18, 23, 24$$
 (3.1)

3.2. Encoding

The navigation data subframe of 292 bits is rate 1/2 convolution encoded and clocked at 50 symbols per second. Each subframe of 292 bits after encoding results in 584 bits. For parameters and coding scheme, refer to below doc

3.2.1. Interleaving

Any burst errors during the data transmission can be corrected by interleaving. In matrix interleaving, input symbols are filled into a matrix column wise and read at the output row wise. This will spread the burst error, if any, during the transmission. For SPS, data is filled into matrix of size 73 by 8(73 columns, 8 rows).

3.2.2. Sync word and Tail bits

Each subframe has a 16 bit word synchronization pattern which is not encoded. Sync pattern is EB90 Hex. Tail bit consists of 6 zero value bits enabling completion of FEC decoding of each subframe in the receiver.

3.3. Modulation

3.3.1. Standard Positioning Service

The SPS signal is BPSK(1) modulated on L5 and S bands. The navigation data at data rate of 50 sps (1/2 rate FEC encoded) is modulo 2 added to PRN code chipped at 1.023 Mcps. The CDMA modulated code, modulates the L5 and S carriers at 1176.45 MHz and

2492.028 MHz respectively.

3.3.2. Pseudo Random Noise codes(PRN)

NavIC uses Gold codes fo SPS signal. They are generated using Linear Feedback Shift Registers. For L5 and S band, the code length is 1ms and consists of 1023 chips. The code is chipped at 1.023 Mcps. Two polynomials G1 and G2 are used to generate the gold code sequence. G2's initial state provides unique PRN code for each satellite. All bits of G1 are initialized as 1. G1 and G2 are XOR'ed to generate final 1023 chip long PRN sequence, the time period being 1ms. For more information refer to chapter 4 in the doc.

3.3.3. Baseband Modulation

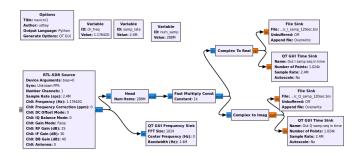
The carrier signal is modulated by BPSK(1), Data channel BOC(5,2), and Pilot Channel BOC(5,2). To have a constant envelop when passed through power amplifier, we add additional signal called interplex signal. For detailed mathematical equations, refer to chapter 3.3 in the below document

Receiver

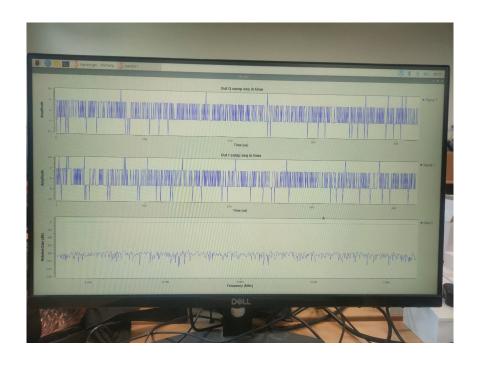
4.1. RTL SDR

Name	RTL-SDR(hardware modded R820T2/RTL2838U DVB-T)
Type	Pre-built and pre-modded with custom driver
Frequency range	0.5 – 1766 MHz(mod: RTL2832U Q-branch pins soldered to
	antenna port)
Max. Bandwidth	Matches sampling rate, but with filter roll-off
Receiver ADC bits	8
Tx. DAC bits	-
TX. Capable	No
Sampling Rate	2.4 MHz (can go up to 3.2 MHz but drops samples)
Frequency accuracy	1
ppm	
Host interface	USB
FPGA	-
Frequency accuracy	1
ppm	

4.1.1. Radio block diagram for L5 band signal receiver



4.1.2. Results

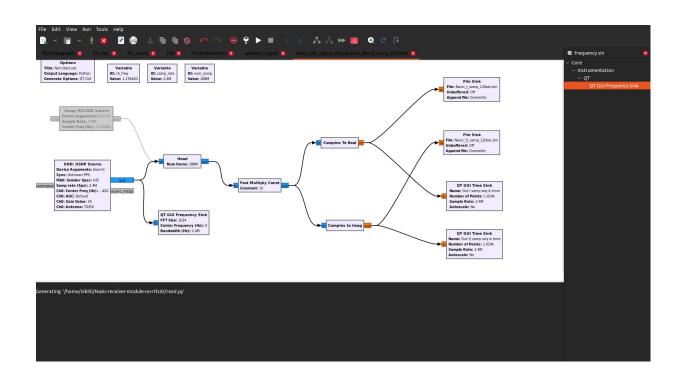


4.2. USRP SDR

4.2.1. SDR specification

Name	USRP B210
Туре	Pre-built
Frequency range	$70~\mathrm{MHz}-6~\mathrm{GHz}$
Max. Bandwidth	56MHz
Receiver ADC bits	12
Tx. DAC bits	12
TX. Capable	Yes
Sampling Rate	56 Msps
Frequency accuracy ppm	-
Host interface	USB 3.0
FPGA	Xilinx Spartan 6 XC6SLX150

4.2.2. Radio block diagram for L5 band signal receiver



4.2.3. Results

