

Top Fabrication (Scale 1:3)

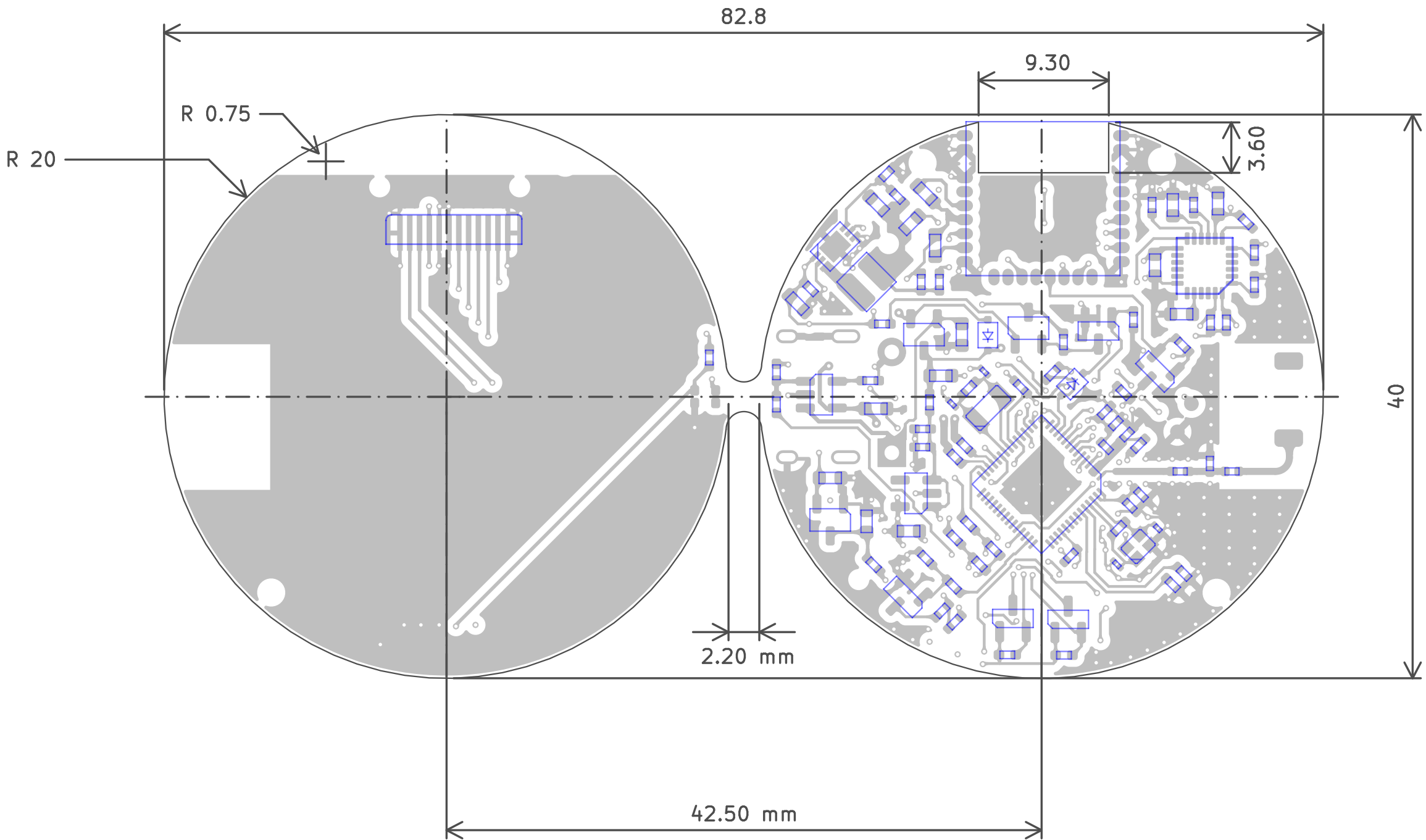
Stackup Table

Layer Name	Type	Material	Thickness (mm)	Color
F.Silkscreen	Top Silk Screen	Not specified	0 mm	Not specified
F.Paste	Top Solder Paste		0 mm	
F.Mask	Top Solder Mask	Not specified	0.01 mm	Not specified
F.Cu	copper		0.035 mm	
Dielectric	prepreg	FR4	0.1 mm	Not specified
In1.Cu	copper		0.035 mm	
Dielectric	core	FR4	1.24 mm	Not specified
In2.Cu	copper		0.035 mm	
Dielectric	prepreg	FR4	0.1 mm	Not specified
B.Cu	copper		0.035 mm	
B.Mask	Bottom Solder Mask	Not specified	0.01 mm	Not specified
B.Paste	Bottom Solder Paste		0 mm	
B.Silkscreen	Bottom Silk Screen	Not specified	0 mm	Not specified

FABRICATION NOTES (UNLESS OTHERWISE SPECIFIED)

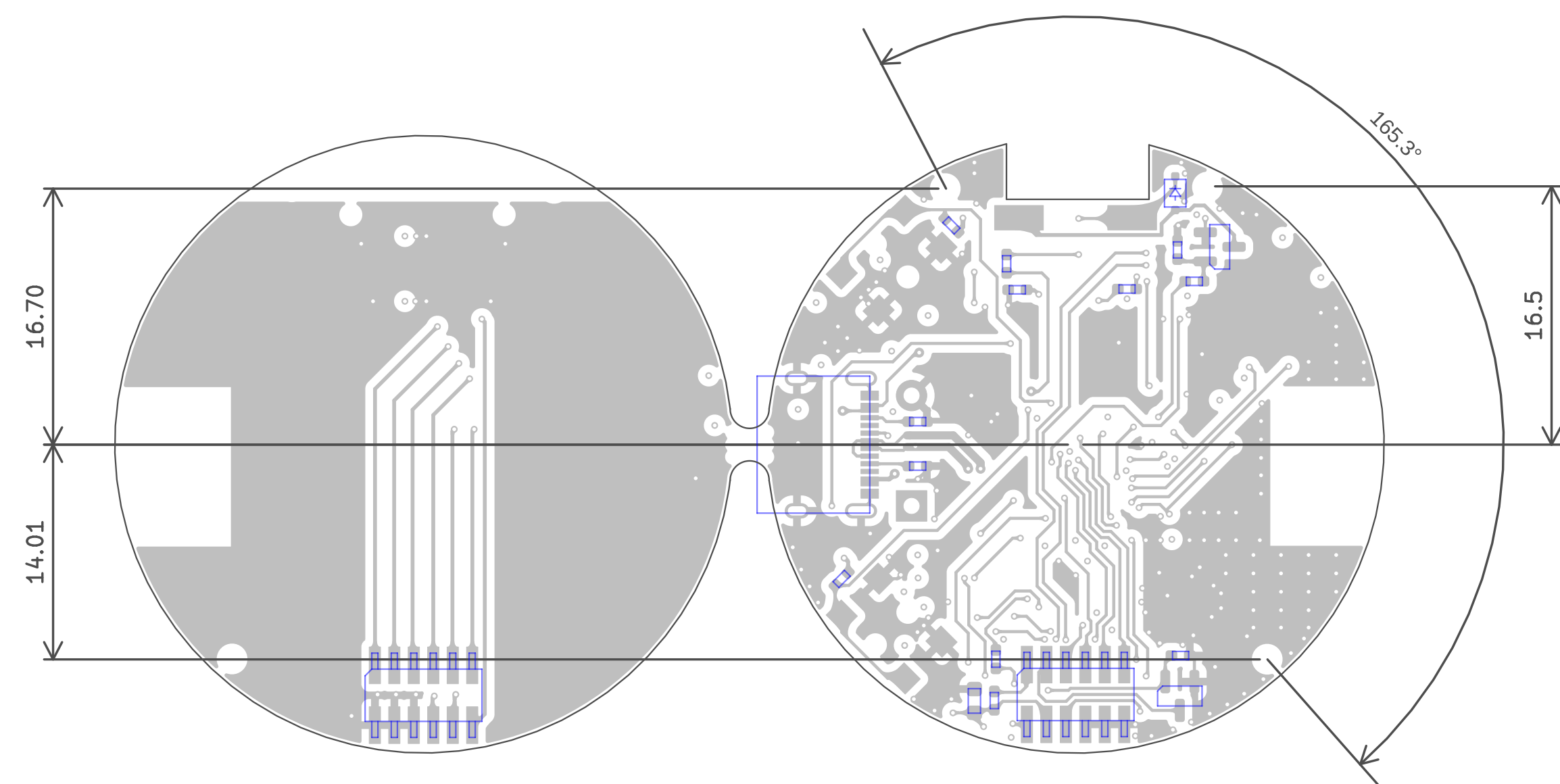
- 1) OUTLINE DEFINED IN SEPARATE GERBER FILE WITH "Edge_Cuts.GBR" SUFFIX.
DIMENSIONS OF CIRCUMSIZED RECTANGLE SHOWN ON THIS DWG FOR REF ONLY.
- 2) SEE SEPARATE DRILL FILES WITH ".DRL" SUFFIX FOR HOLE LOCATIONS.SELECTED HOLE LOCATIONS SHOWN ON THIS DWG FOR REF ONLY.
- 3) IMPEDANCE CONTROL REQUIRED.
Microstrip 90-Ohm Differential (USB)
0.5334 mm width, 0.2032 mm spacing
- 4) CONFIRM TRACE WIDTHS AND SPACINGS.
- 5) DESIGN GEOMETRY MINIMUM FEATURE SIZES:

TRACE WIDTH0.16 mmTRACE TO TRACE0.20 mmMIN. HOLE (PTH)0.4 mmMIN. HOLE (NPTH)0.2 mmANNULAR RING0.1 mmCOPPER TO HOLE0.25 mmCOPPER TO EDGE0.2 mmHOLE TO HOLE0.3 mm



All dimensionsarein millimeters unless otherwise specified.

Bottom Fabrication (Scale 1:3)



All dimensions are in millimeters unless otherwise specified.

Drill Drawing (Top View) (Scale 1:3)

Symbol	Count	Hole Size	Plated
.	273	0.2mm	Plated
o	4 slots	0.6mm	Plated
+	2	1mm	Plated
◻	6	0.5mm	Unplated
◊	2	0.65mm	Unplated
⊠	6	1mm	Unplated
✱	6	1.5mm	Unplated

