Lab 3 report

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1. **4-bit Ping-Pong Counter**
2. **First-In First Out (FIFO) Queue**
3. **Multi-Bank Memory**
4. **Round-Robin FIFO Arbiter**
5. **4-bit Parameterized Ping-Pong Counter**
6. **4-bit Parameterized Ping-Pong Counter on FPGA**