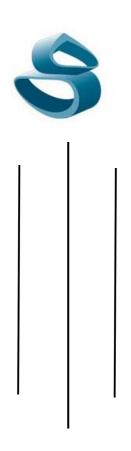
SAGARMATHA ENGINEERING COLLEGE

(TU Affiliated)
Sanepa, Lalitpur



LAB NO: LAB REPORT ON:

Submitted By:	Submitted To:
Name:	Department of electronics and Computer Engineering
Faculty/Year:	Signature:
Roll No:	Date:
Date:	

LAB-07

TITLE: PROGRAMMING IN 8086 MICROPROCESSOR

OBJECTIVE:

1. To get familiar with 8086 commands and learn about character and string input and display.

PROGRAMS:

1) WAP in ALP to take character as input

.MODEL SMALL

.STACK 64

.DATA

.CODE

MAIN PROC FAR

MOV AX, @DATA

MOV DS, AX

MOV AH, 01H

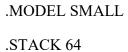
INT 21H

MOV AX, 4C00H

INT 21H

MAIN ENDP

2) Write an ALP to display a character.



.DATA

.CODE

MAIN PROC FAR

MOV AX, @DATA

MOV DS, AX MOV AH, 02H

MOV DL, 'A'

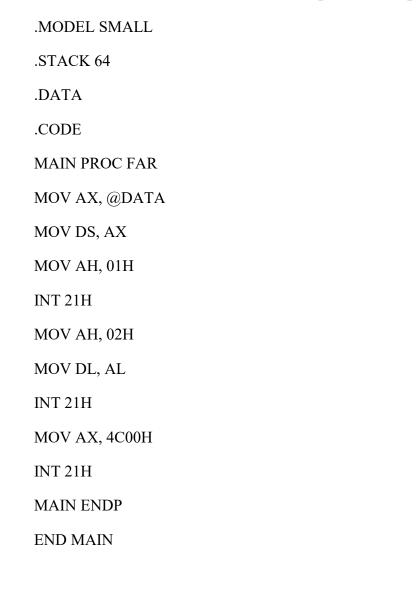
INT 21H

MOV AX, 4C00H

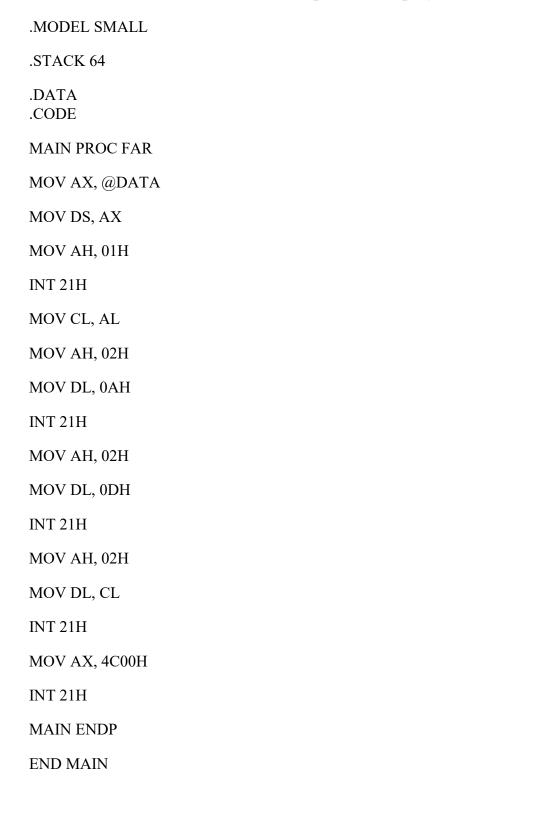
INT 21H

MAIN ENDP

3) Write an ALP to take a character input and display it.



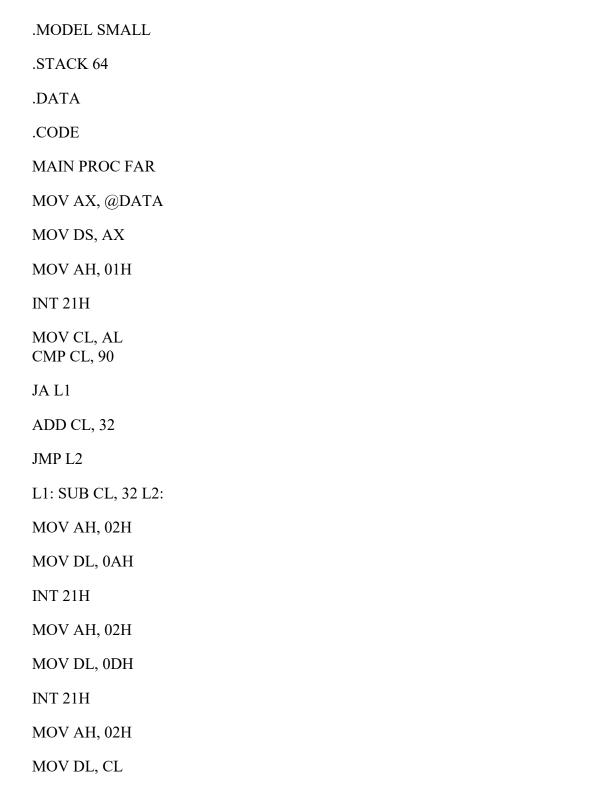
4) Write an ALP to take a character input and display it in next line.



5) Write an ALP to take a character input and display it in uppercase in next line.



6) Write an ALP to take a character input and display toggled case in next line.



INT 21H

MOV AX, 4C00H

INT 21H

MAIN ENDP

7) Write an ALP to display a string.

.MODEL SMALL

.STACK 64

.DATA

STR DB 'ARUN'

.CODE

MAIN PROC FAR

MOV AX, @DATA

MOV DS, AX

MOV AH, 09H

LEA DX, STR

INT 21H

MOV AX, 4C00H

INT 21H

MAIN ENDP

8) Write an ALP to take a string input and display it.

.MODEL SMALL

.STACK 64

INT 21H

MAIN ENDP

.DATA MAXLEN DB 255 ACTLEN DB? INPUT DB 255 DUP ('\$') .CODE MAIN PROC FAR MOV AX, @DATA MOV DS, AX MOV AH, 0AH LEA DX, MAXLEN INT 21H MOV AH, 02H MOV DL, 0AH INT 21H MOV AH, 02H MOV DL, 0DH INT 21H MOV AH, 09H LEA DX, INPUT INT 21H MOV AX, 4C00H

END MAIN

DISCUSSION AND CONCLUSION:

This lab introduced fundamental programming concepts in the 8086 microprocessor using Assembly Language Programs (ALP). The lab covered character and string input/output, including tasks such as displaying characters, toggling case, converting to uppercase, and handling strings. These programs provided insights into using interrupts (INT 21H) for input/output operations.

In conclusion, this lab enhanced understanding of 8086 microprocessor programming by implementing various character and string manipulation tasks.

Lab-OZ TITLE: DRAW EQUIVALENT CIRCUIT OF TRANSFORMER BY SHORT CIRCUIT AND OPEN CIRCUIT TEST

OBJECTIVE:

i) To find open circuit and short circuit parameter of transformer.

ii) To draw equivalent circuit of given transformer.

APPARATUS:

i) 1-\$ transformer

ii) Ammeter

iii) Wattmeter

iv) Voltmeter

v) Connecting cuires

THEORY:

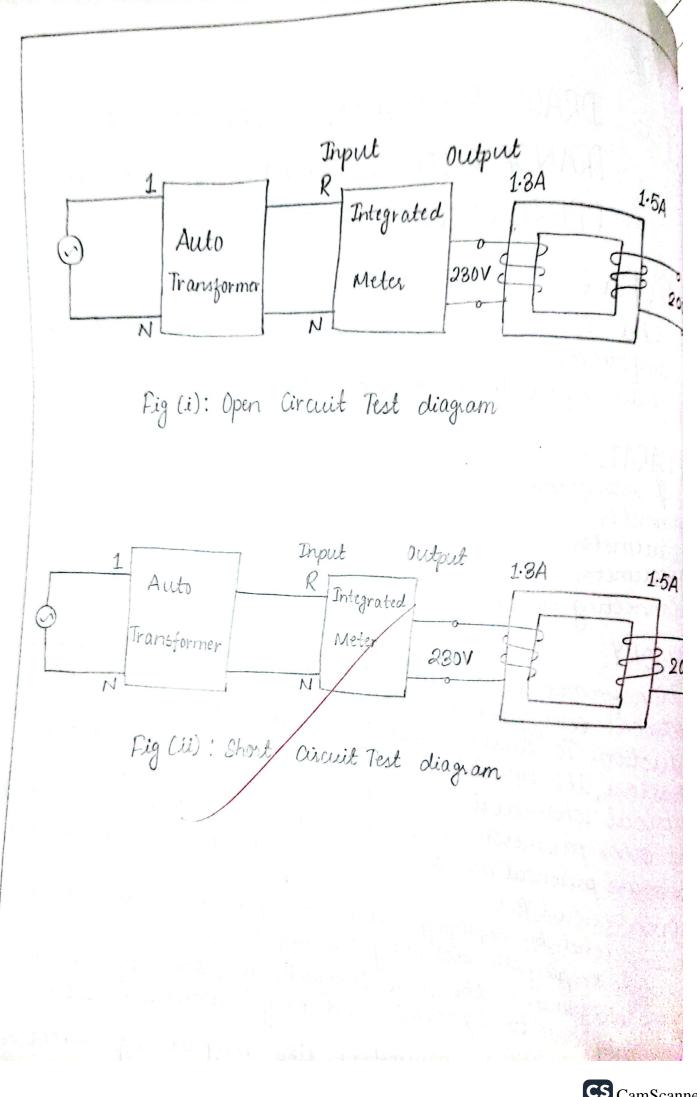
A transformer is a static electric device that transfers electrical energy between circuits through electromagnetic induction. To analyze and understand transformer behaviour, its equivalent circuit is derived, representing the electrical characteristics in terms of resistances, reactances and other parameters. The short circuit (SC) and open circuit (OC) tests are practical methods used to determine these parameters.

1) Open Circuit Test:

i) Conducted by applying rated voltage to the primary winding while keeping the secondary winding open.

ii) It determines the core (magnetizing) losses, which are mainly due to hysteresis and eddy currents in core.

iii) From this test, parameters like magnetizing reactance (Xo) and core loss resistance (Ro) are derived.



2) Short Circuit rest.

i) Performed by short-circuiting the secondary winding and applying a reduced voltage to the primary winding to produce full-load current.

to produce full-load current.

ii) This test determines the copper losses in winding and helps calculate the equivalent impedence (Teq) and its components equivalent resistance (Req) and reactance(Xeq).

By using these parameters, we can make an equivalent circuit of transformer.

OBSERVATION TABLE:

1) Open Circuit Test:

SN	To CA)	Vo(V)	P.CW)
1	0.11	230	8
2	0.137	200	9
3	0.241	115	9/

11) Short Circuit Pest:

SN	Isc (A)	Vsc (V)	Psc(W)
1	1.3	137	17
2	1.3	10.9	14
3	1.3	5.1	6

CALCULATION:

i) For Open circuit test:

$$\therefore \cos \phi_{01} = \frac{8}{230 \times 0.11} = 0.316$$

$$\cos \phi_{02} = \frac{9}{200 \times 0.137} = 0.328$$

$$\cos \phi_{03} = \frac{9}{115 \times 0.241} = 0.324$$

Iw: To cosp. :. Iw1 = 0.11 ×0.316 = 0.035A Two = 0.137 x 0.328 = 0.045A Two = 0.241 x 0.324 = 0.078A Tu: To sinfo Iu1 = 0.11 × 0.949 = 0.104A IM2 = 0.137 X 0.945 = 0.129 A THIS = 0.241X0.946 = 0.228A $X_0 = \frac{V_1}{T_{11}}$ Ro: VI $X_{01} = \frac{230}{0.104} = 2.212 \times 10^{3} \Omega$ $Ro1 = \frac{230}{0.025} = 6.57 \times 10^{3} \Omega$ $X_{02} = \frac{200}{0.129} = 1.55 \times 10^{3} \Omega$ ROZ = 200 0.045 = 4.44 X103-D $X_{03} = \frac{115}{0.228} = 504.39 \Omega$ Ro3 = 115 = 1.47 × 103 D Mean $R_0 = \frac{(6.57 + 4.44 + 1.47) \times 10^3}{2} = 4160 \Omega$ Mean $X_0 = \frac{2.212 \times 10^3 + 1.55 \times 10^3 + 504.39}{2} = 1422.13 \Omega$ ii) For short circuit test: Zeg = Vsc Isc $Req = \frac{\rho_{5c}}{7^{2}}$ $Req_{1} = \frac{17}{(1\cdot3)^{2}} = 10\cdot06\cdot\Omega$ $Req_{2} = \frac{14}{(1\cdot3)^{2}} = 8\cdot28\cdot\Omega$ $Zeq_{2} = \frac{10\cdot9}{1\cdot3} = 8\cdot38\cdot\Omega$ $Zeq_{2} = \frac{10\cdot9}{1\cdot3} = 8\cdot38\cdot\Omega$ Zeq3 = 5.1 = 3.92 Req3 = 6 = 3.55.D Mean $Req = \frac{10.06 + 8.28 + 3.55}{3} = 7.3 \Omega$ Mean $Zeq = \frac{10.64 + 8.38 + 3.92}{3} = 7.61 \Omega$ $Xeq = \sqrt{7eq^2 - Req^2} = \sqrt{(7.61)^2 - (7.3)^2} = 2.15 \Omega$

RESULT: The equivalent circuit will look like: 4160A j1422·13A WWW __________ j 2:15 Ω 7312 3 Fig: Equivalent circuit of transformer referred to primary side.