VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS) IBRAHIMBAGH, HYDERABAD-31

Department of Computer Science and Engineering

BE-VII SEMESTER II-Internal Examination
Name of the COURSE: COMPILER CONSTRUCTION

Subject Reference Code: UI21PC630CS

Open Book Test

Max Marks: 30 Date: 16/05/2024

Time: 2:30PM to 4:30PM

Note: Answer any four questions

(4 X 7.5 = 30M)

Q. No.	Question	Marks	BTL (1/2 /3/4 /5/6)	Mapped	
				CO	PO
1	 Define Synthasized attributes and inheritted attributes S → L . L L L → L B B B → 0 1 Design an L-attributed SDD to compute S.val, the decimal-number value of an input string. For example, the translation of string 101.101 should be the decimal number 5.625. Hint: use an inherited attribute L. Side that tells which side of the decimal point a bit is on. 	7.5	3	3	1,2,3
2	Suppose that we have a production A →BCD. Each of the four non-terminals A, B, C, and D have two attributes: s is a synthesized attribute, and i is an inherited attribute. For each of the sets of rules below, tell whether. (i) the rules are consistent with an S-attributed definition (ii) the rules are consistent with an L-attributed definition, and (iii) whether the rules are consistent with any evaluation order at all? (iv) Draw dependency graph for each set of rules a) A.s = B.i + C.s. b) A.s = B.i + C.s and D.i = A.i + B.s. c) A.s = B.s + D.s. d) A.s = D.i, B.i = A.s + C.s, C.i = B.s, and D.i = B.i + C.i.	7.5	3	3	1,2,3

4.5	3	4	1,2,3
4.5	3	4	1,2,3
4.5	3	4	1,2,3
4.5	3	4	1,2,3
4.5	3	4	1,2,3
4.5	3	4	1,2,3
3	3	4	1,2,3
4.5	3	4	1,2,3

b) Explain how it is possible to "leak memory" using a reference counting garbage collector and describe any technique that might	3	2	4	1.2.2
be used to address this problem.	3	3	4	1,2,3
Define basci block and control flow graph. Discuss the algorithm to construct basic blocks from the given three address code for (i=0; i <n; (i="0;" (j="0;" (k="0;" +="" a[i][k]*b[k][j];<="" c[i][j]="c[i][j]" for="" i++)="" i<n;="" j++)="" j<n;="" k++)="" k<n;="" td=""><td>7.5</td><td>3</td><td>5</td><td>1,2,3</td></n;>	7.5	3	5	1,2,3
Construct the control flow graph for the above code 6 void quicksort(int m, int n)				
<pre>/* recursively sorts a[m] through a[n] */ { int i, j; int v, x; if (n <= m) return; /* fragment begins here */ i = m-1; j = n; v = a[n]; while (1) { do i = i+1; while (a[i] < v); do j = j-1; while (a[j] > v); if (i >= j) break; x = a[i]; a[i] = a[j]; a[j] = x; /* swap a[i], a[j] */ } x = a[i]; a[i] = a[n]; a[n] = x; /* swap a[i], a[n] */ /* fragment ends here */ quicksort(m,j); quicksort(i+1,n); } Explain machine independent optimization and convert the above code into three address code and optimize the generated three address code.</pre>	7.5	3	5	1,2,3