

22/12/2021 INTRODUCTION To ELECTRONICS ENGINEERING

UNIT-1:

* Semi-Conductor: is a material whose resistance & conductance lies between that of conductors & insulators.

Resistivity: $C < SC < I$.

Conductivity: $C > SC > I$

→ As temperature ↑ ses ; semi-conductor acts as a conductor as resistivity ↑ ses

* Temp. coefficient of resistance of semi-conductor is -ve

* Temp. coeff. of conductance of semi-conductor is +ve

* Temp. coeff. of conductance of semi-conductor is +ve

Eg: $\begin{cases} \text{Si} \rightarrow 4 \text{ val e}^- \\ \text{Ge} \rightarrow 4 \text{ val e}^- \end{cases}$

Tetravalent $\rightarrow B$

Pentavalent $\rightarrow P$

* Flow of e^- s from high potential to low potential is called **CURRENT**.

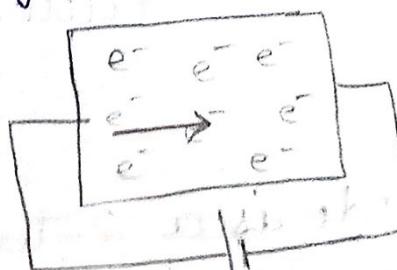
* Ways to free the valence electrons from nucleus influence:

→ External ~~Electric field~~

→ light / photon energy.

→ Increase in Temp. i.e. Heat supply.

→ Doping.



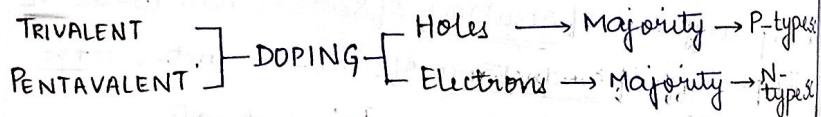
Intrinsic Semi-conductor: Semi-conductor in its purest form (or) semi-conductor before doping.

Extrinsic Semi-conductor: Intrinsic semi-conductors are converted to extrinsic semi-conductor by doping.

Hole \rightarrow +ve charge carrier (hypothetical).

$e^- \rightarrow$ -ve charge carrier (existing).

* Holes & electrons move in opp. direction.



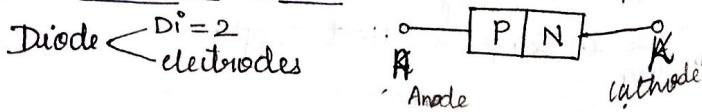
* Doping is to add trivalent/pentavalent impurities to a pure semi-conductor.

* Need of Doping:

To increase the conductivity of semi-conductor.

To improve electrical properties of semi-conductor.

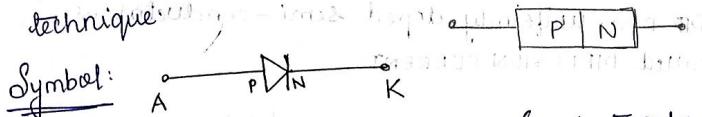
* P-N JUNCTION DIODE:



Diode is a 2-terminal device with 2 electrodes namely Anode & cathode.

construction: ~~Diode~~ Diode is formed by fusing ^{SC} P & N-type materials by special fabrication technique.

Symbol:



• Diode is a UNI-DIRECTIONAL DEVICE (conducts only in one direction).

• Diode acts as a switch as it is uni-directional.

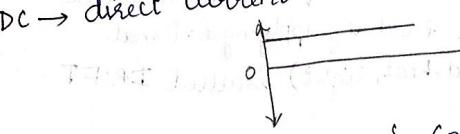
• Diode is mainly used for rectification (RECTIFIER).

RECTIFIER: Converts AC to DC

AC \rightarrow alternating current \rightarrow changes direction



DC \rightarrow direct current \rightarrow +ve quadrant



DC is used for charging. } computers require
Home Appliances requires DC. } AC change carriers

APPLICATIONS: (RECTIFIER)

\rightarrow Rectifier is used for charging.

\rightarrow Home Appliances requires DC.

* TYPES OF CURRENTS INVOLVED IN DIODES:

1) Drift Current:

2) Diffusion Current:

Movement of charge carriers from high concentration region to low concentration region, its ~~happens~~ in ~~the~~ non-uniformly doped semi-conductor is called DIFFUSION CURRENT.

* When p & n type materials are fused to form diode i.e. movement of holes from p-type to n-type the flow of hole is called diffusion current. → Beginning Stage.

• Shifting something with external force. → DRIFT.

• Moving ~~is~~ charge carriers ~~move~~ in one direction by an external force is called DRIFT CURRENT.

• Movement of charge carriers from valence band to conduction band by applying external energy (electric field, heat, light) is called DRIFT CURRENT.

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* Compare Diffusion current & Drift current:

DIFFUSION

- 1) Movement of charge carriers from high conc. to low conc.
- 2) It is done directly without any force.

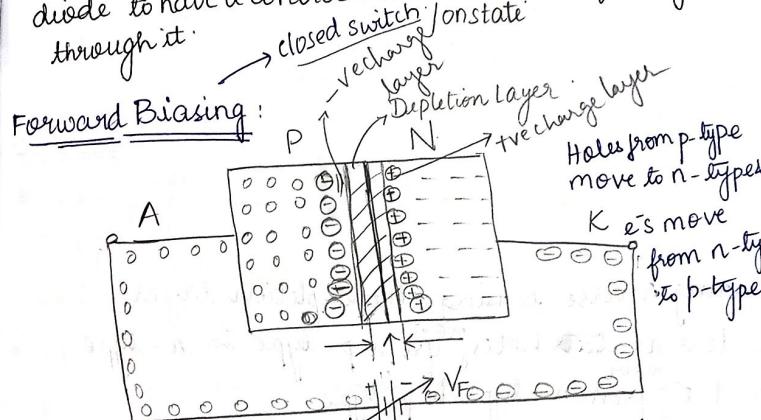
DRIFT:

- 1) movement of charge carriers from V.B To C.B.
- 2) It is done using some external force i.e. heat, light.

- 3) It flows during the beginning of construction
- 4) It depends on doping as it happens only in non-uniformly doped
- 3) It flows even after the construction of diode
- 4) It does happen in uniformly & non-uniformly doped semi-conductor

BIASING OF PN JUNCTION DIODE:

→ Biasing is applying external electric field to the diode to have a control over the current flowing through it.



The region where charge carriers are not moving or not able to move; that region is called Depletion layer.

At the time of depletion layer formation; the device is said to be off.

* After application of voltage; depletion region vanishes slowly (rising) at V_F and the diode carries a maximum current.

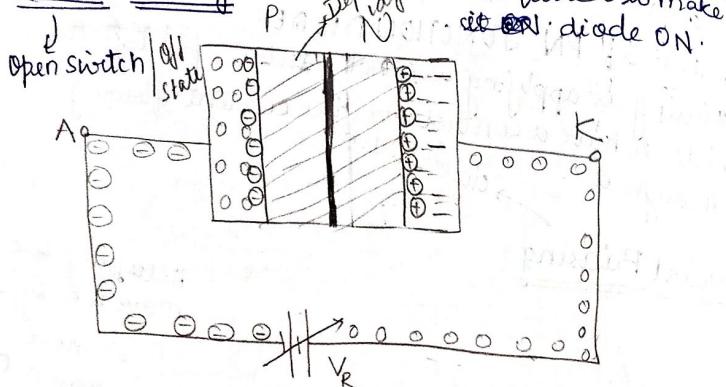
* When the diode changes from off-state to on-state the voltage at which it happens is called

BREAKOVER VOLTAGE/CUT-IN VOLTAGE/KNEE VOLTAGE

* Current is called forward current (I_F)

* REVERSE BIASING:

* Reverse Biasing:



During reverse biasing, the depletion layer widens as holes move from p-type to n-type and e⁻ from n-type to p-type.

* At a time; the flow of e⁻s & holes stops and the diode switches off. When the reverse voltage is applied, the e⁻s from the battery flow towards p-type and e⁻s in p-type repel each other and same happens near n-type i.e. holes from battery are repelled by holes present.

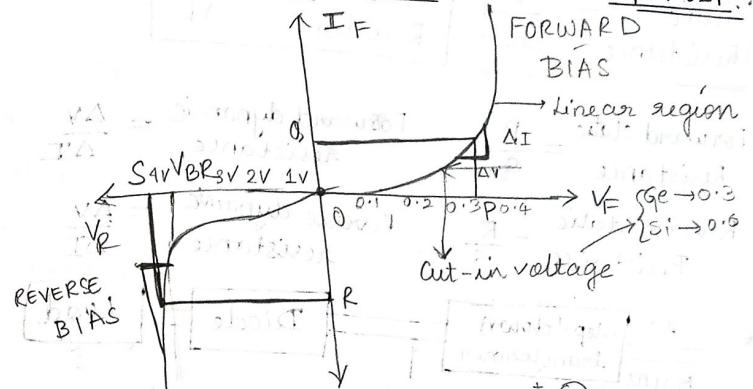
* Due to which the flow of charge carriers starts once again and the diode switches ON.

This results in reverse biasing.

Current is called reverse current (I_R)

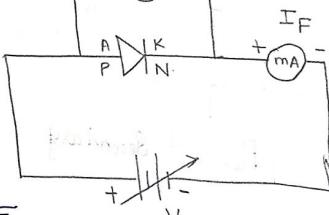
* V-I characteristics of Diode:

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CIRCUIT DIAGRAM:

* The resistance offered by the diode where DC flows is called STATIC RESISTANCE



* The resistance offered by the diode where AC flows is called DYNAMIC RESISTANCE.

* Diode offers 4 types of resistance i.e.

① Forward bias $\xrightarrow{\text{static}}$

② Reverse bias $\xrightarrow{\text{Dynamic}}$

③ Forward bias $\xrightarrow{\text{Dynamic}}$

④ Reverse bias $\xrightarrow{\text{static}}$

$$\text{Resistance of diode} = \frac{\text{Slope of the curve}}{\text{for } V_F}$$

$$\text{Static Resistance} = \frac{V}{I}$$

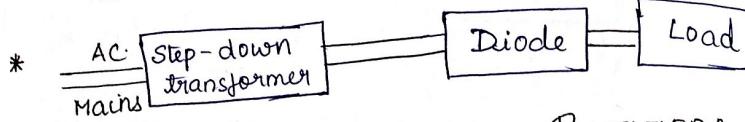
$$\text{Dynamic Resistance} = \frac{\Delta V}{\Delta I}$$

$$\text{Forward static resistance} = \frac{P}{q}$$

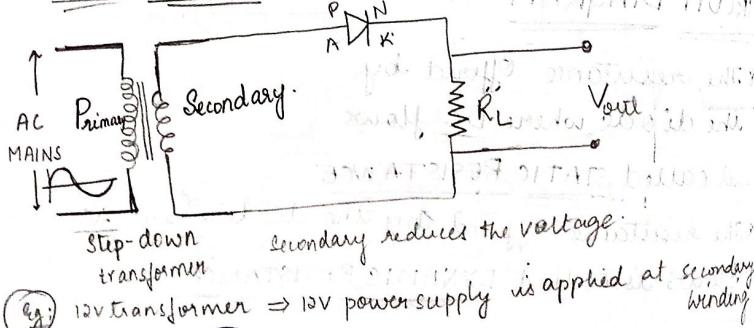
$$\text{Forward dynamic resistance} = \frac{\Delta V}{\Delta I}$$

$$\text{Reverse static resistance} = \frac{R}{S}$$

$$\text{Reverse dynamic resistance} = \frac{\Delta V}{\Delta I}$$



CIRCUIT DIAGRAM: HALF-WAVE RECTIFIER



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INPUT = AC
OUTPUT = DC

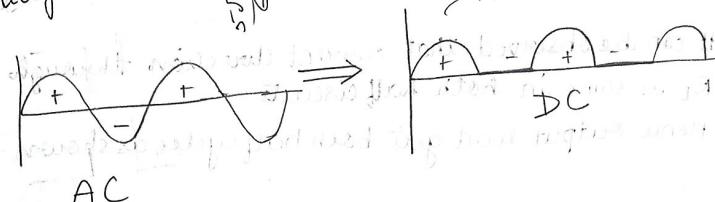
Diodes are used as clippers (clipping circuits)
clampers (clamping circuits)

During +ve half cycle primary passes the current, a reduced value to secondary and it passes +ve current; the diode acts as closed switch and is forward biased, circuit is

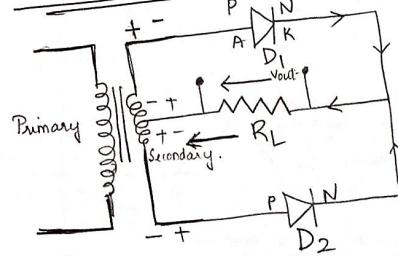
complete and current flows.

During -ve half cycle; primary passes it to secondary and it passes -ve current to p-type; diode acts as open switch and is reverse biased; circuit is open and current does not flow.

The -ve half cycle is suppressed in half-wave rectifier. Almost half of the energy is wasted in half-wave rectifier in the form of heat due to pulsating DC output.



FULL-WAVE RECTIFIER:



CENTRE TAPPED FULL WAVE RECTIFIER.

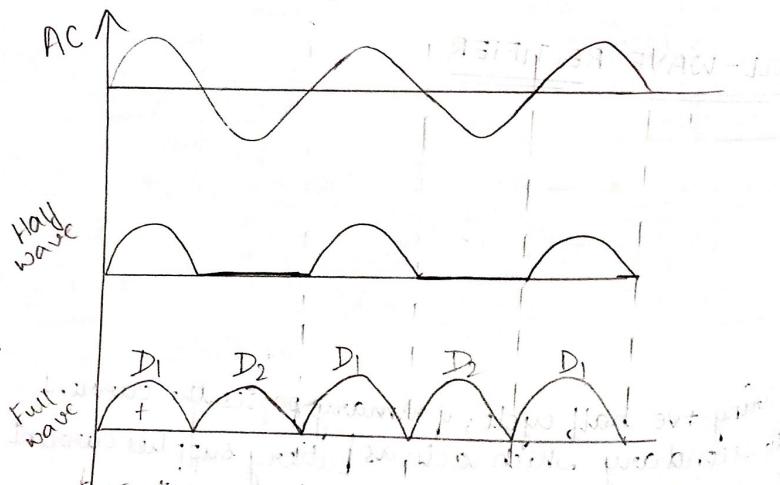
During +ve half cycle; primary passes the current to secondary which acts as battery supplies current to the circuit; only D1 is in ON state and D2 is in OFF state. This implies D1 is in forward bias and hence current flows from upper end of secondary and current flows through RL ending at -ve terminal of secondary centre.

Current does not flow through D_2 as it is off.

During -ve half cycle; current flows from primary to secondary where Secondary D_2 acts as battery; Supplies current to the circuit; where D_1 is in OFF state and D_2 is in ON state. This implies D_2 is in forward bias & hence current flows from lower end of secondary & flows through R_L & ends at +ve terminal of second centre. Current does not flow through D_1 as it is off.

It can be observed that current direction through R_L is same in both half cycles.

Hence output load gets both half cycles as shown.

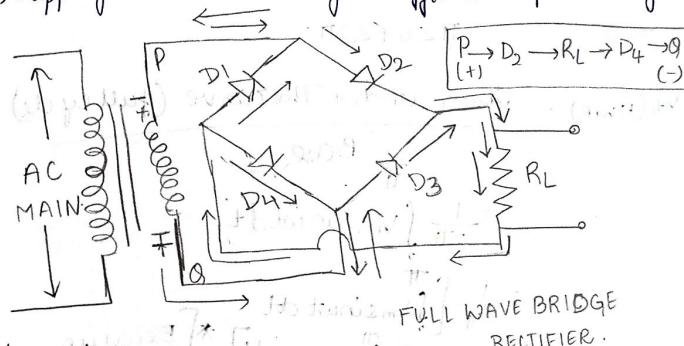


As output has pulses i.e. +ve half cycles; it is called pulsating DC.

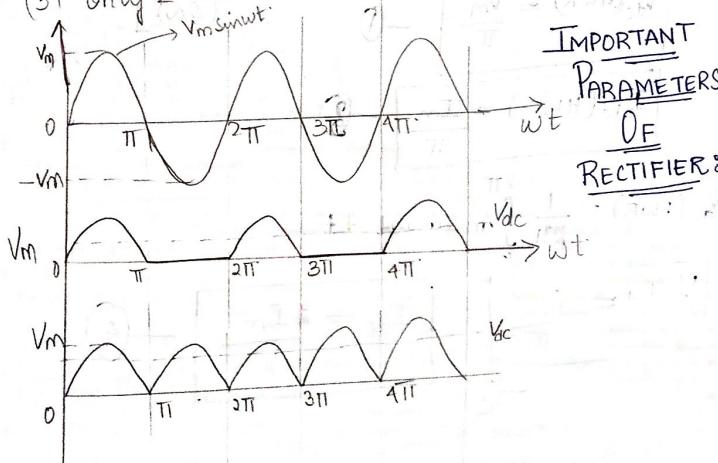
* Because circuit is using centre-tapped Transformer, it is called CENTRE-TAPPED FULL WAVE RECTIFIER.

→ No energy loss; efficiency is high.

→ Tapping the centre exactly is difficult. Practically.



- (1) + to -
- (2) I_L through R_L Same
- (3) Only 2 diodes should be conducting



03/01/2022

RIPPLE FACTOR & EFFICIENCY OF HWR & FWR

$$V_o = V_m \sin \omega t ; 0 < \omega t < \pi$$

$$= 0 \quad \pi < \omega t < 2\pi$$

$$V_{dc(HWR)} = \frac{\text{Area under the curve (full cycle)}}{\text{Base}}$$

$$= \frac{1}{2\pi} \int_0^{2\pi} V_m \sin \omega t dt$$

$$= \frac{1}{2\pi} \left[\int_0^{\pi} V_m \sin \omega t dt + \int_{\pi}^{2\pi} V_m \sin \omega t dt \right]$$

$$V_{dc(HWR)} = \frac{V_m}{\pi} \quad \text{--- (1)}$$

$$I_{dc(HWR)} = \frac{I_m}{\pi} \quad \text{--- (2)}$$

* [Because diode operates in linear region once it becomes ON]

$$V_{dc(FWR)} = \frac{1}{2\pi} \int_0^{2\pi} V_m \sin \omega t dt$$

$$V_{dc(FWR)} = \frac{2V_m}{\pi} \quad \text{--- (3)}$$

$$I_{dc} = \frac{2I_m}{\pi} \quad \text{--- (4)}$$

$$I_{rms}^2 = \frac{1}{2\pi} \int_0^{2\pi} I_m^2 \sin^2 \omega t dt$$

$$= \frac{I_m^2}{2\pi} \int_0^{2\pi} \left(\frac{1 - \cos 2\omega t}{2} \right) dt$$

$$= \frac{I_m^2}{2\pi} \left[\frac{1}{2}(2\pi) - \frac{1}{2} \int_0^{2\pi} \cos 2\omega t dt \right]$$

$$= \frac{I_m^2}{2\pi} \left[\frac{\pi}{2} - \frac{1}{2} \left(\frac{-\sin 2\omega t}{2} \right) \Big|_0^{2\pi} \right]$$

$$I_{rms}^2 = \frac{I_m^2}{2\pi} \cdot \frac{\pi}{2} = \frac{I_m^2}{4}$$

$$I_{rms} = \frac{I_m}{2}$$

$$I_{rms(HWR)} = \frac{I_m}{2} \quad \text{--- (5)}$$

$$I_{rms}^2 = \frac{I_m^2}{2\pi} \left(\frac{2\pi}{2} \right) - 0 \quad \text{--- (6)}$$

$$I_{rms} = \frac{I_m}{\sqrt{2}}$$

$$I_{rms(FWR)} = \frac{I_m}{\sqrt{2}} \quad \text{--- (6)}$$

RIPPLE FACTOR (γ) OF RECTIFIER:

Ripple factor is the term or the parameter used to measure the amount of ripple contents available at the output of the rectifier and is expressed mathematically as below.

$$\gamma = \frac{\text{AC component at output}}{\text{DC component at output}}$$

$$\gamma = \frac{I_{AC}}{I_{DC}}$$

$$I_{rms}^2 = I_{AC}^2 + I_{DC}^2$$

$$\gamma = \sqrt{\frac{I_{rms}^2 - I_{DC}^2}{I_{DC}}} = \sqrt{\left(\frac{I_{rms}}{I_{DC}}\right)^2 - 1}$$

$$\gamma_{HWR} = \sqrt{\left(\frac{I_m}{2}\right)^2 - 1}$$

$$\gamma_{HWR} = \sqrt{\frac{\pi^2}{4} - 1} = \sqrt{2.5 - 1} = \sqrt{1.5} \approx 1.22$$

$$\gamma_{HWR} \approx 1.22 \quad (9)$$

it is not used in practical

$$\gamma_{FWR} = \sqrt{\left(\frac{I_m}{\sqrt{2} I_{DC}}\right)^2 - 1}$$

$$= \sqrt{\frac{\pi^2}{8} - 1} = \sqrt{1.25 - 1} = \sqrt{0.25}$$

$$= 0.48 \approx 0.5$$

$$\gamma_{FWR} \approx 0.48 \quad (10) \rightarrow \text{ripples are less}$$

~~EFFICIENCY OF RECTIFIER: (η)~~

$$\gamma = \frac{P_{AC}}{P_{DC}}$$

EFFICIENCY

EFFICIENCY OF RECTIFIER: (η)

$$\eta = \frac{I_{DC}^2 \times R_{L}}{I_{rms}^2 \times R_L}$$

$$\eta_{HWR} = \frac{I_{rms}^2}{\frac{4\pi^2}{4}} = \frac{4}{\pi^2} = 0.406$$

$$\eta_{HWR} = 0.406 = 40.6\% \quad (11)$$

$$\eta_{FWR} = \frac{I_{rms}^2 \times 4}{\pi^2} = \frac{4B}{\pi^2} = 0.812$$

$$\eta_{FWR} = 0.812 = 81.2\% \quad (12)$$

~~EFFICIENCY OF RECTIFIER: (η)~~

→ Expression for efficiency, above is neglecting the resistance of secondary winding (r_s) and the internal resistance of the diode (r_f).

* r_s & r_f values given in a problem; those values along with R_L are considered only in denominator i.e. AC across the circuit.

* As only AC comes r_s & r_f in the circuit.

→ $r_f = 0 \rightarrow$ ideal diode (internal resistance = 0).



* ELECTRONIC FILTERS:

- Inductor filter (choke filter): connected in series with load.
 - Capacitor filter: connected in parallel with load.
- $X_L = 2\pi f L$ $\rightarrow L$ blocks AC. ($f = \text{high}$)
 $X_C = \frac{1}{2\pi f C}$ $\rightarrow C$ allows AC. ($f = \text{very high}$)
 $X_C = \frac{1}{2\pi f C}$ $\rightarrow C$ blocks DC. ($f = \text{high}$)

Electronic filters are the circuits used to remove the unwanted AC components at the output of rectifier; so that only DC can reach the load.

Inductors & Capacitors are used for this purpose because of their properties stated as above

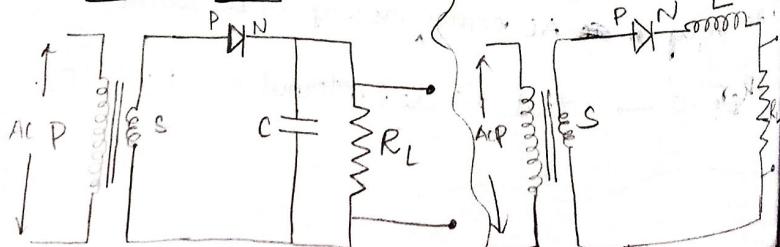
JYPES : (high loads)

* CAPACITOR FILTER & INDUCTOR FILTER;

* L-TYPE FILTER & Π -TYPE FILTER:

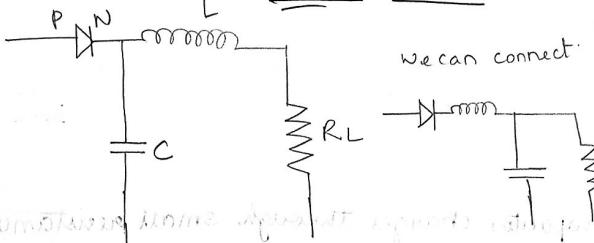
RECTIFIER $\xrightarrow{\text{AC+DC}}$ FILTERS $\xrightarrow{\text{DC}}$ LOAD

→ CAPACITOR FILTER:



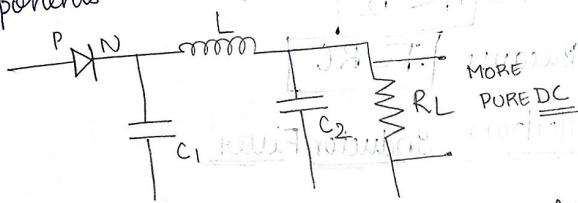
→ INDUCTOR FILTER:

Q: single capacitor is not sufficient to remove unwanted AC pulses; inductor is also used along with it as shown. = L-TYPE FILTER:

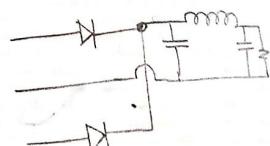
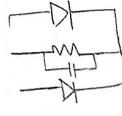
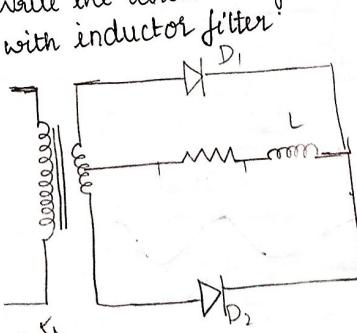


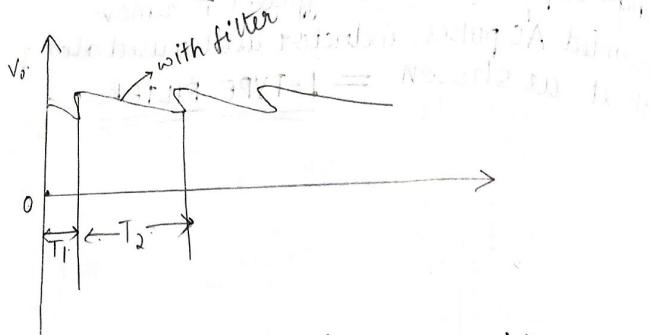
Π -TYPE FILTER:

Q: L-type filter is not sufficient to remove entire AC components then Π -filter can be used.



Q: Write the circuit diagram of full-wave rectifier connected with inductor filter



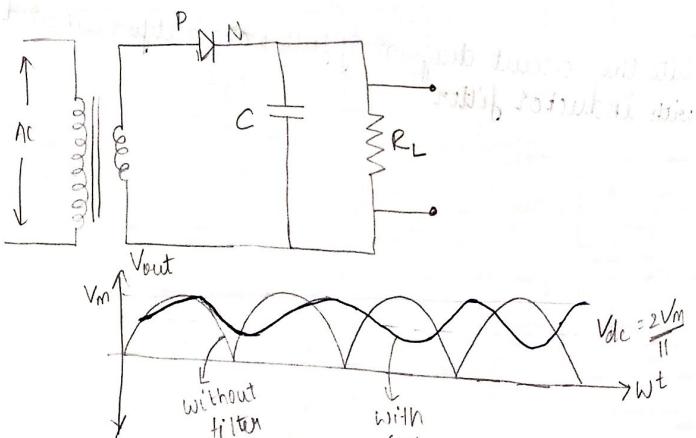


→ Capacitor charges through small resistance R_f hence charges faster (T_1) whereas it discharges through high resistance R_L hence discharges slowly. (T_2)

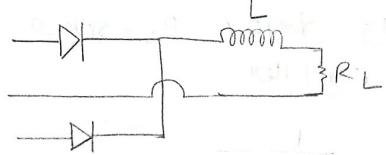
$$\text{hence } T_1 < T_2$$

$$\text{Because } T = RC$$

11/01/2022 Inductor Filter



instantly.



12/01/2022

* RIPPLE FACTOR:

$$\text{FWR: } \frac{\text{high load resistances}}{\text{low load resistances}} \rightarrow \text{L-Type} = \frac{1}{6\sqrt{2}\omega^2 LC}$$

→ capacitor filter

$$\rightarrow \text{LT-Type} = \frac{\sqrt{2}}{8\omega^3 C_1 C_2 R_L}$$

→ Inductor filter

$$f = \frac{RL}{3\sqrt{2}WL}$$

$f \rightarrow$ frequency of input signal.

$R_L \rightarrow$ Load resistance

$L \rightarrow$ Inductance

$\omega \rightarrow$ angular frequency.

$L \rightarrow$ Inductance.

* Capacitor filter is used for suitable for high load resistances & low load currents.

* Inductor filter is suitable for low load resistances & high load currents.

$$* f = 60 \text{ Hz} \quad \omega = 2\pi f = 377 \text{ rad/s} \quad R_L = 100 \Omega \quad \text{Inductor filter; } L = ? \quad \text{FWR: }$$

$$\omega = \frac{R_L}{3\sqrt{2}WL} = 377$$

$$L = \frac{4.1}{100} \times \frac{100 \times 100}{3\sqrt{2} \times 4(2\pi \times 60)} = \frac{10^4}{2030.4 \times \pi} = 1.56 \text{ H}$$

* $C = ?$ $f = 400\text{Hz}$ $\gamma = 10\%$ $R_L = 500\Omega$

FWR; Capacitor filter

$$C = \frac{1}{4\sqrt{3} f C R_L}$$

$$C = \frac{1}{4 \times 1.73 \times 400 \times 500 \times 10^3}$$

$$= \cancel{7.21 \times 10^{-6}}$$

$$= \cancel{7.21 \mu F}$$

$$= \frac{10^{-5} \mu F}{4 \times 1.73 \times 20} = \frac{10^{-5} \mu F}{138.4}$$

$$= 0.00722 \times 10^{-3}$$

$$= \underline{\underline{7.21 \times 10^{-6}}}$$

* Problems on Rectifier

1) HWR: $R_L = 1000\Omega$, $V_{AC} = 325V$, $r_f = 100\Omega$

$I_m = ?$ → Peak value

$I_{DC} = ?$ → avg value

$I_{rms} = ?$

$P_{DC} = ? = I_{DC}^2 R_L$

$P_{AC} = ? = I_{rms}^2 (r_f + R_L)$

$$\eta = \frac{P_{DC}}{P_{AC}}$$

$$I_{rms} = \frac{I_m}{\sqrt{2}} = \frac{3.14}{\sqrt{2}} = 0.093A$$

$$= \underline{\underline{0.1475A}}$$

~~$$P_{DC} = (0.1035)^2 \times 1000 = 10.7W$$

$$P_{AC} = (0.23)^2 (100) = 5.819W$$

$$\eta = \frac{10.7}{5.819} = 0.183$$~~

~~$$P_{DC} = I_{DC} R_L \approx 8.836W$$

$$P_{AC} = I_{rms}^2 (100) \approx 28.769W$$~~

~~$$\eta = \frac{P_{DC}}{P_{AC}} = 0.317 = 31.7\%$$~~

~~$$2) R_L = 3.5 \times 10^3 \Omega$$

$$V_m = 240V$$

$$r_f + r_s = 800\Omega$$

$$I_m, I_{DC}, I_{rms}, P_{DC}, P_{AC}, \eta$$~~

~~$$3) \text{HWR: } V_{DC} = 24V (R_L = 500\Omega)$$

$$r_f = 50\Omega$$

$$V_m = ?$$

$$V_{DC} = \frac{V_m}{\pi} = V_{DC} \rightarrow \text{only when diode resistance is not there}$$~~

~~$$I_{DC} = \frac{V_{DC}}{R_L} = \frac{24}{500} = 0.048A$$~~

~~$$I_m = \frac{0.048}{\sqrt{2}} = \frac{0.05}{\sqrt{2}} = 0.035A$$~~

~~$$V_m = I_m (R_L + r_f)$$~~

~~$$= 0.1507 (550) = 82.885V$$~~

$$2) R_L = 3.5 \times 10^3 \Omega \quad V_m = 240V$$

$$r_f + r_s = 800 \Omega$$

$$I_m = \frac{V_m}{R_L + r_f + r_s} = \frac{240}{3500 + 800} = \frac{240}{4300} = 0.0558A$$

$$I_{DC} = \frac{I_m}{\pi} = \frac{0.0558}{\pi} = 55.8mA$$

$$= \frac{55.8}{3.14} = 17.77mA$$

$$I_{rms} = \frac{I_m}{2} = 27.9mA$$

$$P_{DC} = I_{DC}^2 R_L$$

$$= 108.205 \times 105205.15 \times 10^{-6}$$

$$= 1.105W$$

$$P_{AC} = I_{rms}^2 (4300) = 3.34W$$

4) FWR:

$$\frac{N_1}{N_2} = \frac{20}{1} \quad V_p = 230V \quad f = 50Hz$$

V_{DC} , PIV; fout: all diodes are ideal $r_f = 0$

$$\frac{N_p}{N_s} = \frac{V_p}{V_s} = \frac{20}{1} = \frac{230}{V_s}$$

$$V_s = V_{rms} = 11.5V$$

$$\frac{2V_m}{\pi} = V_{DC}$$

$$V_{DC} = \frac{2 \times 36.21}{3.14}$$

$$V_{rms} = \frac{V_m}{\sqrt{2}}$$

$$V_m = 11.5 \times 1.41$$

$$= 16.21V$$

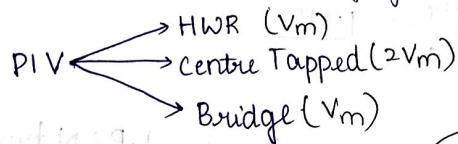
$$V_{DC} = 23V \quad \underline{\underline{V_{DC} = 10.32V}}$$

Time period is halved
frequency is doubled $\Rightarrow f = 2 \times 50 = 100Hz$

$$PIV = V_m (FWR) = 16.26V$$

Peak Inverse Voltage:

Maximum voltage can be applied to a diode in reverse bias without damaging it.



$$5) 10:1 \quad V_i = 220V \quad R_L = 500 \Omega \quad f_{in} = 50Hz$$

$$V_{DC} = ?, \quad I_{rms} = ?, \quad P_{DC} = ?, \quad P_{AC} = ?, \quad \eta$$

PIV; fout; FWR:

$$\frac{N_p}{N_s} = \frac{V_p}{V_s} = \frac{10}{1} = \frac{220}{V_{rms}}$$

$$V_{rms} = \frac{22V}{\pi}$$

$$V_m = V_{rms} \times \sqrt{2}$$

$$= 22 \times 1.41 = 31.02V$$

$$V_{DC} = \frac{2V_m}{\pi}$$

$$= 2 \times \frac{31.02}{3.14} = 19.75V$$

$$I_{rms} = \frac{I_m}{\sqrt{2}} = 31.02mA$$

$$P_{DC} = (I_{DC})^2 R_L$$

$$I_m = \frac{V_m}{R_L}$$

$$= \frac{31.02}{500}$$

$$= 62.04mA$$

$$PIV = V_m = 31.02V$$

$$f_{out} = 100Hz$$

$$I_{DC} = \frac{2Im}{\pi}$$

$$= \frac{2 \times 62.04}{3.14} = 39.5 \text{ mA}$$

$$P_{DC} = (I_{DC})^2 \times R_L = 780.125 \text{ mW} = 0.078 \text{ W}$$

$$P_{AC} = I_{rms}^2 (R_L)$$

$$= 481120.2 \times 10^{-6} \text{ W}$$

$$= 0.048 \text{ W}$$

$$\eta = \frac{P_{DC}}{P_{AC}} = 1.625$$

13/01/2022

* ZENER DIODE: Very heavily doped P & N types



3V Zener \rightarrow constant output voltage

Zener voltage \rightarrow specifically designed such that to produce constant output voltages (V_Z)

Zener diode acts as stabiliser/regulators.

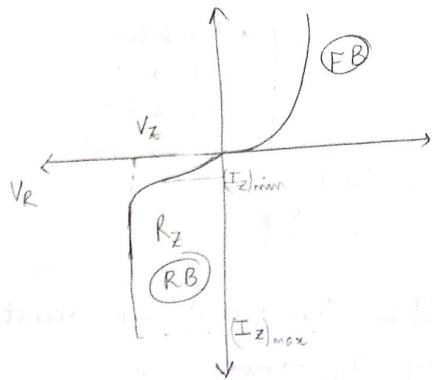
Zener breakdown voltage (V_Z) ranges from 3V to 300V meaning

If 100V Zener diode is used it provides 100V constant at its output

* Zener diode protects home appliances microwaves, washing machines etc. ~~and~~ from natural AC fluctuations (surge currents) current spikes).

Hence zener diode is used as a voltage regulator

* AC Line \rightarrow Zener Diode Regulator \rightarrow Load Resistance

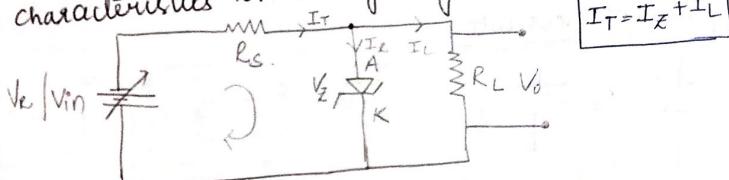


Line Regulation: Line is varying & load kept constant

load Regulation: Load is varying & line kept constant

LINE REGULATION:

Zener diode is always used in reverse bias characteristics for voltage regulation.



$V_{in} \geq V_Z$
LINE $\begin{cases} R_L = \text{const} \\ V_{in} \rightarrow \text{vary} \end{cases}$

Load $\begin{cases} R_L = \text{vary} \\ V_{in} = \text{const} \end{cases}$

17/01/2022

LINE REGULATION: Predicting R_L from AC fluctuations.

$V_{in} \rightarrow \text{vary} \Rightarrow R_L \rightarrow \text{const}; I_L \rightarrow \text{const}$

• $V_{in} \uparrow \text{ses}$

$I_T \uparrow \text{ses}$

$I_z \uparrow \text{ses}$

$V_z \rightarrow \text{const}$ (by virtue of its property)

• $V_{in} \downarrow \text{ses}$
 $I_T \downarrow \text{ses}$
 $I_z \downarrow \text{ses}$
 $V_z \rightarrow \text{const}$

LOAD REGULATION: $V_{in} \rightarrow \text{const} I_T \rightarrow \text{const}$

$R_L \rightarrow \text{vary} I_L \rightarrow \text{vary}$

• $R_L \uparrow \text{ses}$

$I_L \downarrow \text{ses}$

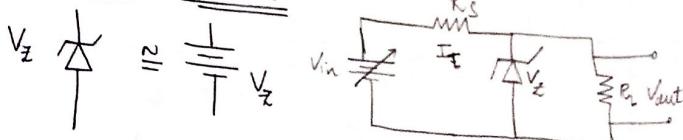
$I_z \uparrow \text{ses}$

$V_z \rightarrow \text{const}$

• $R_L \downarrow \text{ses}$
 $I_L \uparrow \text{ses}$
 $I_z \downarrow \text{ses}$ (I_z should not reduce beyond $I_z(\text{min})$)
 $V_z \rightarrow \text{const}$

I_z should not exceed $\uparrow \text{ses}$ above $I_z(\text{max})$

* Equivalent circuits:



$$V_{in} = I_f R_s + V_z$$

$$R_s = \frac{V_{in} - V_z}{I_f}$$

$$R_s = \frac{V_{in} - V_z}{I_z + I_L}$$

$$I_z = \frac{V_{in} - V_z}{R_s} - I_L$$

* Breakdown Mechanisms in Zener diode:

→ Zener breakdown

< GV

Sufficient to pull the charge carriers from VB ~~area~~; depletion layer is overcome.

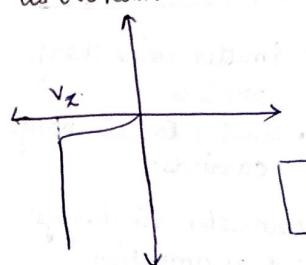
→ Avalanche Breakdown

> 6V

heavy current flows.

through, due to $\uparrow \text{ses}$ in velocity; collisions take place b/w atoms.

~~area~~ apart from pulling away charge carriers.



* Continuous, successive multiplication of formation of charge carriers due to external electric field & heavy doping is called AVALANCHE

Regulated DC Voltage Power Supply:

