# **Computer Organization Spring 2018**

## Lab 6: Cache Simulator

Due: 2018/6/30 23:59:59

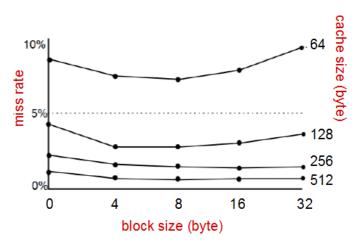
#### 1. Goal

Cache performance is important for system performance. In this lab, you are demanded to simulate cache behaviors by C/C++ style cache simulators. By this training, you will understand the performance difference between different cache architectures.

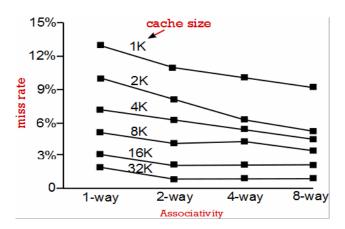
#### 2. Problem

In this problem, you have to implement an *n*-way set-associative cache simulator (LRU replacement policy). Inputting the file Trace.txt that is the memory trace from a benchmark to the simulator. We will supply the direct\_map\_cache.cpp file, you can refer to this and implement your program.

a. Fix the associativity on 1 (direct\_map\_cache), and then observe the difference when adapting the cache size and block size. Please draw a graph as the following example and describe the reason of rise and fall of the lines in the report.



b. Fix the block size on 32 (byte), and then observe the difference when adapting the cache size and associativity. Please draw a graph as the following example and describe the reason of rise and fall of the lines in the report.



## 3. Input and Output

### Input:

- a. memory trace file (Trace.txt)
- b. block size (16B to 256B)
- c. cache size (1KB up to256KB)
- d. associativity (from direct-mapped to fully associative)

#### Output:

- a. miss rate (%)
- b. Hits instructions
- c. Miss instructions

## 4. Execution Example

Input:

Trace1.txt (byte address)

0xbfa437cc

0xbfa437c8

0xbfa437c4

0xbfa437c0

0xbfa437bc

0xbfa437b8

0xbfa437b8

0xbfa43794

0xb8088ea8

0xb8088eac

Cache size=1024 (byte)

Block size=32 (byte)

Output:

Hits instructions: 2,3,4,6,7,10

Misses instructions: 1,5,8,9

Miss rate: 40%

### Associativity=2

You can use this simple testcase to check the correctness of your program!

#### 5. Demand

- a. Please implement this Lab in C/C++ language.
- b. One person form a group.
- c. Please submit your file to E3.
- d. Please compress your report and the code into one single file. The file should be named as: student\_ID.zip (Format must be correct or you will get some penalty)

#### 6. Grade

- a. Total: 100 points, (program 80%, report 20%)
- b. No delay submission is allowed.
- c. Copy (or Copy+Modify) will get 0 point!
- d. Score of this Lab is a reference to bias the final score of this course.

#### 7. Hand in

Put in one single file: ( student\_ID.zip)

- a. code (.cpp/.h)
- b. report (.word)
- c. test file (Trace.txt)