

# KUAN-YU CHANG

## VERIFICATION IP DESIGN ENGINEER

Avery Design Systems, Inc.  
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## PROFESSIONAL EXPERIENCES

Verification IP Design Engineer  
PCI Express upper layer group  
Avery Design Systems, Inc., Taiwan  
Jul. 2021 - present



## EDUCATION

National Tsing Hua University, Hsinchu, Taiwan

Advisor: Prof. Chun-Yi Lee

- M.S. in Computer Science – Jun. 2021
  - Thesis Topic: *Mapping Nearest Neighbor Compliant Quantum Circuits onto a 2-D Hexagonal Architecture* [NDLTD]
- B.S. in Interdisciplinary Program of Science – Jun. 2017
  - Double expertise in *Computer Science* and *Chemistry*



## PUBLICATIONS

- [1] **K.-Y. Chang** and C.-Y. Lee, "Mapping nearest neighbor compliant quantum circuits onto a 2-d hexagonal architecture," *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, pp. 1–1, 2021. DOI: 10.1109/TCAD.2021.3127868.
- [2] Y.-M. Chen, **K.-Y. Chang**, C. Liu, T.-C. Hsiao, Z.-W. Hong, and C.-Y. Lee, "Toward synergism in macro action ensembles," *ICML Workshop on Automated Machine Learning*, 2020. [pdf] [poster]
- [3] Y.-H. Chang, **K.-Y. Chang**, H. Kuo, and C.-Y. Lee, "Reusability and transferability of macro actions for reinforcement learning," *NeurIPS Workshop on Deep Reinforcement Learning*, 2020. [pdf] [video]

## TECHNICAL SKILLS

Programming Languages  
Software & Tools  
Operating Systems

C/C++ , SystemVerilog, Verilog, Bash, Perl, Python  
Docker, LaTeX, MS Office  
Ubuntu, CentOS, macOS, MS Windows

## REFERENCES

**Professor Chun-Yi Lee**  
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**Professor Chia-Chun Chou**  
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