# **KUAN-YU CHANG**

## VERIFICATION IP DESIGN ENGINEER

Avery Design Systems, Inc. No. 76, Section 1, Zhongxiao E Rd Zhongzheng Dist., Taipei City, Taiwan

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### PROFESSIONAL EXPERIENCES

#### **Verification IP Design Engineer**

PCI Express upper layer group Avery Design Systems, Inc., Taiwan Jul. 2021 - present



#### **EDUCATION**

#### National Tsing Hua University, Hsinchu, Taiwan

#### Advisor: Prof. Chun-Yi Lee

- M.S. in Computer Science Jun. 2021
  - Thesis Topic: Mapping Nearest Neighbor Compliant Quantum Circuits onto a 2-D Hexagonal Architecture
- B.S. in Interdisciplinary Program of Science Jun. 2017
  - Double expertise in Computer Science and Chemistry

#### **PUBLICATIONS**

- [1] Y.-M. Chen, K.-Y. Chang, C. Liu, T.-C. Hsiao, Z.-W. Hong, and C.-Y. Lee, "Toward synergism in macro action ensembles," 2020.
- [2] Y.-H. Chang, K.-Y. Chang, H. Kuo, and C.-Y. Lee, "Reusability and transferability of macro actions for reinforcement learning," *arXiv preprint arXiv:1908.01478*, 2019.

#### TECHNICAL SKILLS

**Programming Languages** 

Software & Tools
Operating Systems

C/C++, Bash, Perl, SystemVerilog
Microsoft Office, MEX
Ubuntu, CentOS, macOS

#### REFERENCES

Professor Chun-Yi Lee

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**Professor Chia-Chun Chou** 

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