

# **Lab Material**

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# **AGENDA**

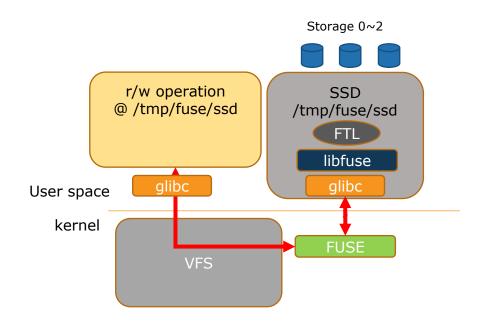
- 1. FUSE SSD overview
- 2. FUSE SSD API overview
- 3. LAB
- 4. DEMO
- 5. NOTE





## **FUSE SSD Overview**

- Learning Flash Transaction Layer (FTL) algorithm in SSD device by FUSE kernel function
- Background
  - Real storage is NAND 0~NAND X
    - It always read/write 512B data
    - · If data is less than 512B, it still stores 512B
    - If data is larger than 512B, it shall store at different NAND
    - Each NAND X is 1MB
  - FUSE\_SSD will represent as FS (/tmp/fuse/SSD)
    - /tmp/fuse/SSD is a file
    - /tmp/fuse/SSD max size = total NAND X size
    - FUSE\_SSD will split data into 512B chunk size and store at different NANDs
- LBA requirement
  - Handle Logical and Physical address mapping
  - #Is will show logical size (512B align) not physical size
    - EX.
      - User write 256B offset\_0 10 times
      - Logical Shall be size 512B
      - Physical shall be 512B\*10

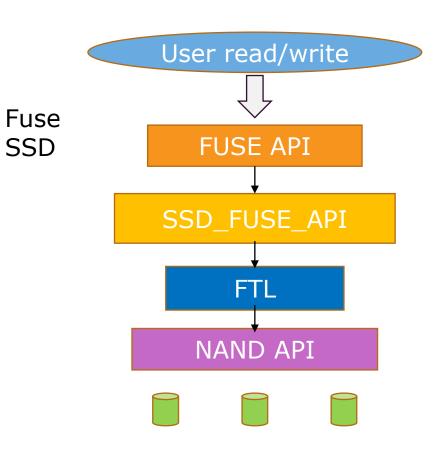






## **FUSE SSD API Overview**

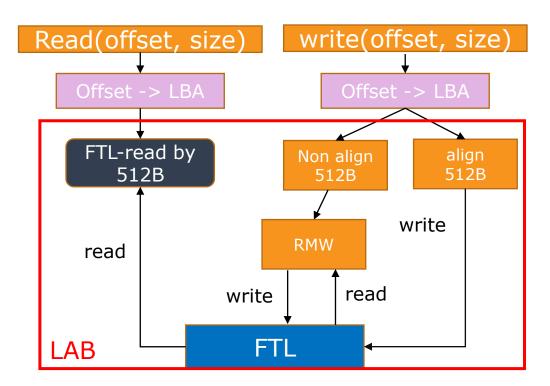
- Read/write in userspace will direct to Fuse SSD
- SSD\_FUSE\_API (Phison provide)
  - Change data offset into Logical Block Address (LBA)
  - LBA is 512B unit
- FTL
  - Handle LBA to Physical Cluster Address (PCA)
  - PCA is 512B unit
- NAND API (Phison provide)
  - Handle storage read/write
  - Storage is aligned to 512B



## **SSD Fuse API**

- Read path(src\_buffer, offset, size)
  - Change offset into LBA (512B)
  - Divide read cmd into 512B package by size
  - Send FTL-read API by 512B package
    - Allocate buffer for FTL
  - Copy data into src\_buffer
- Write path (src\_buffer, offset, size)
  - Change offset into LBA
  - Divide write cmd into 512B package by size
    - If not aligned to 512B: do Read modify write operation (RMW)
    - If aligned to 512B: send FTL-write API by LBA

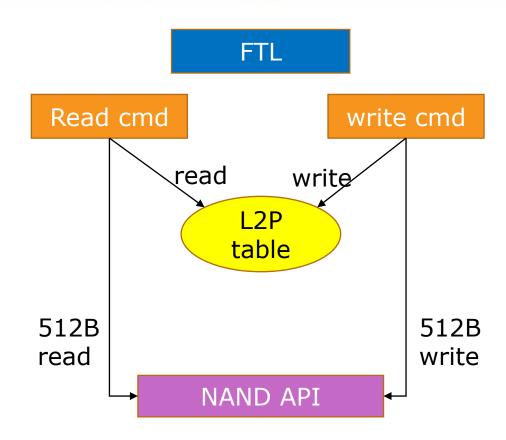
SSD\_FUSE\_API





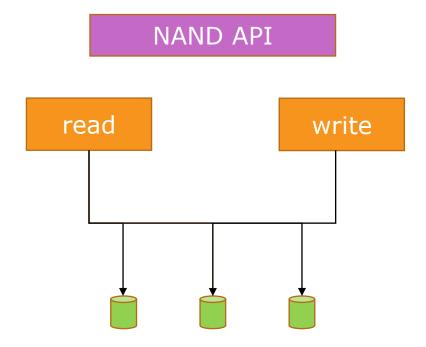
## **SSD Fuse API**

- Read path (tmp\_buffer, LBA)
  - Check LBA to PCA (L2P) to get true storage address
  - Send NAND-read cmd
    - Read data into tmp\_buffer
- Write path (src\_buffer, LBA)
  - Allocate a new PCA address
  - Send NAND-write cmd
  - Update L2P table
- Get logical size
  - Return current logical size
- Get physical size
  - Return current physical size



# **SSD Fuse API**

- Read cmd (PCA, buffer)
  - Read data from storage by PCA into buffer
- Write cmd (PCA, buffer)
  - Write data into storage by PCA from buffer

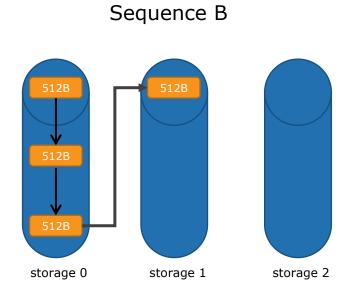


# **Example**

- User space writes a 2KB data
- FUSE\_SSD will split data into 512B chunk size

Sequence A

512B





- Fuse ssd: ssd\_fuse.c
- Common header: ssd\_fuse\_header.h
- Dut: ssd\_fuse\_dut.c
- Package:
  - apt-cache search fuse sudo apt-get update sudo apt-get install fuse3 sudo apt-get install libfuse3-dev
  - reboot

- Modify NAND\_LOCATION to file location
- Compile ssd\_fuse.c/ssd\_fuse\_dut.c
- #gcc -Wall ssd\_fuse.c `pkg-config fuse3 -cflags --libs` -D\_FILE\_OFFSET\_BITS=64 -o ssd\_fuse
- #gcc -Wall ssd\_fuse\_dut.c -o ssd\_fuse\_dut

```
ssd_fuse_header.h
                                                           Save
                         sf ssd fuse golden/media/sf ssd fuse golden
    FUSE-ioctl: ioctl support for FUSE
    Copyright (C) 2008
                               SUSE Linux Products GmbH
                               Tejun Heo < teheo@suse.de>
    Copyright (C) 2008
    This program can be distributed under the terms of the GNU GPLv2.
    See the file COPYING.
 8 #include <svs/tvpes.h>
 9 #include <sys/uio.h>
10 #include <sys/ioctl.h>
11 #define NAND NUM (10)
12 #define NAND SIZE MB (1)
13 #define INVALID PCA
                            (0xFFFFFFFF)
14 #define FULL PCA
15 #define NAND LOCATION
                          "/home/zhi/Desktop/ssd fuse"
```

```
zhi@zhi-VirtualBox:~/Desktop/ssd_fuse$ ./make_ssd
zhi@zhi-VirtualBox:~/Desktop/ssd_fuse$
```

- Start SSD fuse lib with debug mode enable
- Create dir by #mkdir /tmp/ssd
- Mount at /tmp/ssd
- #./ssd\_fuse -d /tmp/ssd

```
pi@raspberrypi:~/windows_share $ ./ssd_fuse -d /tmp/ssd
FUSE library version: 3.10.3
nullpath ok: 0
unique: 2, opcode: INIT (26), nodeid: 0, insize: 56, pid: 0
INIT: 7.32
flags=0x03fffffb
max readahead=0x00020000
  INIT: 7.31
  flags=0x0040f039
  max readahead=0x00020000
  max write=0x00100000
  max_background=0
  congestion_threshold=0
  time_gran=1
  unique: 2, success, outsize: 80
unique: 4, opcode: ACCESS (34), nodeid: 1, insize: 48, pid: 855
  unique: 4, error: -38 (Function not implemented), outsize: 16
unique: 6, opcode: LOOKUP (1), nodeid: 1, insize: 47, pid: 855
LOOKUP /. Trash
getattr[NULL] /.Trash
  unique: 6, error: -2 (No such file or directory), outsize: 16
unique: 8, opcode: LOOKUP (1), nodeid: 1, insize: 52, pid: 855
LOOKUP /. Trash-1000
getattr[NULL] /.Trash-1000
  unique: 8, error: -2 (No such file or directory), outsize: 16
```

- We can ls /tmp/ssd/ssd\_file to get file details
- Can write a data to it (echo)
- Will notify data size is increased
- 0 -> 12

```
pi@raspberrypi:~/windows_share $ ls /tmp/ssd/ssd_file
/tmp/ssd/ssd_file
pi@raspberrypi:~/windows_share $ ls -al /tmp/ssd/ssd_file
-rw-r--r-- 1 pi pi 0 Feb 11 13:57 /tmp/ssd/ssd_file
pi@raspberrypi:~/windows_share $ echo "hello world" > /tmp/ssd/ssd_file
pi@raspberrypi:~/windows_share $ ls -al /tmp/ssd/ssd_file
-rw-r--r-- 1 pi pi 12 Feb 11 13:59 /tmp/ssd/ssd_file
pi@raspberrypi:~/windows_share $
```

- In last page, we write "hello world" to ssd
- The fuse ssd will print out it allocate a pca to store data

```
etattr[NULL] /ssd_file
   NODEID: 2
   unique: 42, success, outsize: 144
 ınique: 44, opcode: OPEN (14), nodeid: 2, insize: 48, pid: 13747
open flags: 0x20201 /ssd_file
   open[0] flags: 0x20201 /ssd_file
   unique: 44, success, outsize: 32
unique: 46, opcode: FLUSH (25), nodeid: 2, insize: 64, pid: 13747
   unique: 46, error: -38 (Function not implemented), outsize: 16
unique: 48. opcode: WRITE (16), nodeid: 2. insize: 92, pid: 13747
write[0] 12 bytes to 0 flags: 0x20001
ssd wirte lba 0, range 1
ssd do write non align idx 0, size 12
gen first pca 0
nand write 0 pca pass
ftl update l2p lba=0, pca=0
ssd write return size = 12
  write[0] 12 bytes to 0
  unique: 48, success, outsize: 24
unique: 50, opcode: RELEASE (18), nodeid: 2, insize: 64, pid: 0
   unique: 50, success, outsize: 16
unique: 52, opcode: LOOKUP (1), nodeid: 1, insize: 49, pid: 17339
LOOKUP /ssd file
getattr[NULL] /ssd file
   NODEID: 2
   unique: 52, success, outsize: 144
```

- Although logical size is 12B, the physical size is actually 512B.
- We can get this info by DUT
  - #./ssd\_fuse\_dut /tmp/ssd/ssd\_file l
  - Return logical size
  - #./ssd\_fuse\_dut /tmp/ssd/ssd\_file p
  - Return physical size (512B unit)

- User can use C to read/write ssd\_file or use vi to modify ssd\_file
- But can "not" use geany (GUI text editor)

```
pi@raspberrypi:~/windows_share $ vi /tmp/ssd/ssd_file
pi@raspberrypi:~/windows_share $ cat /tmp/ssd/ssd_file
Fthis from vi terminal
hello world
pi@raspberrypi:~/windows_share $
```



# **Lab Challenge**

#### Tasks:

- Complete basic write/read function
- Modify writing sequence A to sequence B
- Implement garbage collection

#### Target:

- Keep WAF as minimum as possible
- Compare data pass



# Lab Challenge

- ftl\_write (left blank):
  - Use get\_next\_pca to find empty PCA for data writing
  - Write data from buffer to PCA
  - Update L2P table
- ssd\_do\_write (left blank):
  - Divide write cmd into 512B package by size
  - Use ftl\_write to write data
    - Need to handle writing non-aligned data

# Lab Challenge

- get\_next\_pca (need modify):
  - Change next pca address to next page instead of next NAND
- ftl\_gc (left blank):
  - Decide the source block to be erased
  - Move all the valid data that in source block to another block
  - Update L2P table
  - Erase the source block with invalid data





# Grouping

Form a group of 5 students (by 11/20)



https://docs.google.com/spreadsheets/d/1uTsyhalGny4-El7uV6TsOFYlBW\_iXVEkpsUcIIZ-rzs/edit?usp=sharing

## **Next Action**

- Contact mentors for Q&A
  - <u>haozhi lee@phison.com</u>
  - vincent wang@phison.com
- Prepare PPT to present algorithm on 12/11
- Complete lab challenge and provide source code to mentors on 12/11, before 9am
- Phison will generate host behaviors for testing
- Group with the most efficient GC algorithm (smallest WAF) can win prizes





# **THANK YOU!**

