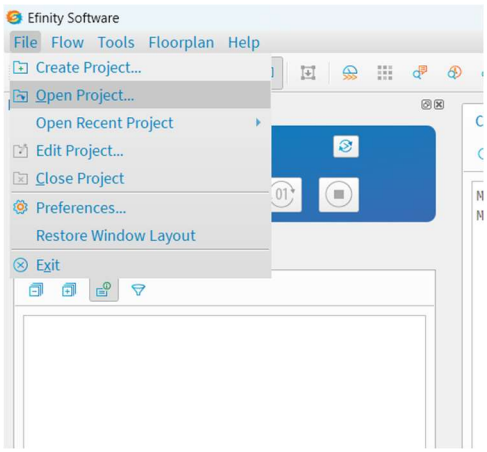
Efinix Design Flow

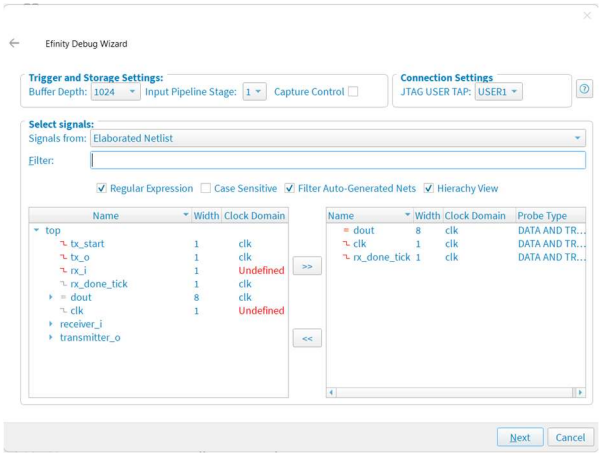
Steps

1. Start the Efinix if necessary and open the lab2 project (lab2.xml) you created in the previous lab using the File>Open Project.



Efinity Debug Wizard

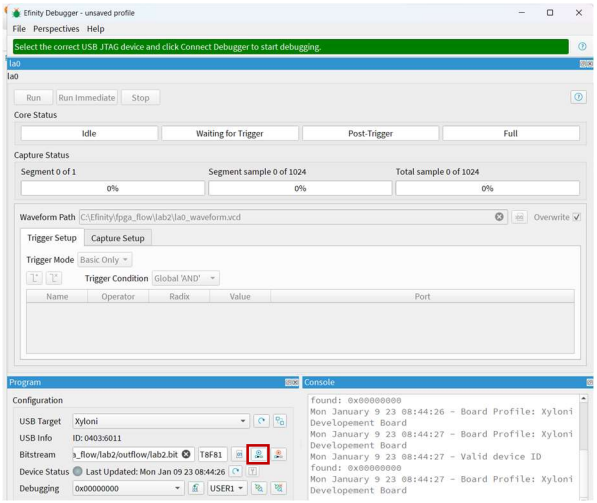
1. Click the Efinity Debug Wizard then select dout, clk, rx_done_tick.
2. Select clock domain as clk. Click Next. Click Finish.



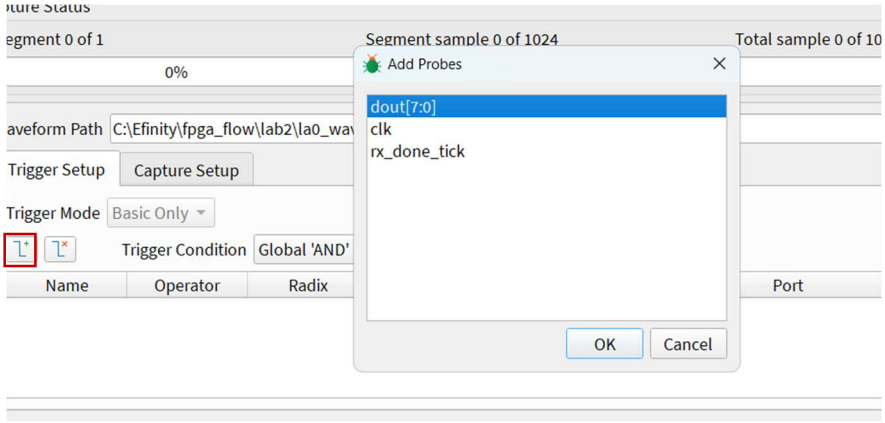
Open Efinity Debugger

1. Click the Open Debugger.

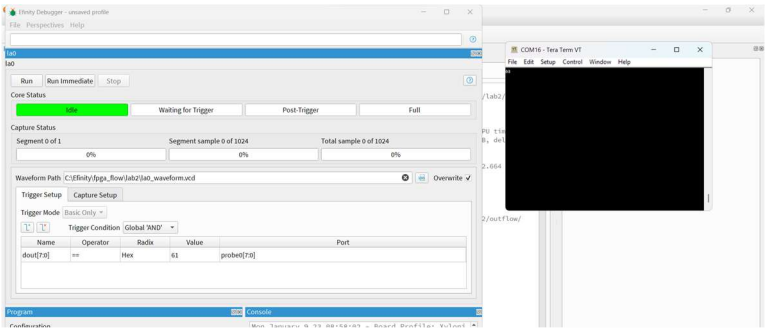
2. Select your target board then upload your .bit file for program the board.



3. Click Add Probes button then select dout[7:0] and click ok.

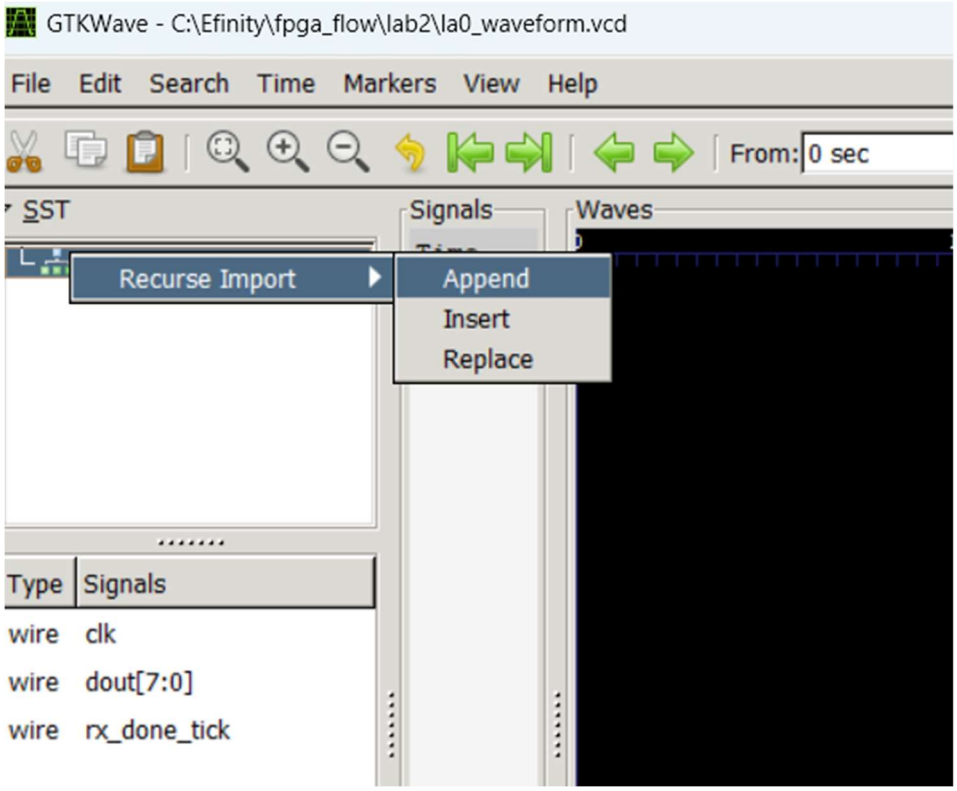


4. Set Radix as Hex and value as 61 which equal to char “a”.
5. Open Tera Term and set baud rate and com port.
6. Enter character “a” then click Run.



Analyze Waveform of Debug in GTKWave

1. Open the workspace folder of lab2 then open .vcd file.
2. Right click on the top and click Rescurse Import>Append.



3. Analyze the waves of dout[7:0].