

Experiment - 2

★ Aim :- Realization of all basic gates using NAND gate.

★ Apparatus :- Bread board, NAND gate (IC-7400), wires.

★ Theory :- A NAND gate is formed by an AND gate followed by a NOT gate. The output of AND gate becomes the input of NOT gate and the output of NOT gate becomes final output.

A NAND is one of the two universal gate. It is called a universal gate because any other gate can be verified with help of NAND gate.

NOT gate :- $Y = (AA)' = A'$

AND gate :- $Y = (\overline{A+B})' = A \cdot B$

OR gate :- $Y = (\overline{A \cdot B})' = A + B$

NOR gate :- $Y = \overline{A+B}$

★ Procedure :-

1. Connect the transistor to the power.
2. Connect one NAND gate for any of one logic function realized.
3. Connect the input of first gate to logic source and output of last gate to logic indicator.
4. Apply logics and various inputs and observe output for each one.
5. Verify the truth table for each input combinations.
6. Repeat the process for all basic logic functions.
7. Switch off the power supply.

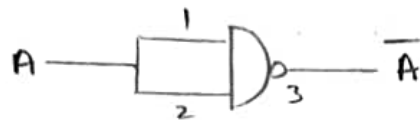
★ Result :- We implemented and verified logic gates \rightarrow AND, OR, NOR,

Teacher's Signature _____

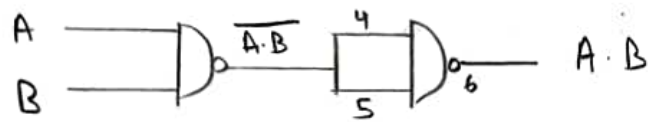
EX-NOR, NOT gates using NAND gate.

★ Discussion :- NAND gate is known as universal gate. since any of the basic gates or any boolean expression can be derived from them. Further practical apps include burglar alarm, freezer warning, ~~bo~~ buzzer, automatic watering system etc.

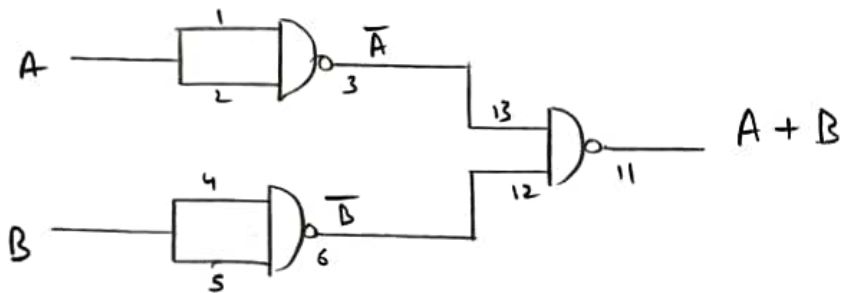
★ NOT Gate :-



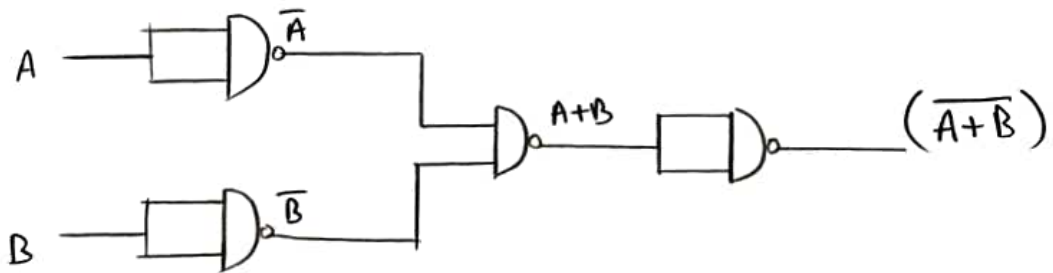
★ AND Gate :-



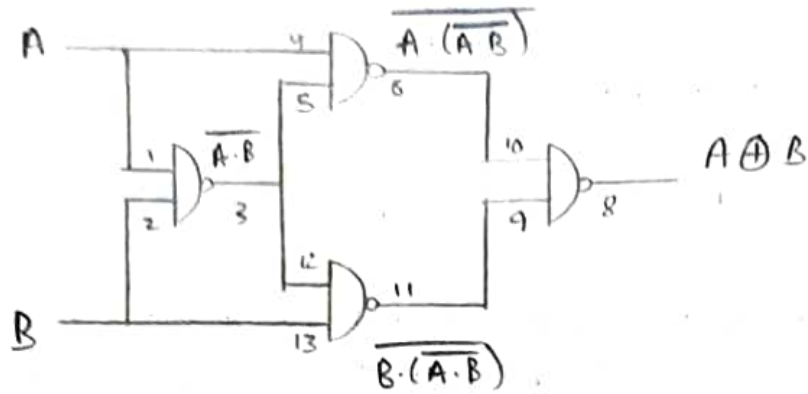
★ OR Gate :-



★ NOR Gate :-



* XOR Gate :-



* XNOR Gate :-

