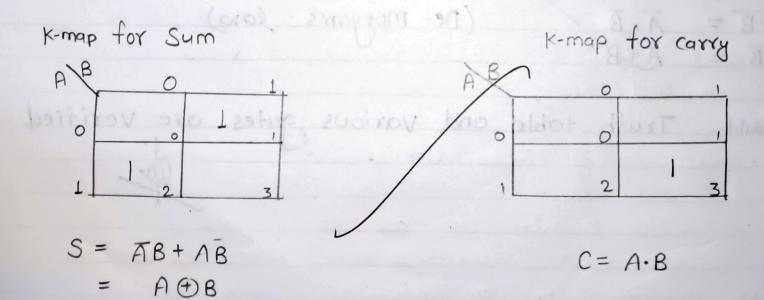


Truth table of half adder

	Input		Output	
	A	Basi	Sino	C
	0	0	0	0
	0	1.		0/
	ľ	(0)	ibutive	61
	1	1	0/	
-1				



(commitative law).

()+8)+A

N(B+1) = AB+AC

A = BAIA

-	Experiment 2(A)  DATE PAGE_4_
	Aim: To verify the operation of half adder.
	Apparatus: Bread Board, connecting wires, power supply, IC 7408 and IC 7486.
	Control of the contro
-	Theory: Half adder can add two bit individually output of half adder has sum bit and carry bit for sum
	Sum is higher when (01 and 10) combination exist. Therefore  Sum = AB + AB
	Boolean expression sym = ATB
	E. Const.
	Carry is high only at the combination of  A=1 and B=1
	$Coyt = A \cdot B$
	Holf adder has output = Sum + Cout

= ADB + A.B

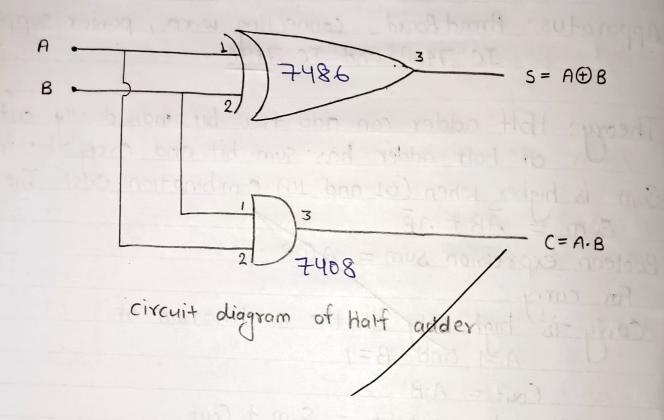
Procedure:

- Tonstruct the circuit as given in the logic diagram.

  Tonsert the correct Ic's on the bread board

  To Grive Vcc and ground to all Ic's

  Verify the output by truth table

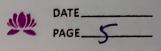


LAGGECTIAC;

(A) & tramina

the consect Ich on the based board

side that to traffic sit



-	
	Result: observation of half adder has been verified.
	3/6/1
	The state of the s
	Precautions!
$\rightarrow$	Insert the IC's carefully in the bread. Doory harrow
al six	damaging the pins.
**	Precautions:  Insert the IC's correfully in the bread board without damaging the pins.  Sheitch off the bread board when not in use
***	Specifical desired and the specifical desired an
	Win the second s
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	AND SALES OF THE PROPERTY OF T