

# Release Note for SMDK2416 (Windows Embedded CE 5.0)

\$3C2416 Sep 11, 2008 (Preliminary) REV 0.5

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# S3C2450 RISC Microprocessor Release Note

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# **Revision History**

Revision No	Description of Change	Refer to	Author(s)	Date
0.1	Preliminary draft	-	SangHee Kim	2007-06-11
0.2	Preliminary draft	-	TS Team	2008-07-11
0.3	Preliminary draft	-	TS Team	2008-07-14
0.4	Preliminary draft	-	Hyuk Lee	2008-08-04
0.5	Preliminary draft	-	Hyuk Lee	2008-09-11

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## 1 SMDK2416\_WinCE50\_PM\_REL\_0.06\_080911

Updated at Sep 11, 2008

#### What's New?

# Fixes made in this update

• Code changed for supporting 4KB NAND page.

#### **Known Issues**



### 2 SMDK2416\_WinCE50\_PM\_REL\_0.05\_080804

Updated at Aug 4, 2008

#### What's New?

# Fixes made in this update

1. IDLE Mode bug is fixed.

#### **Known Issues**

#### 3 SMDK2416\_WinCE50\_PM\_REL\_0.04\_080714

Updated at July 14, 2008

#### What's New?

# Fixes made in this update

- 2. In previous version, Sleep and WakUp function in Eboot is not Working Well in IROM booting Mode. This bug was fixed.
- 3. SDMMC D/D has been Fixed the problem on missing the Transfer Complete Interrupt.

#### **Known Issues**



#### 4 SMDK2416\_WinCE50\_PM\_REL\_0.03\_080711

Updated at July 11, 2008

#### What's New?

- 4. The function to disable FASTPATH is added. FASTPATH is enabled basically.
- 5. Sleep fuction in Eboot is Added

#### Fixes made in this update

- 1. Write Status Clear Async Mode on HSMMC has been enabled.
- 1.1. This fix can make async-clear enable about Normal and Error interrupt status bit.
- 1.2. Because exception handling on status bits is not needed, the performance of HSMMC ISR has been increased.
- 2. Inserted retention release codes in off.c.
  - 2.1. In previous version, IROM has these codes, but now IROM removes that codes.
  - 2.2. So, retention release codes are inserted in off.c
- 3. The Interrupt handler of SDMMC driver use "Normal Interrupt Status Register" instead of "Slot Interrupt Status Register", because "Slot Interrupt Status Register" was not applied correctly to the SDMMC controller.

#### **Known Issues**



# 5 SMDK2416\_WinCE50\_PM\_REL\_0.01\_BETA\_080611 BSP Release

Updated at June 11, 2008

#### What's New?

- 1. Beta version released.
- Bootloader, Touch, Display, AC97, NandStorage, PowerSave(IdleMode) is working.
- HSMMC, IIC, HS-SPI, IrDA, Serial, Post Processor, USB Device, Usb Host, PowerButton driver is working.
- Audio(IIS), SDIO,2D,CFCon, USB KITL, Sleep, DVS function is working.
- 2. IROM booting is working.

#### Fixes made in this update

Nothing

#### **Known Issues**

