

Descriptions for the circuit:

1) Page 2: PHY IP101GR & Mode setting.

2) Page 3: Transformer.

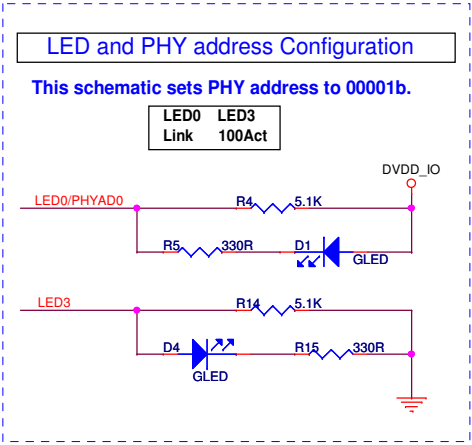
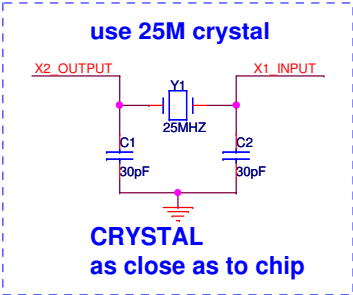
Date	Description
2011/09/15	Initial Release



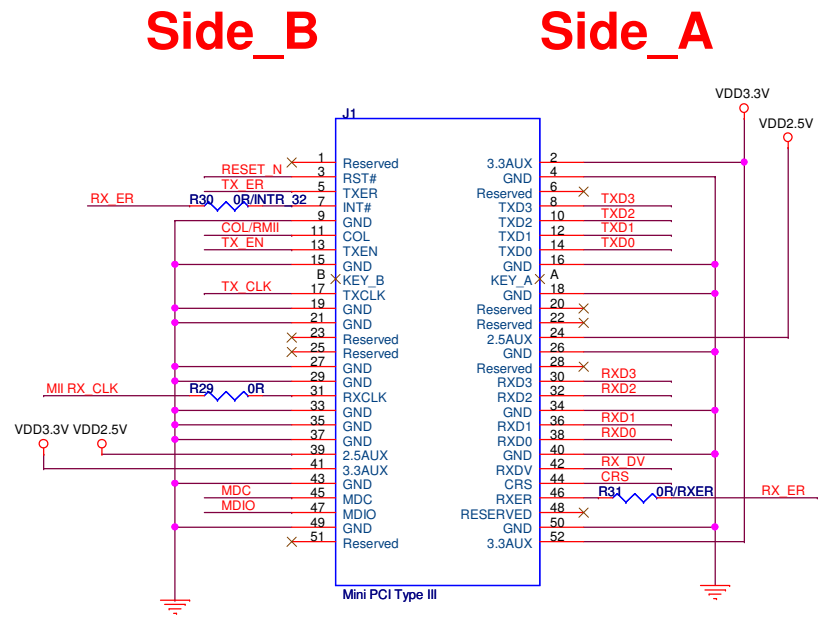
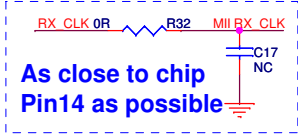
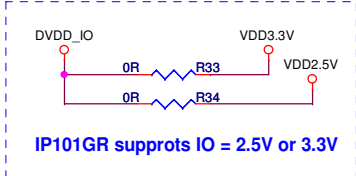
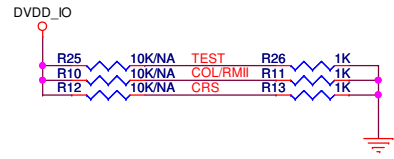
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Title **IP101GR Demo board**

Size A	Document Number <b>Note</b>	Rev V01
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	Pull high	Pull down
TEST	TEST mode	Normal mode
COL/RMII	RMII mode	MII mode
CRS	LED mode1	LED mode0



pin21 RXER/INTR_32			
	R3	R30	R30
RXER	NA	NA	0R
INTR32 low active	5.1K	0R	NA
INTR32 high active	NA	0R	NA

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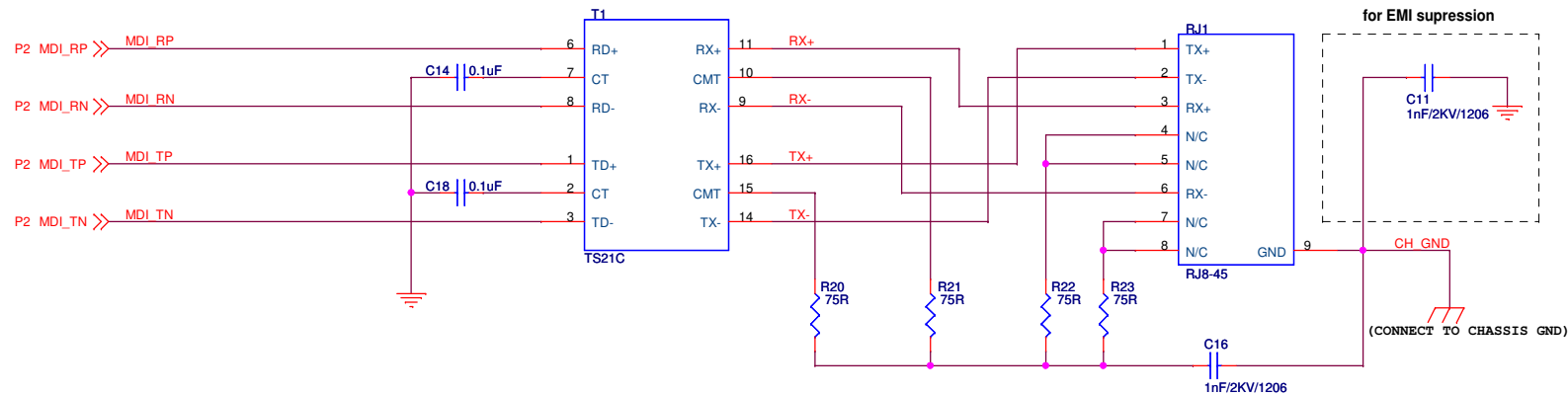
**Title**  
**IP101GR Demo board**

**Size**  
**B**

**Document Number**  
**IP101GR**

**Date:** Wednesday, June 06, 2012
 **Sheet** 2 **of** 3

**Rev**  
**V01**



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Title **IP101GR Demo board**

Size B	Document Number <b>Transformer</b>	Rev V01
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