

Kunj Hiteshbhai Trivedi

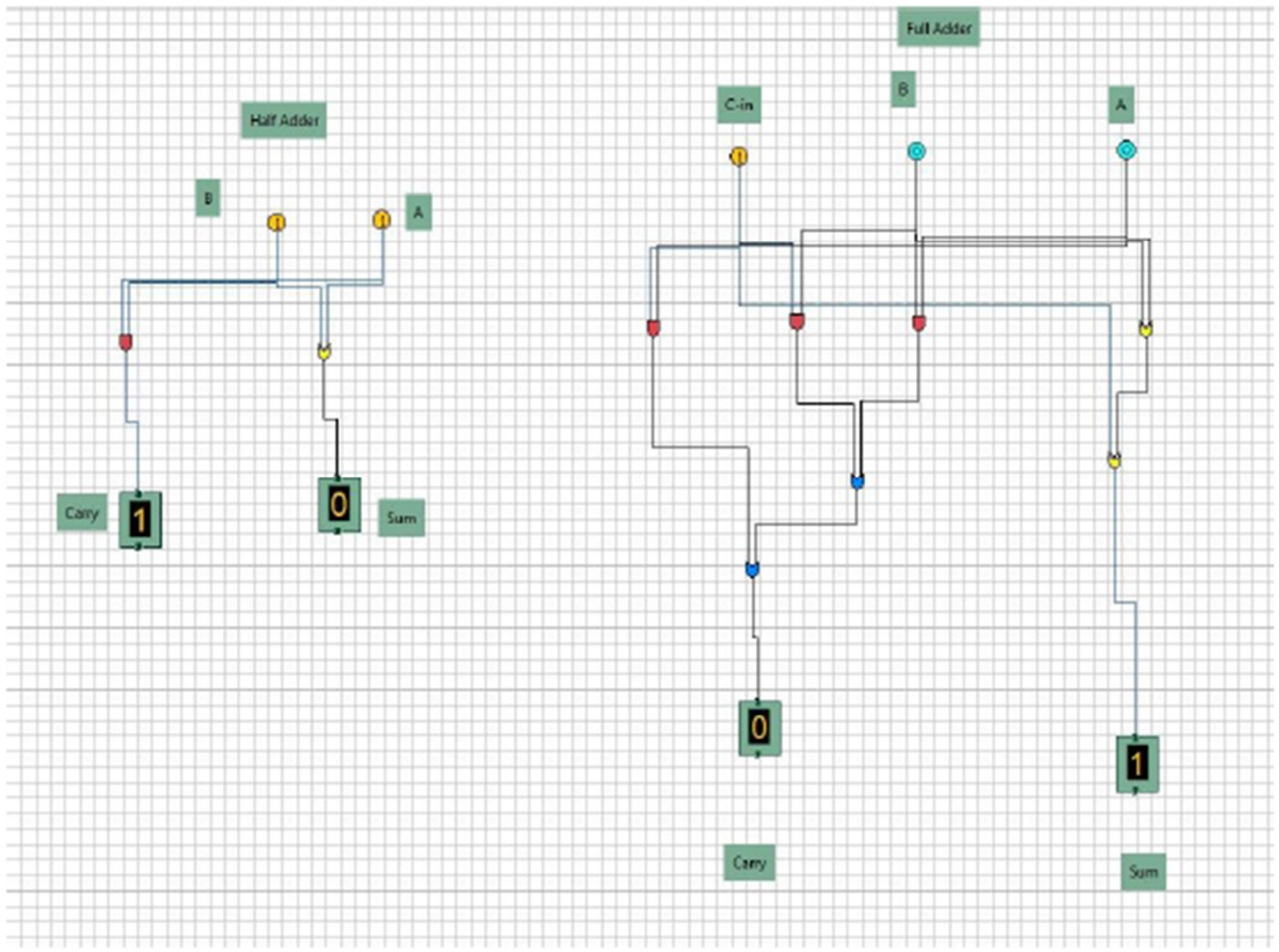
SE-9 Roll no.:38

Kunj Hiteshbhai Trivedi

Experiment No.:02

Half Adder And Full Adder

OUTPUT:



Half Adder Pretest :

Pre Test

1) Which of the following Gates is not used in the design statement



- ☐ AND
- ☐ OR
- ☐ NOR
- ☒ NAND

2) How many ICs are needed for the connection ?



- ☐ 1
- ☒ 2
- ☐ 3
- ☐ NONE

3) When one of the input switch value is 1 and the other input switch value is 0 what will be the output ?



- ☐ 0
- ☒ 1
- ☐ Both
- ☐ NONE

4) Which connection is mandatory for the IC ?



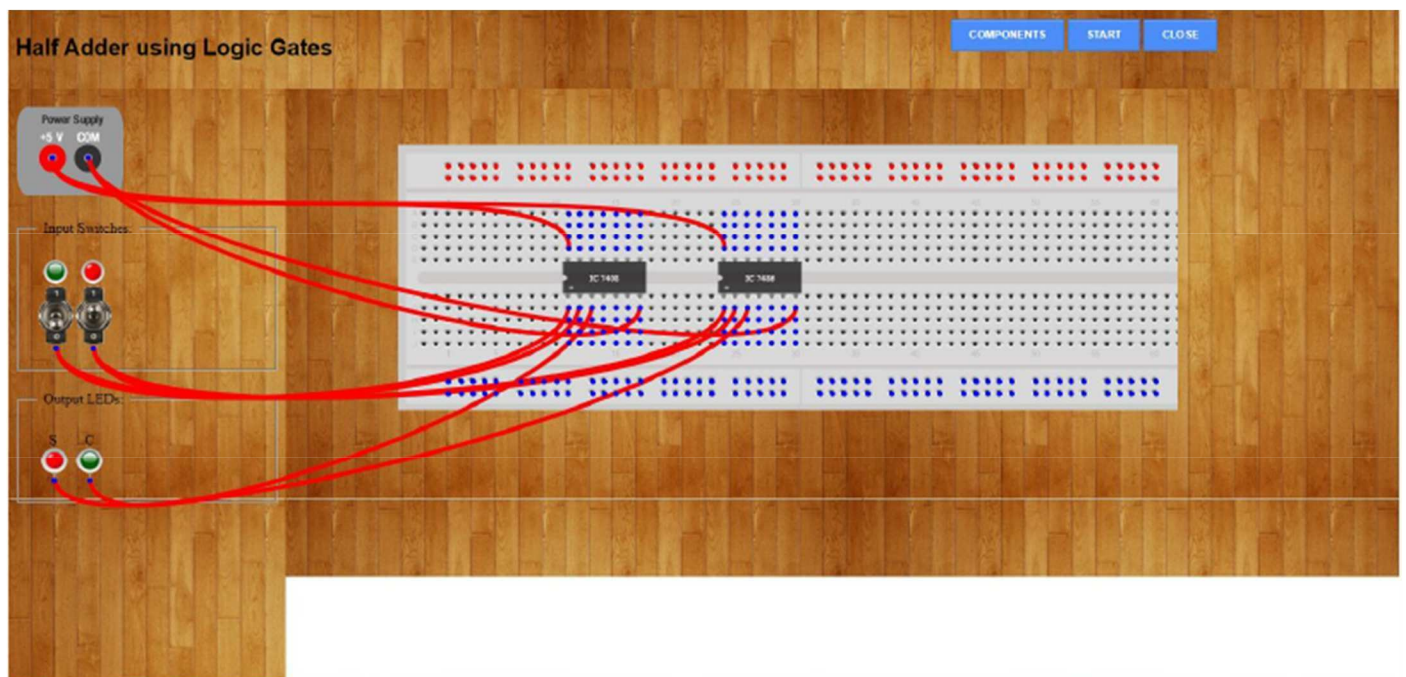
- ☐ VCC
- ☐ Ground
- ☒ Both
- ☐ NONE

5) Why do you use half adder?



- ☐ To count the no. of bits changes
- ☒ To Sum values
- ☐ Error correction
- ☐ None of the Mentioned

Half Adder Breadboard :



Half Adder Post-test :

Post Test

1) Once you land on the simulator page, what is the default condition of the switch



- ☐ On
- ☒ Off
- ☐ Both On
- ☐ Both Off

2) How to place the IC ?



- ☒ By drag to the breadboard
- ☐ Clicking on the ICs
- ☐ By right click and add
- ☐ None

3) What is the purpose of the VCC ?



- ☒ Power supply
- ☐ Ground connection
- ☐ Both
- ☐ None

4) Which logic gates are implemented in the ICs ?



- ☐ OR
- ☐ NOR
- ☐ NOT
- ☒ NONE

5) What are the IC needed for the experiments ?



- ☐ IC 7408
- ☐ IC 7486
- ☒ Both
- ☐ None