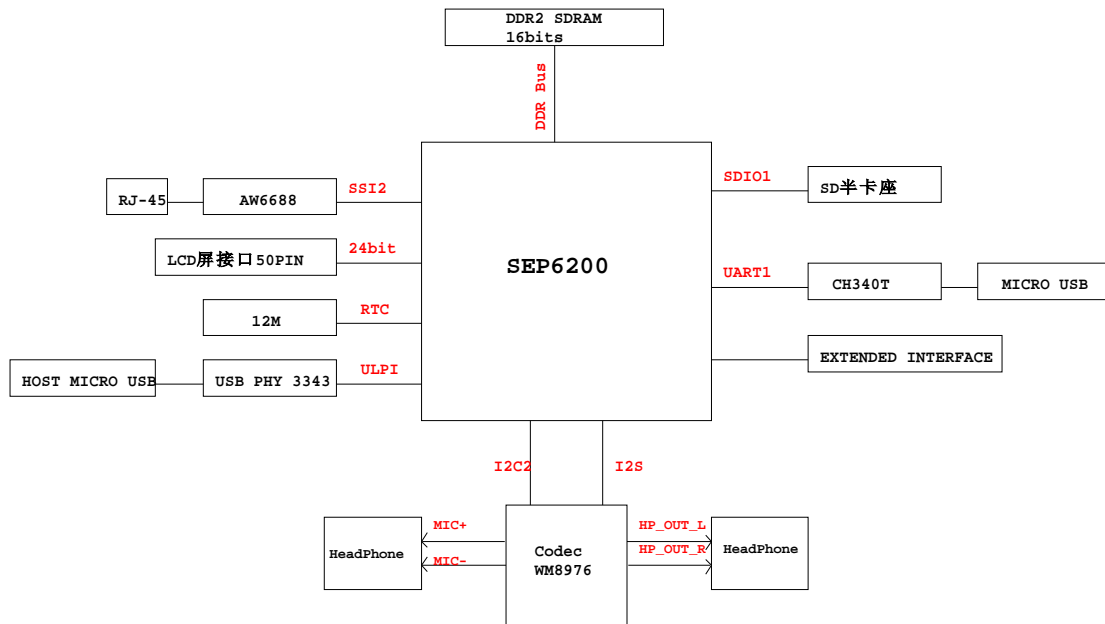


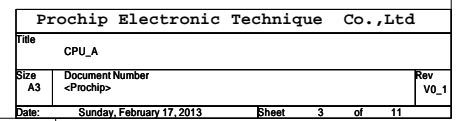
HiveBoard Schematics

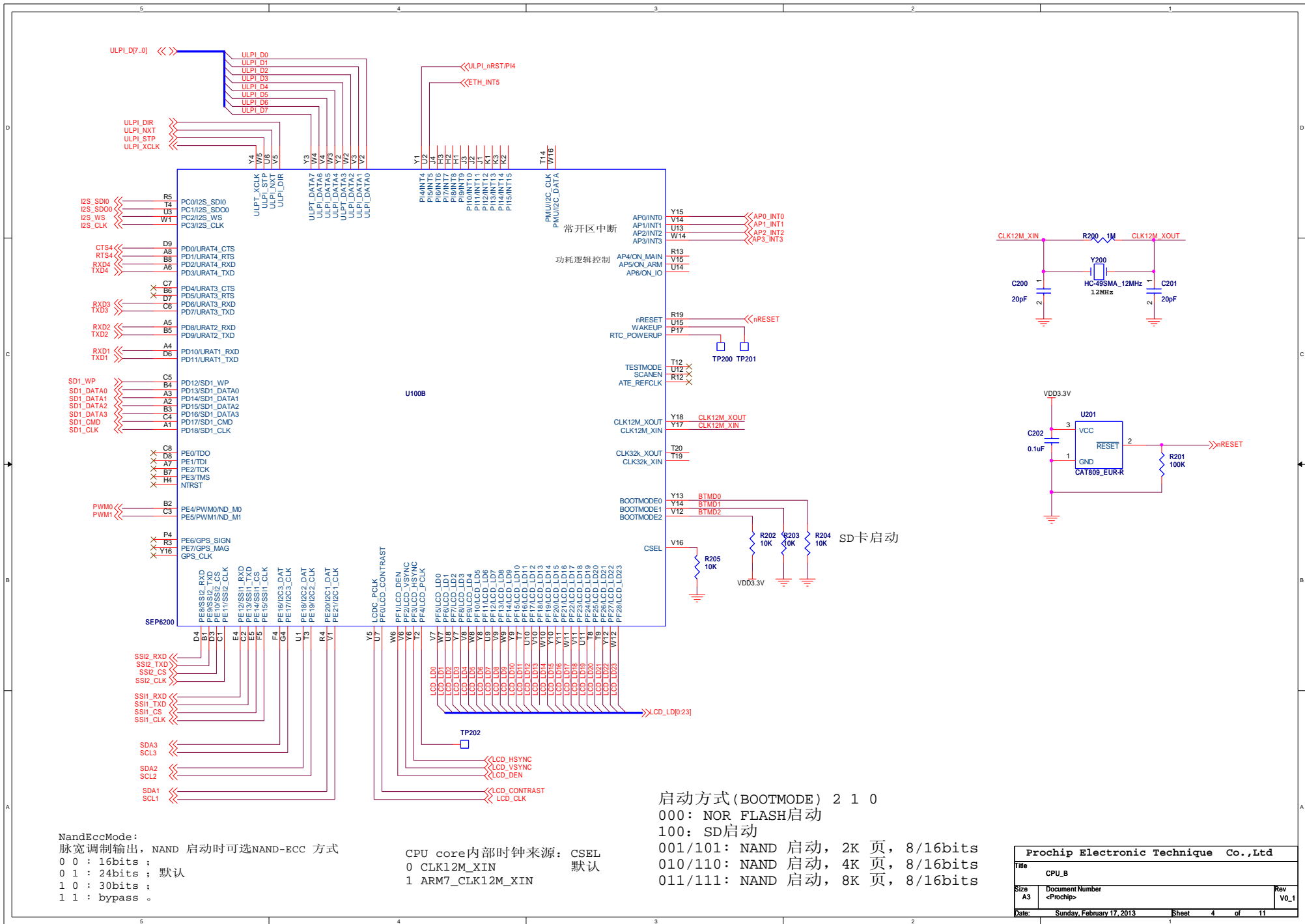
Revision	Date	Discription	Design By	Drawn By
Rev0.1	2013-02-04	Preliminary Version	Yuan Dong	Wang Xiaojian

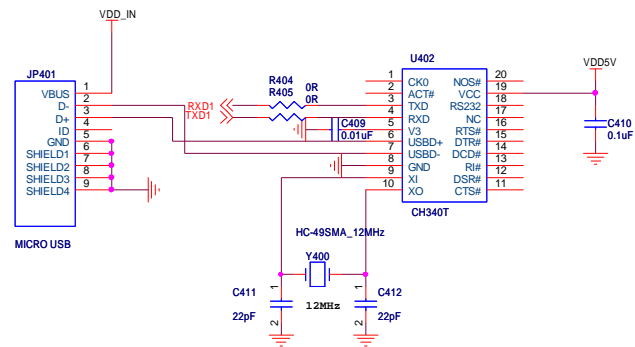
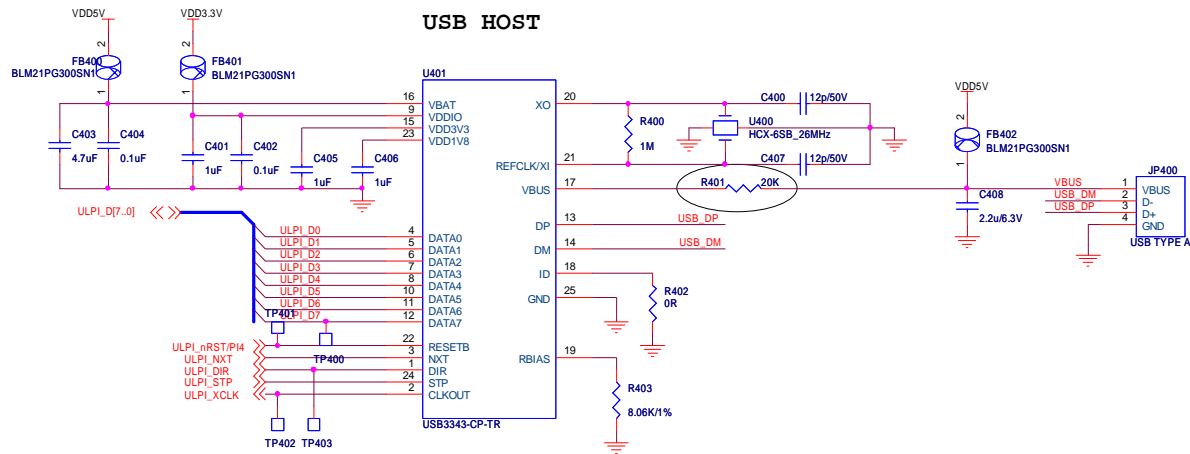
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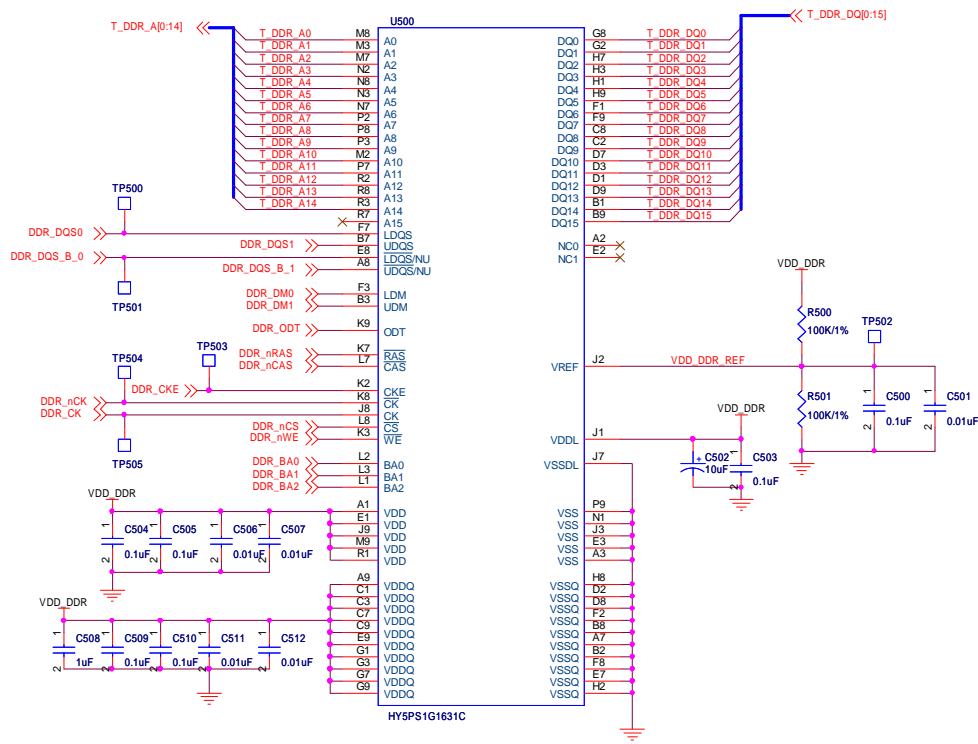
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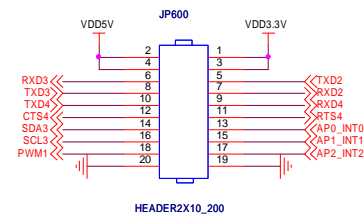
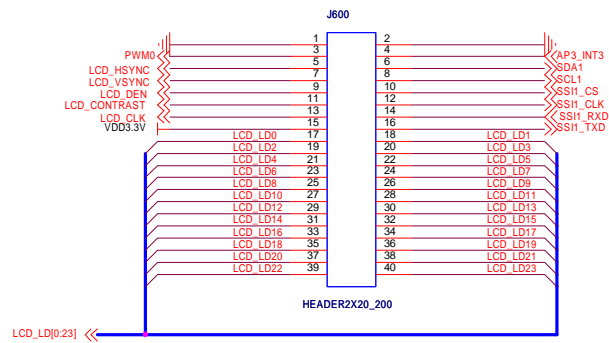




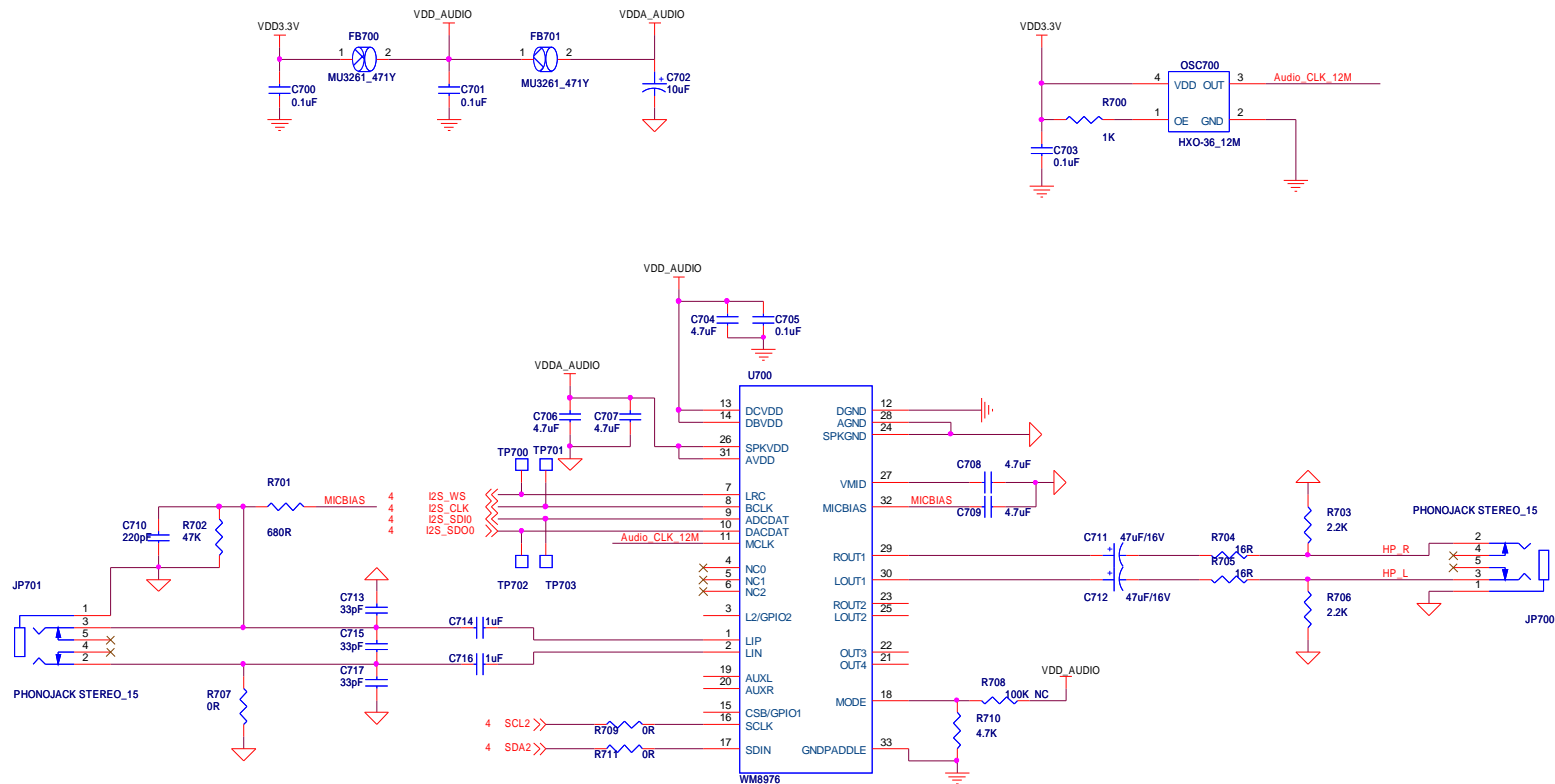




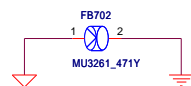




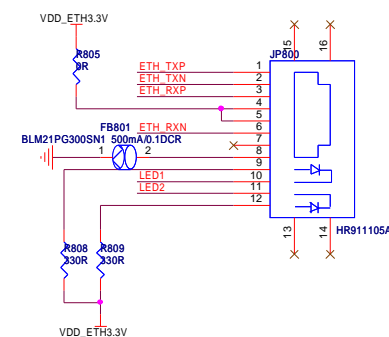
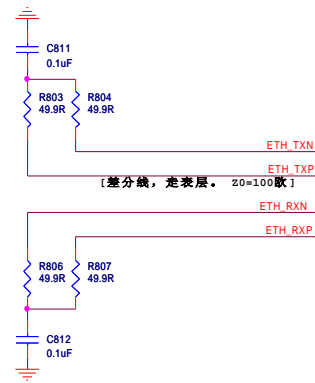
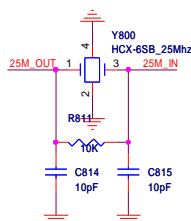
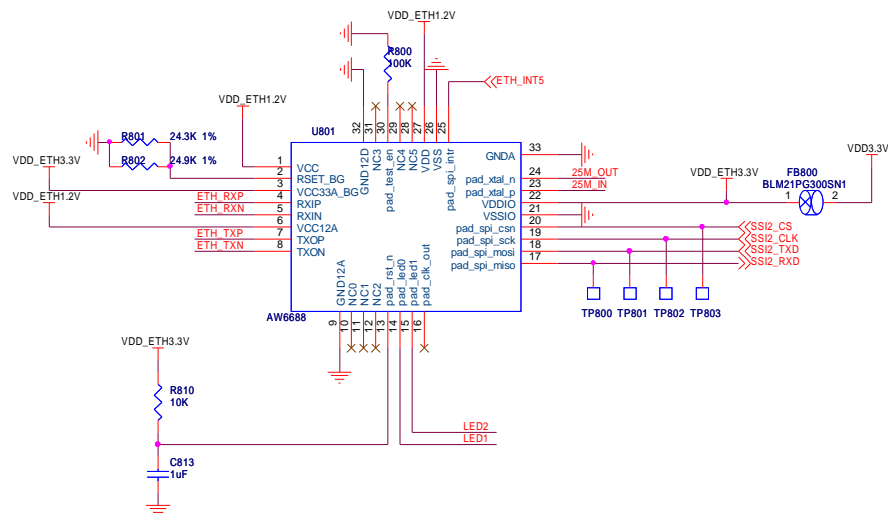
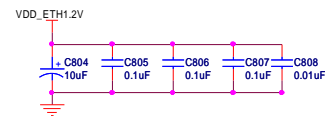
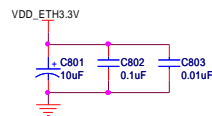
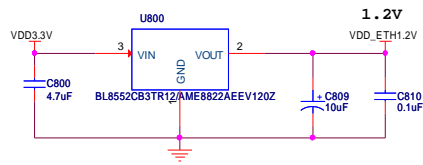
3.3V



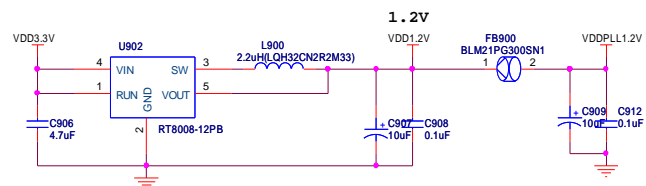
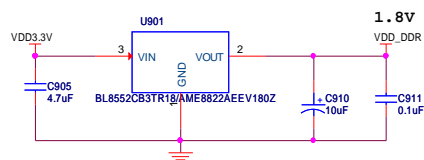
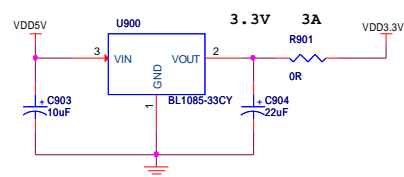
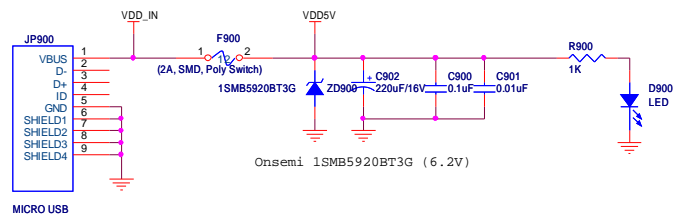
340: 由于芯片没有MCLK脚, 此处需要外接时钟源
48M或者12M



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SD_DET 低有效
SD_WP 高有效

