HiveBoard Schematics

Revision	Date	Discription	Design By	Drawn By
Rev0.1	2013-02-04	Preliminary Version	Yuan Dong	Wang Xiaojian

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Part Reference

<Component><Number>

U : Component or Regurator IC
C : Capacitor
CB : Capacitor Bypass
CT : Capacitor Tantal
CTB : Capacitor Tantal Bypass
J : Jumper
JB : CPU or Base connector
JP : Jumper Power
R : Resistor
RA : Resistor Array
RP : Resistor Power
VR : Variable Resistor
L : Inductor
FB : Ferrite Bead
OSC : Oscillator
X : X-tal (Crystal)
Q : Transistor or FET
D : Diode
ZD : Zener Diode
LED : LED Diode
SW : SWitch Tact/Push
CON : CONnector
CFG : ConFiGure switch (DIP/Slide)
TP : Test Point (SMD)
TPH : Test Point Hole (Through Hole)
MTH: Mount Through Hole
MOD : MODule Interface connector

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Title: HIVEBOARD

Size	Document Number	Rev
A3	Revision Histroy	V0_1

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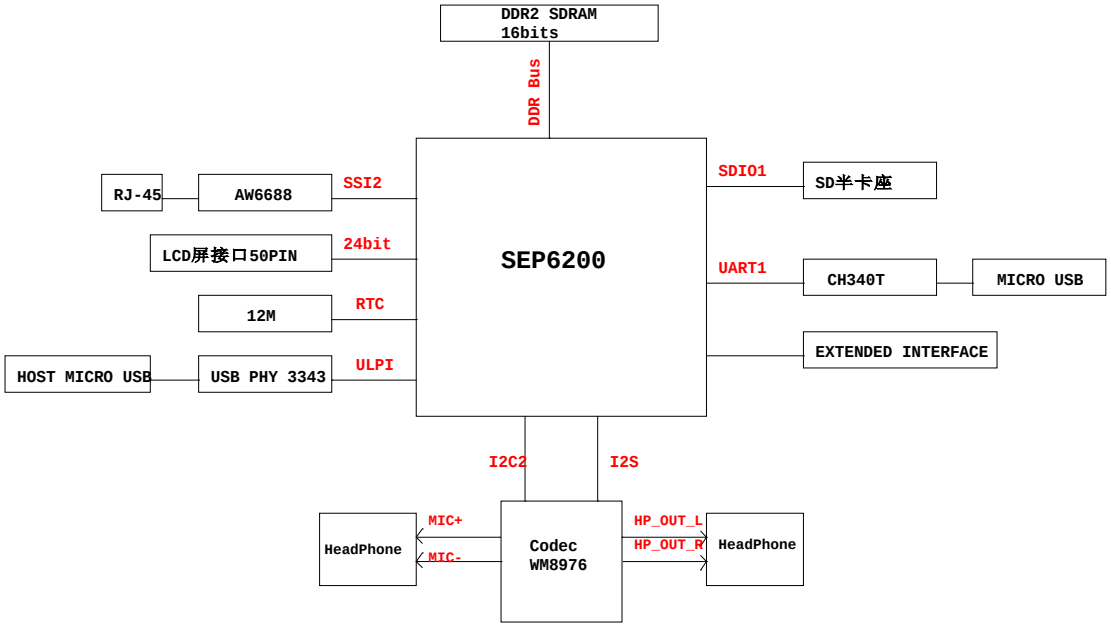

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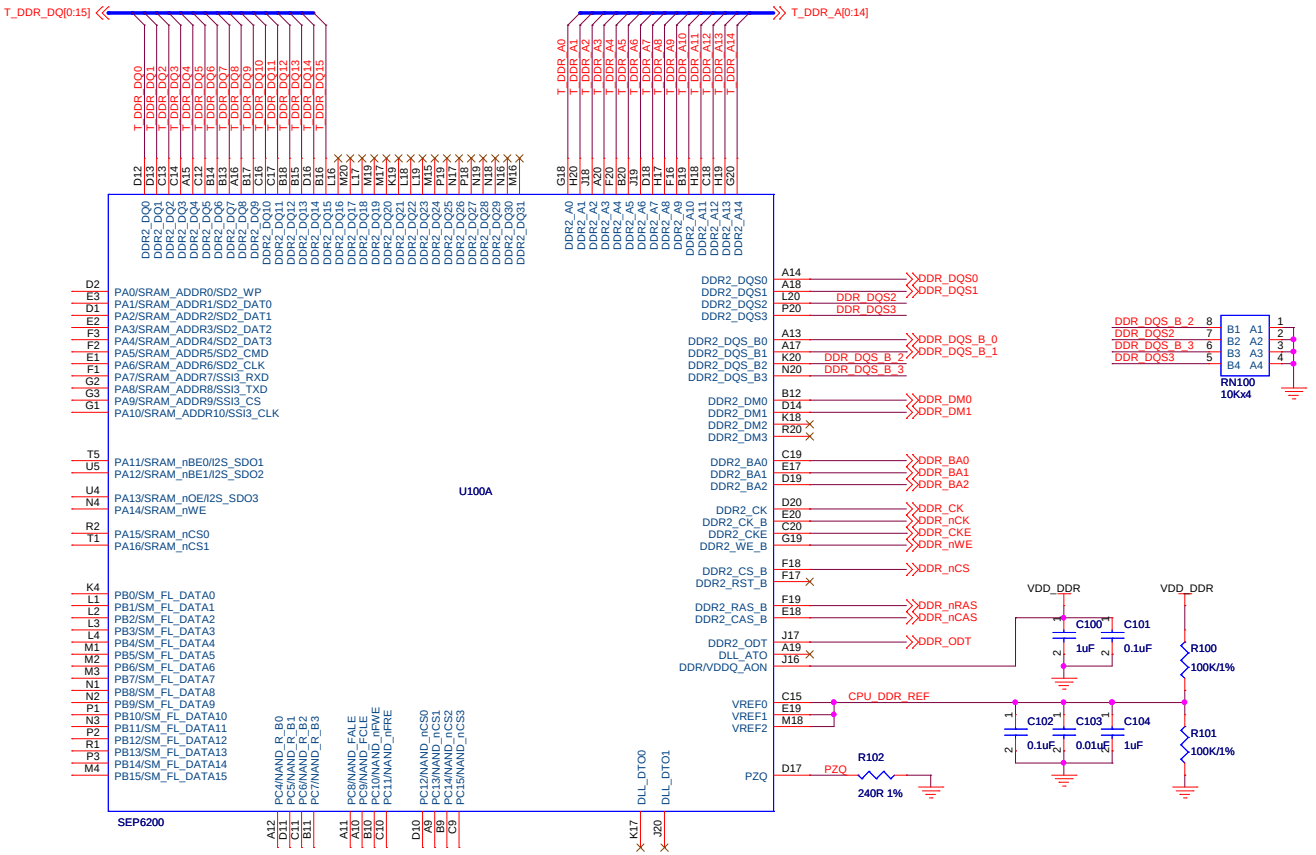

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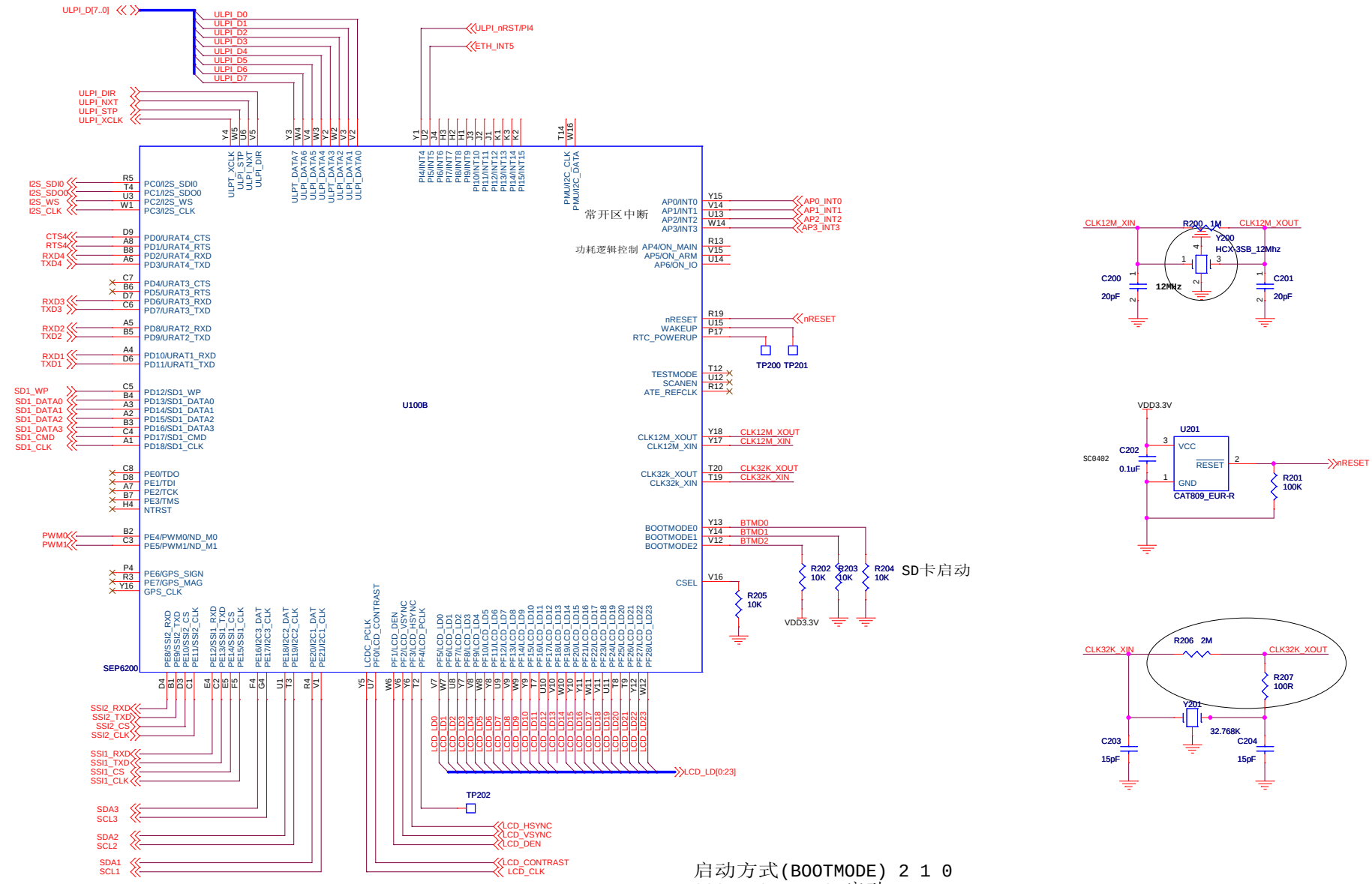

NandEccMode:
脉宽调制输出，NAND 启动时可选NAND-ECC 方式
0 0 : 16bits ;
0 1 : 24bits ; 默认
1 0 : 30bits ;
1 1 : bypass 。

CPU core内部时钟来源: CSEL
0 CLK12M_XIN
1 ARM7_CLK12M_XIN 默认

启动方式(BOOTMODE) 2 1 0
000: NOR FLASH启动
100: SD启动
001/101: NAND 启动, 2K 页, 8/16bits
010/110: NAND 启动, 4K 页, 8/16bits
011/111: NAND 启动, 8K 页, 8/16bits

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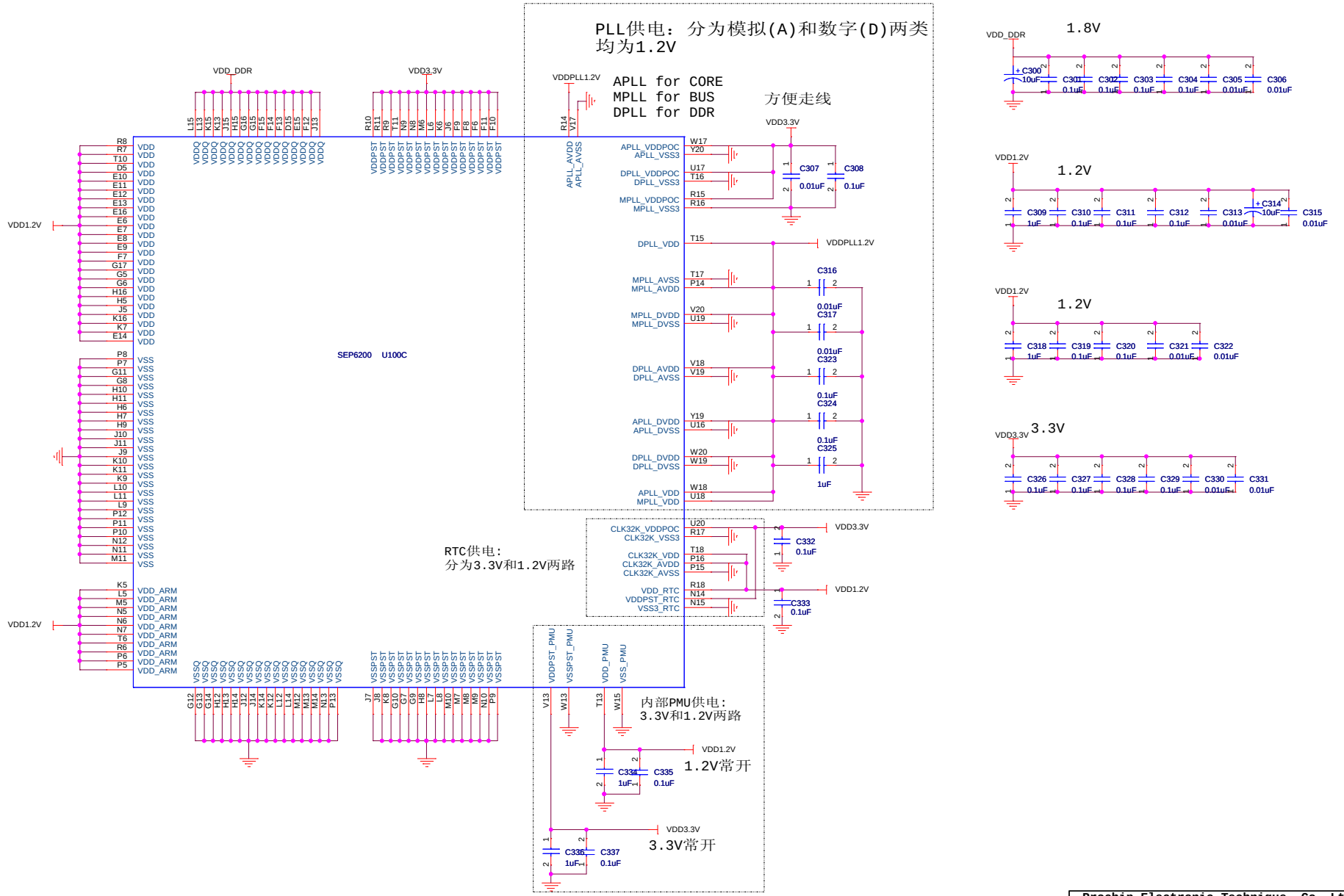

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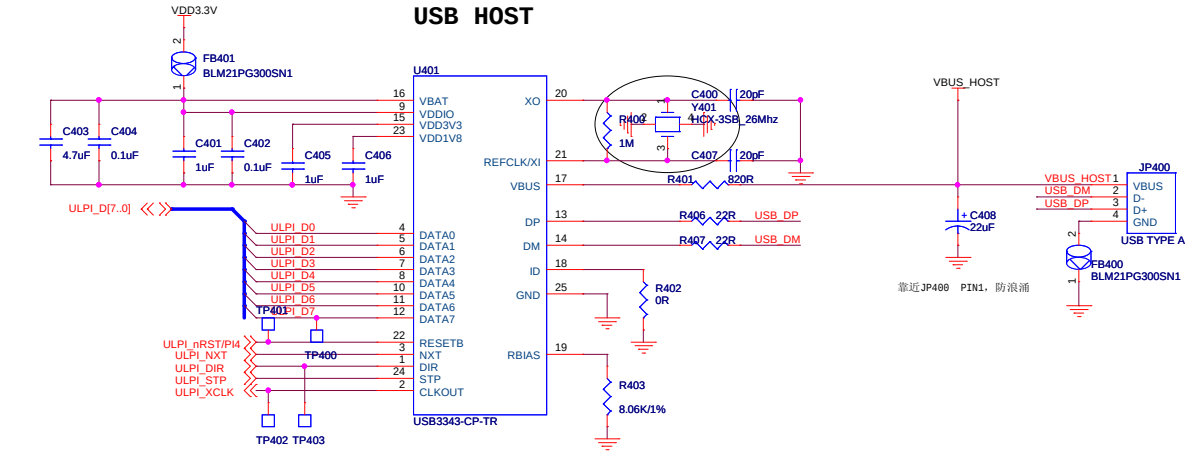
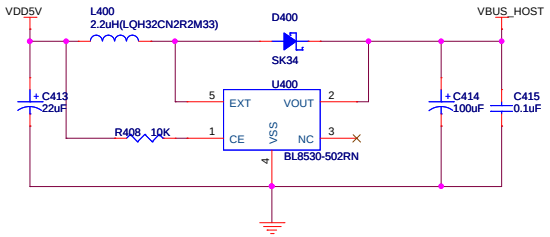
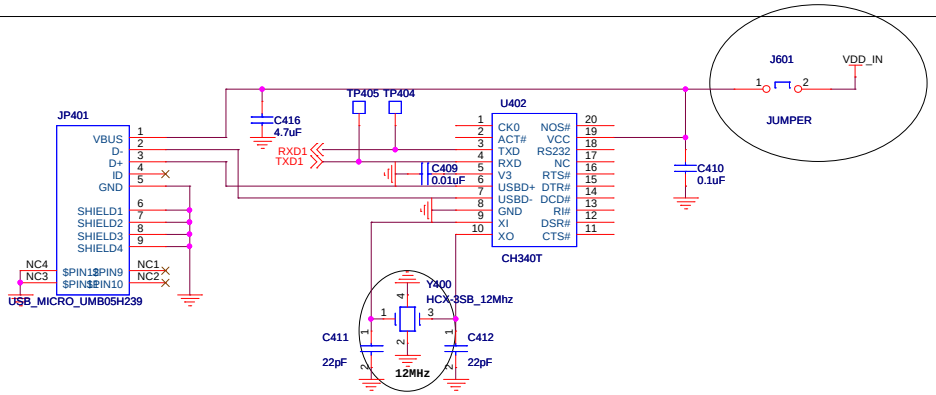
USB HOST







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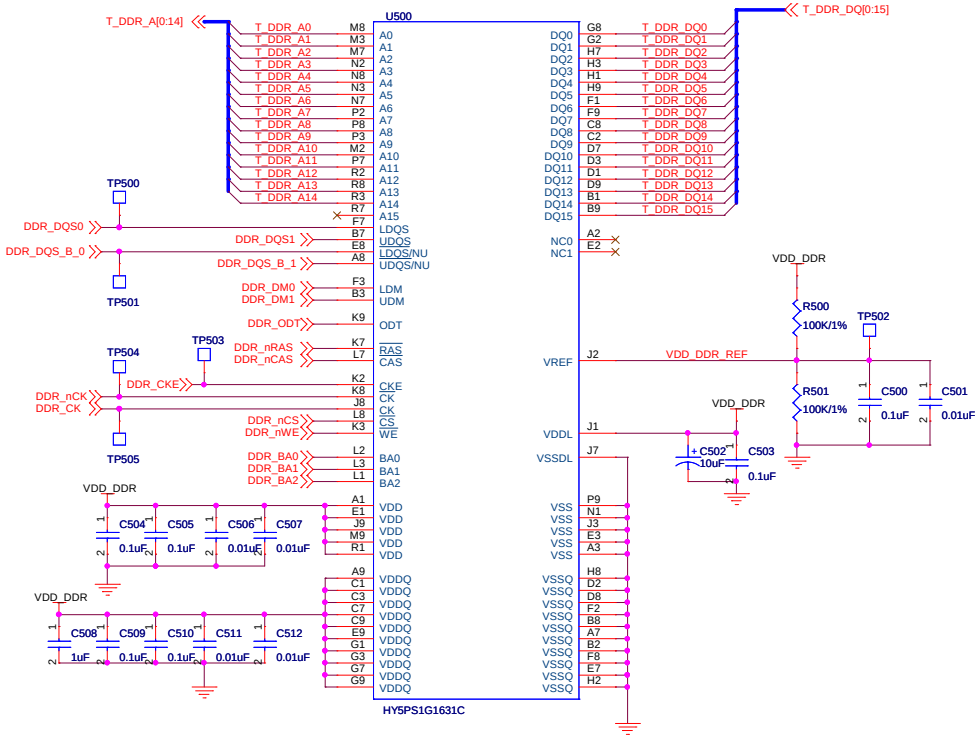

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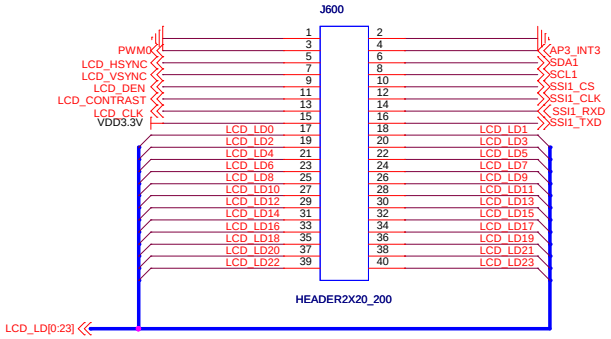

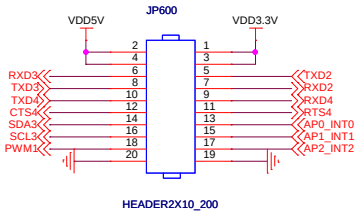

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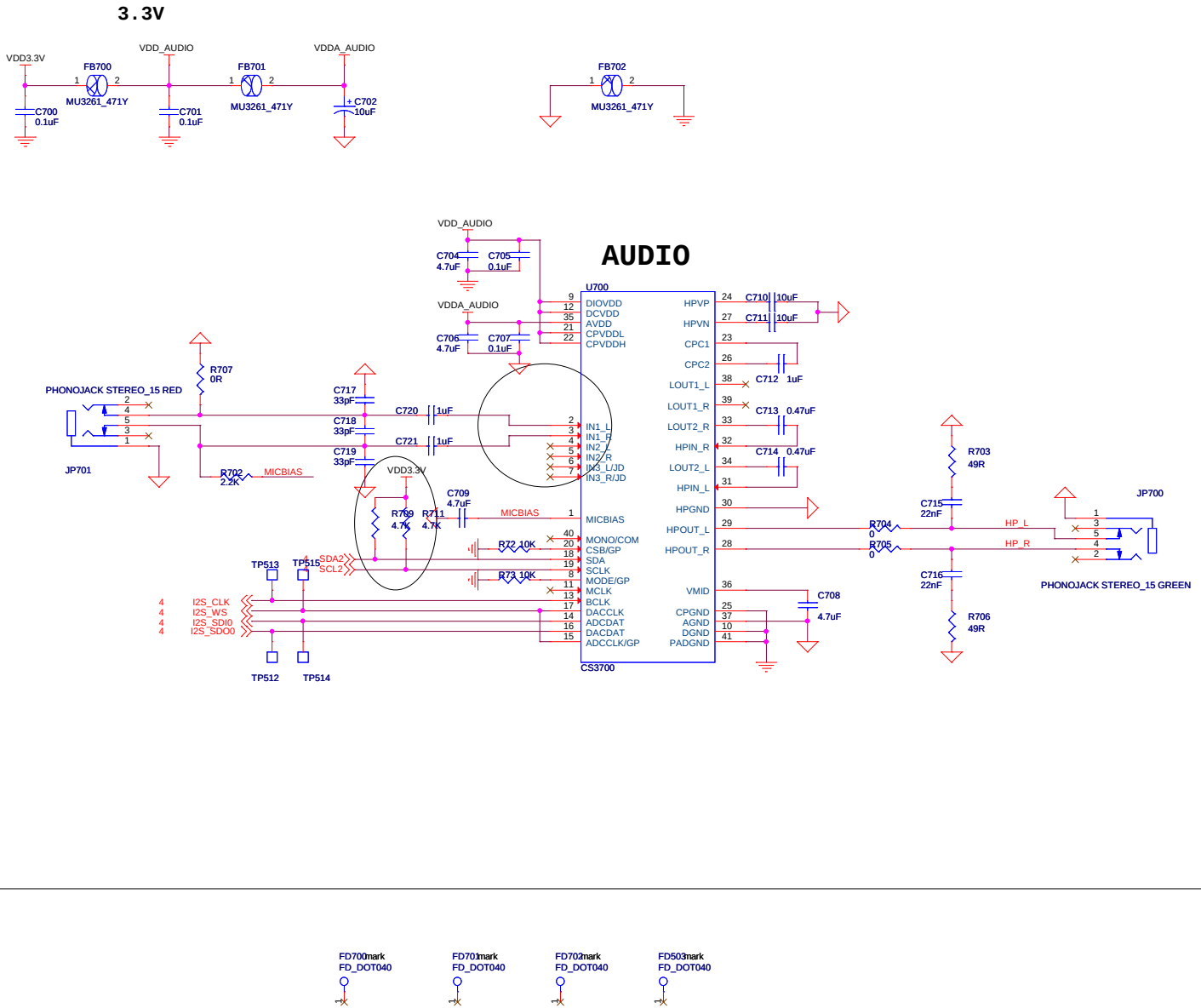

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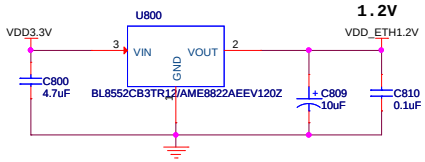

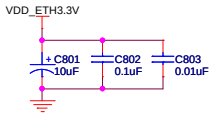

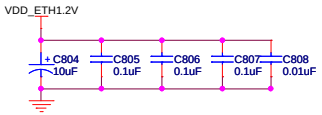

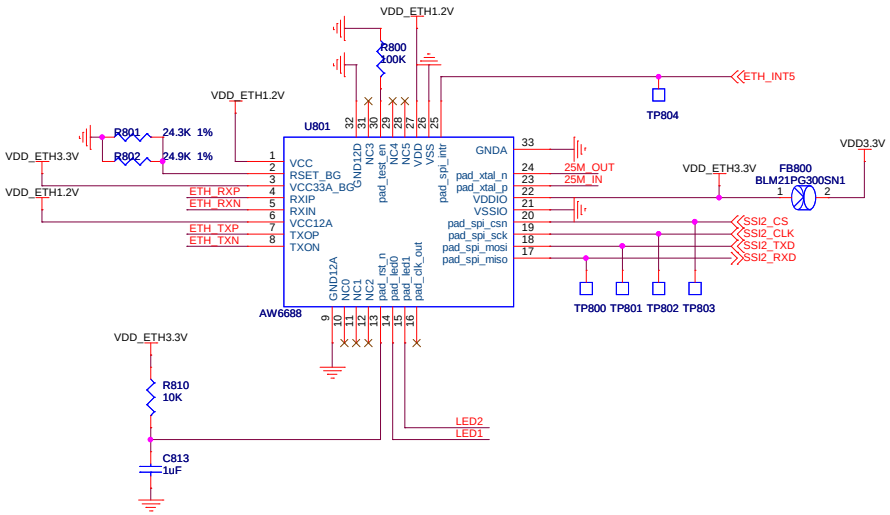

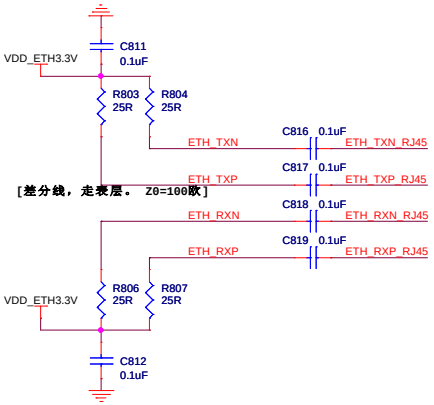

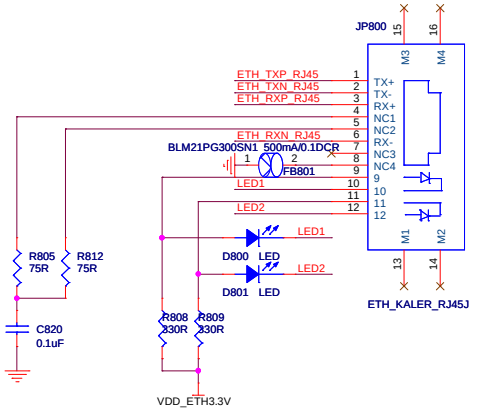

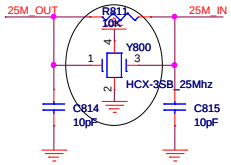

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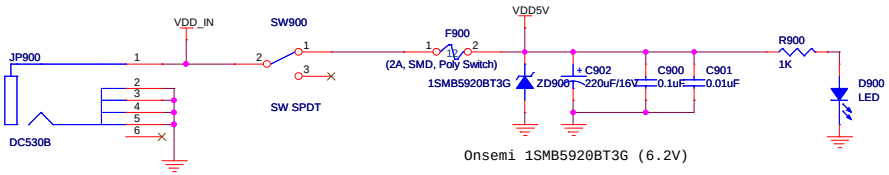
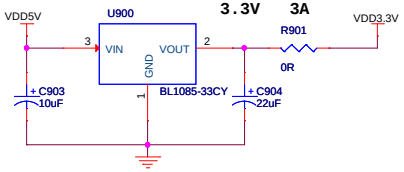
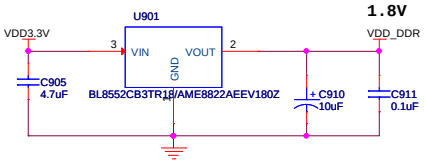
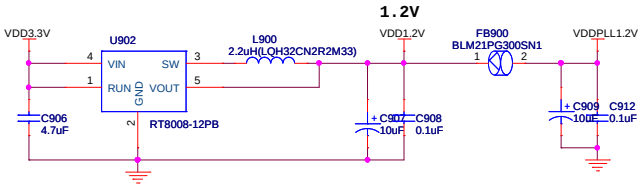


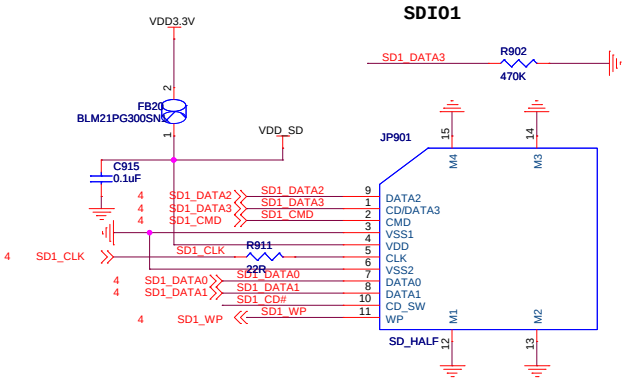
Onsemi 1SMB5920BT3G (6.2V)

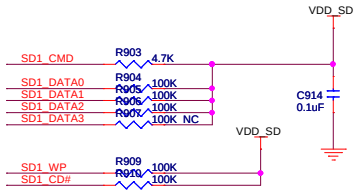






SD_DET 低有效
SD_WP 高有效





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