Lab 4, Question 19

- Line 17

Pseudo instruction	la $a0, string1			
Assembly instruction				
Machine instruction (hex)				
Machine instruction (binary)				
Instruction field (decimal)				
Field function	opcode			

Pseudo instruction	la $a0, string1			
Assembly instruction				
Machine instruction (hex)				
Machine instruction (binary)				
Instruction field (decimal)				
Field function	opcode			

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Pseudo instruction	li $v0, 4			
Assembly instruction				
Machine instruction (hex)				
Machine instruction (binary)				
Instruction field (decimal)				
Field function	opcode			

- Line 29

Pseudo instruction	lw $s2, quitVal			
Assembly instruction				
Machine instruction (hex)				
Machine instruction (binary)				
Instruction field (decimal)				
Field function	opcode			

Pseudo instruction	lw $s2, quitVal			
Assembly instruction				
Machine instruction (hex)				
Machine instruction (binary)				
Instruction field (decimal)				
Field function	opcode			

- Line 30

Assembly instruction with label	beq $s0, $s2, exit			
Raw Assembly instruction				
Machine instruction (hex)				
Machine instruction (binary)				
Instruction field (decimal)				
Field function	opcode			

- Line 41

Assembly instruction	syscall		
Machine instruction (hex)			
Machine instruction (binary)			
Instruction field (decimal)			
Field function	opcode		

- Line 42

Pseudoinstruction	move $s1, $v0				
Assembly instruction					
Machine instruction (hex)					
Machine instruction (binary)					
Instruction field (decimal)					
Field function	opcode				

- Line 72

Assembly instruction with label	j main	
Actual assembly instruction		
Machine instruction (hex)		
Machine instruction (binary)		
Field function	opcode	