Audio Prop	Native	SPIO	Audio	Olxalo	Kbar 2C	CAN	SPI	Serial	Analog	PWM	Digital		Digital	PWM	Analog	Serial	SPI	CAN	(bar	Olxalo	Audio	SPIO	Vative	Prop
G GND			4	<u> </u>		0	0)	U)	1		GND	On 1 1 5v	Vin	ш	1	0)	U)			т.	4			5V
	AD_B0_03	1.3			17	RX2	CS1	RX1		1X1	0		GND											G G
	AD_B0_02	1.2			16	TX2 M	IISO1	TX1		1X0	1	O 34 O	3.3V	250mA ma	ax									3V 3.3
S	EMC_04	4.4	O2	1:4	6					4A2	2		23	4A1	A9			RX1		3:9	MCL1	1.25	AD-B1_09	Α
М	EMC_05	4.5	LR2	1:5	7					4B2	3		22	4A0	A8			TX1		3:08		1.24	AD_B1_08	
Α	EMC_06	4.6	BCL2	1:6	8					2A0	4		21		A7	RX5				3:11	BCL1	1.27	AD_B1_11	Α
A A-EN	EMC_08	4.8	IN2	1:8	17					2A1	5		20		A6	TX5				3:10	LRC1	1.26	AD_B1_10	Α
M-CS	B0_10	2.10	O1D	2:10						2A2, Q41	6		19	Q30	A5	CTS3		SC	CLO	3:00		1.16	AD_B1_00	S C
L-EN	B1_01	2.17	O1A	2:17, 3:17	15			RX2		1B3	7	·	18	Q31	A4			SD	0A0	3:01		1.17	AD_B1_01	S C
	B1_00	2.16	IN1	2:16, 3:16	14 sda0			TX2		1A3	8		17		А3	TX4		SD	A1	3:06		1.22	AD_B1_06	
	B0_11	2.11	O1C	2:11						2B2,Q42	9	O' / / O_	16		A2	RX4		SC	L1	3:07		1.23	AD_B1_07	
S	B0_00	2.0	MQR	2:0			CS0			Q10	10	MIMXRT1062 DVJ6A	15	Q33	A1	RX3				3:03	SPDI	1.19	AD_B1_03	V
SM M/L	B0_02	2.2		2:2		TX1 M	MOSI0			Q12	11	0N00X	14	Q32	A0	TX3				3:02	SPDO	1.18	AD_B1_02	
SM M	B0_01	2.1	MQL	2:1		M	MISO0			Q11	12	CTAB1912J	13	Q20	LED		SCK0	rx1		2:03		2.3	B0_03	M SM
											3.3V		GND											
	AD_B0_12	1.12			SCL2			TX6	A10-1	1X2	24		41	G21	A17					3:5		1.21	AD_B1_05	
	AD_B0_13	1.13			SDA2	_		RX6	A11-1	1X3	25		40		A16					3:4		1.20	AD_B1_04	
	AD_B1_14	1.30		3:14		M	MOSI1		A12-2		26		39		A15-2		MISO1			3:13		1.29	AD_B1_13	
	AD_B1_15	1.31		3:15		S	SCK1		A13-2		27		38		A14-2		CS1-0			3:12		1.28	AD_B1_12	
	EMC_32	3.18						RX7		3B1	28		37	2B3			CS0-1		17	2:19,3:19		2.19	B1_03	
	EMC_31	4.31		_				TX7		3A1	29		36	2A3			CS0-2		16	2:18,3:18		2.18	B1_02	
	EMC_37	3.23		1	23	RX3				G13	30		35			TX8				2:28,3:28		2.28	B1_12	
	EMC_36	3.22		1	22	TX3				G12	31		34			RX8		RX1		2:29,3:29		2.29	B1_13	
	B0_12	2.12	O1B	2:12	10						32	O THIIII O	33	2B0				TX1	9	1:7	MCL2	4.7	EMC_07	
												DIO Pins												
-	SD_B0_03	2.15		DATA1	7	М	IISO2			1B1	42		47	1A2		TX5			8	DATA2	;	3.16	SD_B0_04	
	SD_B0_02			DATA0	6		IOSI2	CTS5		1A1	43			1B2		RX5			(DATA3	;	3.17	SD_B0_05	
	SD_B0_01	2.12		CLK	E CDA1	CC	22				GND		45	1A0	3.3V		SCK2	SC	L1 4	4 CMD	:	3.12	SD_B0_00	
	3D_B0_01	3.13		CLK	5 SDA1	CS	02			180	44	I			3.34									
												ack Memory Chips												
	EMC_26	4.26		1:12			F	RX1		1B1	52	, '	GND											
	EMC_25	4.25					7	X1		1A1	53		50	1B2		CTS8	MOSI2			1:14		4.28	EMC_28	
	EMC_29	4.29		1:15		М	IISO2			3A0	54		49	1A2		S	SCK2			1:13	,	4.27	EMC_27	
											3.3V	(pin 1)	51	3B3,Q23				SCI	L1				EMC_22	
																								
	EMC_26	4.26		1:12				RX1		1B1	52		GND											
	EMC_25	4.25						X1		1A1	53		50			CTS8				1:14			EMC_28	
	EMC_29	4.29		1:15		M	IISO2			3A0	54		49	1A2			SCK2			1:13			EMC_27	
											3.3V	(pin 1)	48	1B0		RX8					•	4.24	EMC_24	