Audio Prop	Native	GPIO	Audio	FlexiO	Xbar 12C	CAN	SPI	Serial	Analog	PWM	Digital		Digital	PWM	Analog	Serial	SPI	CAN	12C	FlexIO	Audio	GPIO	Native	Prop
G GND											GND	5 VO	Vin											5V
	AD_B0_03				17	RX2		RX1		1X1	0	0 [GND											G
	AD_B0_02		00		16	TX2	MISO1	TX1		1X0	1	3v = 3v		250mA m				DV4		2.2	1401.4	4.05	10.01.00	3V
S	EMC_04	4.4	02	1:4	6					4A2	2		23	4A1	A9			RX1		3:9	MCL1		AD-B1_09	
M	EMC_05	4.5	LR2	1:5	7					4B2	3		22	4A0	A8	DVC		TX1		3:08	DOL 4		AD_B1_08	
A	EMC_06		BCL2		47					2A0	4		21		A7	RX5				3:11	BCL1		AD_B1_11	
A A-EN	EMC_08	4.8	IN2	1:8	17					2A1	5	CHILL CHILL 2	20	020	A6	TX5			2010	3:10	LRC1		AD_B1_10 AD_B1_00	
M-CS	B0_10		O1D	2:10 2:17, 3:17	45			DVO		2A2, Q41	6		19	Q30		CTS3			SCL0	3:00				
L-EN	B1_01	2.17						RX2		1B3	7		18	Q31	A4	TX4			SDA0	3:01			AD_B1_01	
	B1_00	2.16	IN1 O1C		14 Suau			172		1A3	8	. >	17		A3	RX4			SCL1	3:06			AD_B1_06	
3	B0_11 B0_00	2.11		2:11			CS0			2B2,Q42 Q10	10	MIMXRT1052	16 15	Q33	A2 A1	RX3			SCLI	3:07 3:03	SPDI		AD_B1_07 AD_B1_03	
M M/L	B0_00 B0_02	2.2	WICK	2:2		TV1	MOSI0			Q10 Q12		DVJ6A	14	Q32	A0	TX3				3:02	SPDO		AD_B1_03 AD_B1_02	
M M	B0_02 B0_01	2.1	MQL			IVI	MISO0			Q12 Q11	11 12	0N00X CTAB1912J	13	Q32 Q20	LED	173	SCK0	n/1		2:03	3700	2.3	B0_03	M
IVI IVI	Б0_01	2.1	MQL	2.1			MISOU				3.3V		GND		LED		SCRU	IXI		2.03		2.3	BU_U3	IVI
	AD_B0_12	1 12			SCL2			TV6	A10-1	1X2	24			G21	A17					3:5		1 21	AD_B1_05	:
	AD_B0_12 AD_B0_13				SDA2				A10-1	1X3	25	6 10 0	40	GZT	A17					3:4			AD_B1_03 AD_B1_04	
	AD_B0_13 AD_B1_14			3:14	SDAZ		MOSI1	KAO	A11-1	173	26	\$	39		A15-2		MISO1			3:13			AD_B1_04 AD_B1_13	
	AD_B1_14 AD_B1_15			3:15			SCK1		A12-2		27	1	38		A14-2		CS1-0			3:12			AD_B1_13 AD_B1_12	
	EMC_32			3.10			SUNT	RX7	A13-2	3B1	28	* * * * * * * * * * * * * * * * * * *	37	2B3	A14-2		CS0-1		1	7 2:19,3:19		2.19	B1_03	•
	EMC_31	4.31						TX7		3A1	29	A S S S S	36	2A3			CS0-1		1	6 2:18,3:18		2.18	B1_03 B1_02	
	EMC_37	3.23			23	RX3		IXI		G13	30	6 →	35	2/13		TX8	000-2			2:28,3:28		2.28	B1_02 B1_12	
	EMC_36	3.22			22	TX3				G12	31		34			RX8		RX1		2:29,3:29		2.29	B1_12	
	B0_12		O1B	2:12	10	17.0				012	32	& CEPTIFIED S	33	2B0		TOTO		TX1		1:7	MCL2	4.7	EMC_07	
	B0_12	2.12	OID	2.12								MIIIIIII-	00	200				17(1		1.7	WIOLZ	7.1	LINO_07	
	SD_B0_03	2.15		DATA1	7		MISO2			1B1	42	O Pins	17	1A2	_	TX5				8 DATA2		2.16	SD_B0_04	
	SD_B0_03 SD_B0_02			DATA1 DATA0	6		MOSI2	CTS5		1A1	43	<u>l </u>		1B2		RX5				9 DATA3			SD_B0_04 SD_B0_05	
											GND		_	1A0			SCK2		SCL1	4 CMD			SD_B0_00	
	SD_B0_01	3.13		CLK	5 SDA1		CS2			1B0	44				3.3V									
												k Memory Chips												
	EMC_26	4.26		1:12				RX1		1B1	52	_	GND											
	EMC_25	4.25						TX1		1A1	53		50	1B2		CTS8	MOSI2			1:14	4	4.28	EMC_28	
	EMC_29	4.29		1:15			MISO2			3A0	54		49	1A2			SCK2			1:1:	<mark>3</mark>	4.27	EMC_27	
											3.3V	est.	51	3B3,Q23					SCL1			4.22	EMC_22	
	EMC_26	4.26		1:12				RX1		1B1	52		GND)										
	EMC_25	4.25						TX1		1A1	53		50			CTS8	MOSI2			1:14	4	4.28	EMC_28	
										3A0										1:1:				
	EMC_29	4.29		1:15			MISO2			SAU	54		49	1A2			SCK2			1.1	3	4.27	EMC_27	