

Pin	Name	ALT0	Alt1	ALT2	ALT3	ALT4	Alt5	ALT6	ALT7	ALT8	ALT9
0	AD_B0_03	FLEXCAN2_RX	XBAR1_INOUT17	LPUART6_RX	USB_OTG1_OC	FLEXPWM1_PWMX01	GPIO1_IO03	REF_CLK_24M	LPSP13_PCS0		
1	AD_B0_02	FLEXCAN2_TX	XBAR1_INOUT16	LPUART6_TX	USB_OTG1_PWR	LEXPWM1_PWMX0	GPIO1_IO02	LP12C1_HREQ	LPSP13_SDI		
2	EMC_04	SEMC_DATA04	FLEXPWM4_PWMA02	SAI2_TX_DATA	XBAR1_INOUT06	FLEXIO1_FLEXIO04	GPIO4_IO04				
3	EMC_05	SEMC_DATA05	FLEXPWM4_PWMB0	SAI2_TX_SYNC	XBAR1_INOUT07	FLEXIO1_FLEXIO05	GPIO4_IO05				
4	EMC_06	SEMC_DATA06	FLEXPWM2_PWMA00	SAI2_TX_BCLK	XBAR1_INOUT08	FLEXIO1_FLEXIO06	GPIO4_IO06				
5	EMC_08	SEMC_DM00	FLEXPWM2_PWMA01	SAI2_RX_DATA	XBAR1_INOUT17	FLEXIO1_FLEXIO08	GPIO4_IO08				
6	B0_10	LCD_DATA06	QTIMER4_TIMER1	FLEXPWM2_PWMA02	SAI1_TX_DATA03	FLEXIO2_FLEXIO10	GPIO2_IO10	SRC_BOOT_CFG06		ENET2_CRS	
7	B1_01	LCD_DATA13	XBAR1_INOUT15	LPUART4_RX	SAI1_TX_DATA00	FLEXIO2_FLEXIO17	GPIO2_IO17	FLEXPWM1_PWMB03		ENET2_RDATA00	FLEXIO3_FLEXIO17
8	B1_00	LCD_DATA12	XBAR1_INOUT14	LPUART4_TX	SAI1_RX_DATA00	FLEXIO2_FLEXIO16	GPIO2_IO16	FLEXPWM1_PWMA03		ENET2_RX_ER	FLEXIO3_FLEXIO16
9	B0_11	LCD_DATA07	QTIMER4_TIMER2	FLEXPWM2_PWMB02	SAI1_TX_DATA02	FLEXIO2_FLEXIO11	GPIO2_IO11	SRC_BOOT_CFG07		ENET2_C0L	
10	B0_00	LCD_CLK	QTIMER1_TIMER0	MQS_RIGHT	LPSP14_PCS0	FLEXIO2_FLEXIO00	GPIO2_IO00	SEMC_CSX01		ENET2_MDC	
11	B0_02	LCD_HSYNC	QTIMER1_TIMER2	FLEXCAN1_TX	LPSP14_SDO	FLEXIO2_FLEXIO02	GPIO2_IO02	SEMC_CSX03		ENET2_1588_EVENT0_OUT	
12	B0_01	LCD_ENABLE	QTIMER1_TIMER1	MQS_LEFT	LPSP14_SDI	FLEXIO2_FLEXIO01	GPIO2_IO01	SEMC_CSX02		ENET2_MDIO	
13	B0_03	LCD_VSYNC	QTIMER2_TIMER0	FLEXCAN1_RX	LPSP14_SCK	FLEXIO2_FLEXIO03	GPIO2_IO03	WD0G2_RESET_B_DEB		ENET2_1588_EVENT0_IN	
14/A0	AD_B1_02	USB_OTG1_ID	QTIMER3_TIMER2	LPUART2_TX	SPDIF_OUT	ENET_1588_EVENT2_OUT	GPIO1_IO18	USDHC1_CD_B	KPP_ROW06	GPT2_CLK	FLEXIO3_FLEXIO02
15/A1	AD_B1_03	USB_OTG1_OC	QTIMER3_TIMER3	LPUART2_RX	SPDIF_IN	ENET_1588_EVENT2_IN	GPIO1_IO19	USDHC2_CD_B	KPP_COL06	GPT2_CAPTURE1	FLEXIO3_FLEXIO03
16/A2	AD_B1_07	FLEXSPIB_DATA00	LP12C3_SCL	LPUART3_RX	SPDIF_EXT_CLK	CSI_HSYNC	GPIO1_IO23	USDHC2_DATA3	KPP_COL04	GPT2_COMPARE3	FLEXIO3_FLEXIO07
17/A3	AD_B1_06	FLEXSPIB_DATA01	LP12C3_SDA	LPUART3_TX	SPDIF_LOCK	CSI_VSYNC	GPIO1_IO22	USDHC2_DATA2	KPP_ROW04	GPT2_COMPARE2	FLEXIO3_FLEXIO06
18/A4	AD_B1_01	USB_OTG1_PWR	QTIMER3_TIMER1	LPUART2_RTS_B	LP12C1_SDA	CCM_PMIC_READY	GPIO1_IO17	USDHC1_VSELECT	KPP_COL07	ENET2_1588_EVENT0_IN	FLEXIO3_FLEXIO01
19/A5	AD_B1_00	USB_OTG2_ID	QTIMER3_TIMER0	LPUART2_CTS_B	LP12C1_SCL	WD0G1_B	GPIO1_IO16	USDHC1_W	KPP_ROW07	ENET2_1588_EVENT0_OUT	FLEXIO3_FLEXIO00
20/A6	AD_B1_10	FLEXSPIA_DATA03	WD0G1_B	LPUART8_TX	SAI1_RX_SYNC	CSI_DATA07	GPIO1_IO26	USDHC2_WP	KPP_ROW02	ENET2_1588_EVENT1_OUT	FLEXIO3_FLEXIO10
21/A7	AD_B1_11	FLEXSPIA_DATA02	EWM_OUT_B	LPUART8_RX	SAI1_RX_BCL	CSI_DATA06	GPIO1_IO27	USDHC2_RESET_B	KPP_COL02	ENET2_1588_EVENT1_IN	FLEXIO3_FLEXIO11
22/A8	AD_B1_08	FLEXSPIA_SS1_B	FLEXPWM4_PWMA00	FLEXCAN1_TX	CCM_PMIC_READY	CSI_DATA09	GPIO1_IO24	USDHC2_CMD	KPP_ROW03		FLEXIO3_FLEXIO08
23/A9	AD_B1_09	FLEXSPIA_DQS	FLEXPWM4_PWMA01	FLEXCAN1_RX	SAI1_MCLK	CSI_DATA08	GPIO1_IO25	USDHC2_CLK	KPP_COL03		FLEXIO3_FLEXIO09
24/A10	AD_B0_12	LP12C4_SCL	CCM_PMIC_READY	LPUART1_TX	WD0G2_WD0G_B	FLEXPWM1_PWMX02	GPIO1_IO12	ENET_1588_EVENT1_OUT	NMI_GLUE_NMI		
25/A11	AD_B0_13	LP12C4_SDA	GPT1_CLK	LPUART1_RX	EWM_OUT_B	FLEXPWM1_PWMX03	GPIO1_IO13	ENET_1588_EVENT1_IN	REF_CLK_24M		
26/A12	AD_B1_14	FLEXSPIA_SCLK	ACMP_OUT02	LPSP13_SDO	SAI1_TX_BCLK	CSI_DATA03	GPIO1_IO30	USDHC2_DATA6	KPP_ROW00	ENET2_1588_EVENT3_OUT	FLEXIO3_FLEXIO14
27/A13	AD_B1_15	FLEXSPIA_SS0_B	ACMP_OUT03	LPSP13_SCK	SAI1_TX_SYNC	CSI_DATA02	GPIO1_IO31	USDHC2_DATA7	KPP_COL00	ENET2_1588_EVENT3_IN	FLEXIO3_FLEXIO15
28	EMC_32	SEMC_DATA10	FLEXPWM3_PWMB01	LPUART7_RX	CCM_PMIC_RDY	CSI_DATA21	GPIO3_IO18			ENET2_TX_EN	
29	EMC_31	SEMC_DATA09	FLEXPWM3_PWMA01	LPUART7_TX	LPSP11_PCS1	CSI_DATA22	GPIO4_IO31			ENET2_TDATA01	
30	EMC_37	SEMC_DATA15	XBAR1_IN23	GPT1_COMPARE3	SAI3_MCLK	CSI_DATA16	GPIO3_IO23	USDHC2_WP		ENET2_RX_EN	FLEXCAN3_RX
31	EMC_36	SEMC_DATA14	XBAR1_IN22	GPT1_COMPARE2	SAI3_TX_DATA	CSI_DATA17	GPIO3_IO22	USDHC1_WP		ENET2_RDATA01	FLEXCAN3_TX
32	B0_12	LCD_DATA08	XBAR1_INOUT10	ARM_TRACE_CLK	SAI1_TX_DATA01	FLEXIO2_FLEXIO12	GPIO2_IO12	SRC_BOOT_CFG08		ENET2_TDATA00	
33	EMC_07	SEMC_DATA07	FLEXPWM2_PWMB00	SAI2_MCLK	XBAR1_INOUT09	FLEXIO1_FLEXIO07	GPIO4_IO07				
34	B1_13	WD0G1_B	LPUART5_RX	CSI_VSYNC	ENET_1588_EVENT0_OUT	FLEXIO2_FLEXIO29	GPIO2_IO29	USDHC1_WP		SEMC_DQS4	FLEXIO3_FLEXIO29
35	B1_12		LPUART5_TX	CSI_PIXCLK	ENET_1588_EVENT0_IN	FLEXIO2_FLEXIO28	GPIO2_IO28	USDHC1_CD_B			FLEXIO3_FLEXIO28
36	B1_02	LCD_DATA14	XBAR1_INOUT16	LPSP14_PCS2	SAI1_TX_BCLK	FLEXIO2_FLEXIO18	GPIO2_IO18	FLEXPWM2_PWMA03		ENET2_RDATA01	FLEXIO3_FLEXIO18
37	B1_03	LCD_DATA15	XBAR1_INOUT17	LPSP14_PCS1	SAI1_TX_SYNC	FLEXIO2_FLEXIO19	GPIO2_IO19	FLEXPWM2_PWMB03		ENET2_RX_EN	FLEXIO3_FLEXIO19
38/A14	AD_B1_12	FLEXSPIA_DATA01	ACMP_OUT00	LPSP13_PCS0	SAI1_RX_DATA00	CSI_DATA05	GPIO1_IO28	USDHC2_DATA4	KPP_ROW01	ENET2_1588_EVENT2_OUT	FLEXIO3_FLEXIO12
39/A5	AD_B1_13	FLEXSPIA_DATA00	ACMP_OUT01	LPSP13_SDI	SAI1_TX_DATA00	CSI_DATA04	GPIO1_IO29	USDHC2_DATA5	KPP_COL01	ENET2_1588_EVENT2_IN	FLEXIO3_FLEXIO13
40/A16	AD_B1_04	FLEXSPIB_DATA03	ENET_MDIO	LPUART3_CTS_B	SPDIF_SR_CLK	CSI_PIXCLK	GPIO1_IO20	USDHC2_DATA0	KPP_ROW05	GPT2_CAPTURE2	FLEXIO3_FLEXIO04
41/A17	AD_B1_05	FLEXSPIB_DATA02	ENET_MDIO	LPUART3_RTS_B	SPDIF_OUT	CSI_MCLK	GPIO1_IO21	USDHC2_DATA1	KPP_COL05	GPT2_COMPARE1	FLEXIO3_FLEXIO05
42	SD_B0_03	USDHC1_DATA1	FLEXPWM1_PWMB01	LPUART8_RTS_B	XBAR1_INOUT07	LPSP11_SDI	GPIO3_IO15			ENET2_RDATA00	SEMC_CLK6
43	SD_B0_02	USDHC1_DATA0	FLEXPWM1_PWMA01	LPUART8_CTS_B	XBAR1_INOUT06	LPSP11_SDO	GPIO3_IO14			ENET2_RX_ER	SEMC_CLK5
44	SD_B0_01	USDHC1_CLK	FLEXPWM1_PWMB00	LP12C3_SDA	XBAR1_INOUT05	LPSP11_PCS0	GPIO3_IO13	FLEXSPIB_SS1_B		ENET2_TX_CLK	ENET2_REF_CLK2
45	SD_B0_00	USDHC1_CMD	FLEXPWM1_PWMA00	LP12C3_SCL	XBAR1_INOUT04	LPSP11_SCK	GPIO3_IO12	FLEXSPIA_SS1_B		ENET2_TX_EN	SEMC_DQS4
46	SD_B0_05	USDHC1_DATA3	FLEXPWM1_PWMB02	LPUART8_RX	XBAR1_INOUT09	FLEXSPIB_DQS	GPIO3_IO17	CCM_CLK02		ENET2_RX_EN	
47	SD_B0_04	USDHC1_DATA2	FLEXPWM1_PWMA02	LPUART8_TX	XBAR1_INOUT08	FLEXSPIB_SS0_B	GPIO3_IO16	CCM_CLK01		ENET2_RDATA01	
48	EMC_24	SEMC_CAS	FLEXPWM1_PWMB00	LPUART5_RX	ENET_TX_EN	GPT1_CAPTURE1	GPIO4_IO24			FLEXSPI2_A_SS0_B	
49	EMC_27	SEMC_CKE	FLEXPWM1_PWMA02	LPUART5_RTS_B	LPSP11_SCK	FLEXIO1_FLEXIO13	GPIO4_IO27			FLEXSPI2_A_DATA01	
50	EMC_28	SEMC_WE	FLEXPWM1_PWMB02	LPUART5_CTS_B	LPSP11_SDO	FLEXIO1_FLEXIO14	GPIO4_IO28			FLEXSPI2_A_DATA02	
51	EMC_22	SEMC_BA1	FLEXPWM3_PWMB03	LP12C3_SCL	LPUART4_A00	QTIMER2_TIMER3	GPIO4_IO22			FLEXSPI2_A_SS1_B of	
52	EMC_26	SEMC_CLK	FLEXPWM1_PWMB01	LPUART6_RX	ENET_RX_ER	FLEXIO1_FLEXIO12	GPIO4_IO26			FLEXSPI2_A_DATA00	
53	EMC_25	SEMC_RAS	FLEXPWM1_PWMA01	LPUART6_TX	ENET_TX_CLK	ENET_REF_CLK	GPIO4_IO25			FLEXSPI2_A_SCLK	
54	EMC_29	SEMC_CS0	FLEXPWM3_PWMA00	LPUART6_RTS_B	LPSP11_SDI	FLEXIO1_FLEXIO15	GPIO4_IO29			FLEXSPI2_A_DATA03	