Pin	Name	ALT0	ALt1	ALT2	ALT3	ALT4	Alt5	ALT6	ALT7	ALT8	ALT9
0	AD BO 03	FLEXCAN2 RX	XBAR1 INOUT17	LPUART6 RX	USB OTG1 OC	FLEXPWM1 PWMX01	GPIO1 1003	REF CLK 24M	LPSPI3 PCS0		
1	AD B0 02	FLEXCAN2 TX	XBAR1 INOUT16	LPUART6 TX	USB OTG1 PWR	LEXPWM1 PWMX0	GPIO1 1002	LPI2C1 HREQ	LPSPI3 SDI		
2	EMC 04	SEMC DATA04	FLEXPWM4 PWMA02	SAI2 TX DATA	XBAR1 INOUT06	FLEXIO1_FLEXIO04	GPIO4_IO04		_		
3	EMC 05	SEMC DATA05	FLEXPWM4 PWMB0	SAI2 TX SYNC	XBAR1 INOUT07	FLEXIO1 FLEXIO05	GPIO4_IO05				
4	EMC 06	SEMC DATA06	FLEXPWM2 PWMA00	SAI2 TX BCLK	XBAR1 INOUT08	FLEXIO1 FLEXIO06	GPIO4 1006				
5	EMC 08	SEMC DM00	FLEXPWM2 PWMA01	SAI2 RX DATA	XBAR1_INOUT17	FLEXIO1 FLEXIO08	GPIO4 1008				
6	BO 10	LCD DATA06	QTIMER4 TIMER1	FLEXPWM2 PWMA02	SAI1 TX DATA03	FLEXIO2 FLEXIO10	GPIO2 IO10	SRC BOOT CFG06		ENET2 CRS	
7	B1_01	LCD_DATA13	XBAR1_INOUT15	LPUART4_RX	SAI1_TX_DATA00	FLEXIO2_FLEXIO17	GPIO2_IO17	FLEXPWM1_PWMB03		ENET2_RDATA00	FLEXIO3_FLEXIO17
8	B1 00	LCD DATA12	XBAR1 INOUT14	LPUART4 TX	SAI1 RX DATA00	FLEXIO2 FLEXIO16	GPIO2 IO16	FLEXPWM1 PWMA03		ENET2 RX ER	FLEXIO3 FLEXIO16
9	BO 11	LCD DATA07	QTIMER4 TIMER2	FLEXPWM2 PWMB02	SAI1 TX DATA02	FLEXIO2 FLEXIO11	GPIO2 IO11	SRC BOOT CFG07		ENET2 COL	
10	B0_00	LCD_CLK	QTIMER1_TIMER0	MQS_RIGHT	LPSPI4_PCS0	FLEXIO2_FLEXIO00	GPIO2_IO00	SEMC_CSX01		ENET2_MDC	
11	B0_02	LCD_HSYNC	QTIMER1_TIMER2	FLEXCAN1_TX	LPSPI4_SDO	FLEXIO2_FLEXIO02	GPIO2_IO02	SEMC_CSX03		ENET2_1588_EVENT0_OUT	
12	B0_01	LCD_ENABLE	QTIMER1_TIMER1	MQS_LEFT	LPSPI4_SDI	FLEXIO2_FLEXIO01	GPIO2_IO01	SEMC_CSX02		ENET2_MDIO	
13	BO 03	LCD VSYNC	QTIMER2 TIMER0	FLEXCAN1 RX	LPSPI4 SCK	FLEXIO2 FLEXIO03	GPIO2 1003	WDOG2_RESET_B_DEB		ENET2 1588 EVENTO IN	
14/A0	AD B1 02	USB OTG1 ID	QTIMER3 TIMER2	LPUART2 TX	SPDIF OUT	ENET 1588 EVENT2 OUT	GPIO1 IO18	USDHC1 CD B	KPP ROW06	GPT2 CLK	FLEXIO3 FLEXIO02
15/A1	AD_B1_03	USB_OTG1_OC	QTIMER3_TIMER3	LPUART2_RX	SPDIF_IN	ENET_1588_EVENT2_IN	GPIO1_IO19	USDHC2_CD_B	KPP_COL06	GPT2_CAPTURE1	FLEXIO3_FLEXIO03
16/A2	AD_B1_07	FLEXSPIB_DATA00	LPI2C3_SCL	LPUART3_RX	SPDIF_EXT_CLK	CSI_HSYNC	GPIO1_IO23	USDHC2_DATA3	KPP_COL04	GPT2_COMPARE3	FLEXIO3_FLEXIO07
17/A3	AD_B1_06	FLEXSPIB_DATA01	LPI2C3_SDA	LPUART3_TX	SPDIF_LOCK	CSI_VSYNC	GPIO1_IO22	USDHC2_DATA2	KPP_ROW04	GPT2_COMPARE2	FLEXIO3_FLEXIO06
18/A4	AD_B1_01	USB_OTG1_PWR	QTIMER3_TIMER1	LPUART2_RTS_B	LPI2C1_SDA	CCM_PMIC_READY	GPIO1_IO17	USDHC1_VSELECT	KPP_COL07	ENET2_1588_EVENT0_IN	FLEXIO3_FLEXIO01
19/A5	AD_B1_00	USB_OTG2_ID	QTIMER3_TIMER0	LPUART2_CTS_B	LPI2C1_SCL	WDOG1_B	GPIO1_IO16	USDHC1_W	KPP_ROW07	ENET2_1588_EVENT0_OUT	FLEXIO3_FLEXIO00
20/A6	AD_B1_10	FLEXSPIA_DATA03	WDOG1_B	LPUART8_TX	SAI1_RX_SYNC	CSI_DATA07	GPIO1_IO26	USDHC2_WP	KPP_ROW02	ENET2_1588_EVENT1_OUT	FLEXIO3_FLEXIO10
21/A7	AD_B1_11	FLEXSPIA_DATA02	EWM_OUT_B	LPUART8_RX	SAI1_RX_BCL	CSI_DATA06	GPIO1_IO27	USDHC2_RESET_B	KPP_COL02	ENET2_1588_EVENT1_IN	FLEXIO3_FLEXIO11
22/A8	AD_B1_08	FLEXSPIA_SS1_B	FLEXPWM4_PWMA00	FLEXCAN1_TX	CCM_PMIC_READY	CSI_DATA09	GPIO1_IO24	USDHC2_CMD	KPP_ROW03		FLEXIO3_FLEXIO08
23/A9	AD_B1_09	FLEXSPIA_DQS	FLEXPWM4_PWMA01	FLEXCAN1_RX	SAI1_MCLK	CSI_DATA08	GPIO1_IO25	USDHC2_CLK	KPP_COL03		FLEXIO3_FLEXIO09
24/A10	AD_B0_12	LPI2C4_SCL	CCM_PMIC_READY	LPUART1_TX	WDOG2_WDOG_B	FLEXPWM1_PWMX02	GPIO1_IO12	ENET_1588_EVENT1_OUT	NMI_GLUE_NMI		
25/A11	AD_B0_13	LPI2C4_SDA	GPT1_CLK	LPUART1_RX	EWM_OUT_B	FLEXPWM1_PWMX03	GPIO1_IO13	ENET_1588_EVENT1_IN	REF_CLK_24M		
26/A12	AD_B1_14	FLEXSPIA_SCLK	ACMP_OUT02	LPSPI3_SDO	SAI1_TX_BCLK	CSI_DATA03	GPIO1_IO30	USDHC2_DATA6	KPP_ROW00	ENET2_1588_EVENT3_OUT	FLEXIO3_FLEXIO14
27/A13	AD_B1_15	FLEXSPIA_SSO_B	ACMP_OUT03	LPSPI3_SCK	SAI1_TX_SYNC	CSI_DATA02	GPIO1_IO31	USDHC2_DATA7	KPP_COL00	ENET2_1588_EVENT3_IN	FLEXIO3_FLEXIO15
28	EMC_32	SEMC_DATA10	FLEXPWM3_PWMB01	LPUART7_RX	CCM_PMIC_RDY	CSI_DATA21	GPIO3_IO18			ENET2_TX_EN	
29	EMC_31	SEMC_DATA09	FLEXPWM3_PWMA01	LPUART7_TX	LPSPI1_PCS1	CSI_DATA22	GPIO4_IO31			ENET2_TDATA01	
30	EMC_37	SEMC_DATA15	XBAR1_IN23	GPT1_COMPARE3	SAI3_MCLK	CSI_DATA16	GPIO3_IO23	USDHC2_WP		ENET2_RX_EN	FLEXCAN3_RX
31	EMC_36	SEMC_DATA14	XBAR1_IN22	GPT1_COMPARE2	SAI3_TX_DATA	CSI_DATA17	GPIO3_IO22	USDHC1_WP		ENET2_RDATA01	FLEXCAN3_TX
32	B0_12	LCD_DATA08	XBAR1_INOUT10	ARM_TRACE_CLK	SAI1_TX_DATA01	FLEXIO2_FLEXIO12	GPIO2_IO12	SRC_BOOT_CFG08		ENET2_TDATA00	
33	EMC_07	SEMC_DATA07	FLEXPWM2_PWMB00	SAI2_MCLK	XBAR1_INOUT09	FLEXIO1_FLEXIO07	GPIO4_I007				
34	B1_13	WDOG1_B	LPUART5_RX	CSI_VSYNC	ENET_1588_EVENT0_OUT	FLEXIO2_FLEXIO29	GPIO2_IO29	USDHC1_WP		SEMC_DQS4	FLEXIO3_FLEXIO29
35	B1_12		LPUART5_TX	CSI_PIXCLK	ENET_1588_EVENT0_IN	FLEXIO2_FLEXIO28	GPIO2_IO28	USDHC1_CD_B	_		FLEXIO3_FLEXIO28
36	B1_02	LCD_DATA14	XBAR1_INOUT16	LPSPI4_PCS2	SAI1_TX_BCLK	FLEXIO2_FLEXIO18	GPIO2_IO18	FLEXPWM2_PWMA03		ENET2_RDATA01	FLEXIO3_FLEXIO18
37	B1_03	LCD_DATA15	XBAR1_INOUT17	LPSPI4_PCS1	SAI1_TX_SYNC	FLEXIO2_FLEXIO19	GPIO2_IO19	FLEXPWM2_PWMB03		ENET2_RX_EN	FLEXIO3_FLEXIO19
38/A14	AD_B1_12	FLEXSPIA_DATA01	ACMP_OUT00	LPSPI3_PCS0	SAI1_RX_DATA00	CSI_DATA05	GPIO1_IO28	USDHC2_DATA4	KPP_ROW01	ENET2_1588_EVENT2_OUT	FLEXIO3_FLEXIO12
39/A5	AD_B1_13	FLEXSPIA_DATA00	ACMP_OUT01	LPSPI3_SDI	SAI1_TX_DATA00	CSI_DATA04	GPIO1_IO29	USDHC2_DATA5	KPP_COL01	ENET2_1588_EVENT2_IN	FLEXIO3_FLEXIO13
40/A16	AD_B1_04	FLEXSPIB_DATA03	ENET_MDC	LPUART3_CTS_B	SPDIF_SR_CLK	CSI_PIXCLK	GPIO1_IO20	USDHC2_DATA0	KPP_ROW05	GPT2_CAPTURE2	FLEXIO3_FLEXIO04
41/A17	AD_B1_05	FLEXSPIB_DATA02	ENET_MDIO	LPUART3_RTS_B	SPDIF_OUT	CSI_MCLK	GPIO1_IO21	USDHC2_DATA1	KPP_COL05	GPT2_COMPARE1	FLEXIO3_FLEXIO05
42	SD_B0_03	USDHC1_DATA1	FLEXPWM1_PWMB01	LPUART8_RTS_B	XBAR1_INOUT07	LPSPI1_SDI	GPIO3_IO15			ENET2_RDATA00	SEMC_CLK6
43	SD_B0_02	USDHC1_DATA0	FLEXPWM1_PWMA01	LPUART8_CTS_B	XBAR1_INOUT06	LPSPI1_SDO	GPIO3_IO14			ENET2_RX_ER	SEMC_CLK5
44	SD_B0_01	USDHC1_CLK	FLEXPWM1_PWMB00	LPI2C3_SDA	XBAR1_INOUT05	LPSPI1_PCS0	GPIO3_IO13	FLEXSPIB_SS1_B		ENET2_TX_CLK	ENET2_REF_CLK2
45	SD_B0_00	USDHC1_CMD	FLEXPWM1_PWMA00	LPI2C3_SCL	XBAR1_INOUT04	LPSPI1_SCK	GPIO3_IO12	FLEXSPIA_SS1_B		ENET2_TX_EN	SEMC_DQS4
46	SD_B0_05	USDHC1_DATA3	FLEXPWM1_PWMB02	LPUART8_RX	XBAR1_INOUT09	FLEXSPIB_DQS	GPIO3_IO17	CCM_CLKO2		ENET2_RX_EN	
47	SD_B0_04	USDHC1_DATA2	FLEXPWM1_PWMA02	LPUART8_TX	XBAR1_INOUT08	FLEXSPIB_SSO_B	GPIO3_IO16	CCM_CLKO1		ENET2_RDATA01	
48	EMC_24	SEMC_CAS	FLEXPWM1_PWMB00	LPUART5_RX	ENET_TX_EN	GPT1_CAPTURE1	GPIO4_IO24			FLEXSPI2_A_SSO_B	
49	EMC_27	SEMC_CKE	FLEXPWM1_PWMA02	LPUART5_RTS_B	LPSPI1_SCK	FLEXIO1_FLEXIO13	GPIO4_IO27			FLEXSPI2_A_DATA01	
50	EMC_28	SEMC_WE	FLEXPWM1_PWMB02	LPUART5_CTS_B	LPSPI1_SDO	FLEXIO1_FLEXIO14	GPIO4_IO28			FLEXSPI2_A_DATA02	
51	EMC_22	SEMC_BA1	FLEXPWM3_PWMB03	LPI2C3_SCL	ENET_TDATA00	QTIMER2_TIMER3	GPIO4_IO22			FLEXSPI2_A_SS1_B of	
52	EMC_26	SEMC_CLK	FLEXPWM1_PWMB01	LPUART6_RX	ENET_RX_ER	FLEXIO1_FLEXIO12	GPIO4_IO26			FLEXSPI2_A_DATA00	
53	EMC_25	SEMC_RAS	FLEXPWM1_PWMA01	LPUART6_TX	ENET_TX_CLK	ENET_REF_CLK	GPIO4_IO25			FLEXSPI2_A_SCLK	
54	EMC_29	SEMC_CS0	FLEXPWM3_PWMA00	LPUART6_RTS_B	LPSPI1_SDI	FLEXIO1_FLEXIO15	GPIO4_IO29			FLEXSPI2_A_DATA03	

IOMUXC xxx SELECT INPUT

IOMUXC_x	xx_SELECT_INF	PUT									
Column1	Column2	Column3	Column4	Column5	Column6	Column7	Column8	Column9	Column10	Column11	Column12
			IOMUXC_XBAR1_IN17_SE	IOMUXC_LPUART6_RX_SELE							
0	AD_B0_03	FLEXCAN2_RX	LECT_INPUT=0x1	CT_INPUT=1	USB_OTG1_OC	FLEXPWM1_PWMX01	GPIO1_I003	REF_CLK_24M	LPSPI3_PCS0		
				IOMUXC_LPUART6_TX_SELE							
1	AD BO 02	FLEXCAN2 TX	XBAR1 INOUT16	CT INPUT=1	USB OTG1 PWR	LEXPWM1 PWMX0	GPIO1 1002	LPI2C1 HREQ	LPSPI3 SDI		
					IOMUXC_XBAR1_IN06_SELE						
2	EMC 04	SEMC DATA04	FLEXPWM4 PWMA02	SAI2 TX DATA	CT INPUT=0x0	FLEXIO1_FLEXIO04	GPIO4 1004				
					IOMUXC XBAR1 IN07 SELE						
3	EMC_05	SEMC_DATA05	FLEXPWM4_PWMB0	SAI2_TX_SYNC	CT_INPUT=0x0	FLEXIO1_FLEXIO05	GPIO4_I005				
					IOMUXC_XBAR1_IN08_SELE						
4	EMC 06	SEMC DATA06	FLEXPWM2_PWMA00	SAI2 TX BCLK	CT INPUT=0x0	FLEXIO1 FLEXIO06	GPIO4_IO06				
			_		IOMUXC_XBAR1_IN17_SELE		_				
5	EMC_08	SEMC_DM00	FLEXPWM2_PWMA01	SAI2_RX_DATA	CT_INPUT=0x0	FLEXIO1_FLEXIO08	GPIO4_IO08				
6	BO 10	LCD DATA06	QTIMER4_TIMER1	FLEXPWM2_PWMA02	SAI1_TX_DATA03	FLEXIO2 FLEXIO10	GPIO2_IO10	SRC_BOOT_CFG06		ENET2 CRS	
				IOMUXC_LPUART4_RX_SELE			_			-	
7	B1 01	LCD DATA13	XBAR1 INOUT15	CT INPUT=2	SAI1 TX DATA00	FLEXIO2 FLEXIO17	GPIO2 IO17	FLEXPWM1 PWMB03		ENET2 RDATA00	FLEXIO3 FLEXIO17
				IOMUXC_LPUART4_TX_SELE				-			
8	B1 00	LCD DATA12	XBAR1 INOUT14	CT INPUT=2	SAI1 RX DATA00	FLEXIO2 FLEXIO16	GPIO2 IO16	FLEXPWM1 PWMA03		ENET2 RX ER	FLEXIO3 FLEXIO16
9	B0_11	LCD_DATA07	QTIMER4_TIMER2	FLEXPWM2_PWMB02	SAI1_TX_DATA02	FLEXIO2 FLEXIO11	GPIO2_IO11	SRC_BOOT_CFG07		ENET2 COL	
10	B0_00	LCD_CLK	QTIMER1_TIMER0	MQS_RIGHT	LPSPI4_PCS0	FLEXIO2_FLEXIO00	GPIO2_IO00	SEMC_CSX01		ENET2_MDC	
11	B0_00	LCD_HSYNC	QTIMER1_TIMER2	FLEXCAN1_TX	LPSPI4_SDO	FLEXIO2_FLEXIO02	GPIO2_IO02	SEMC_CSX03		ENET2_1588_EVENT0_OUT	
12	B0_02	LCD_ENABLE	QTIMER1 TIMER1	MQS LEFT	LPSPI4_SDI	FLEXIO2_FLEXIO02	GPIO2_IO01	SEMC_CSX02		ENET2_IS88_EVENTO_OOT	
13	BO 03	LCD_ENABLE	QTIMER2 TIMER0	FLEXCAN1_RX	LPSPI4_SCK	FLEXIO2_FLEXIO03	GPIO2_IO03	WDOG2 RESET B DEB		ENET2 1588 EVENTO IN	
10	50_03	205_101110	QTIMENE_TIMENO	IOMUXC_LPUART2_TX_SELE		TELMOL_TELMOOD	0.102_1005	***************************************			
14/A0	AD B1 02	USB OTG1 ID	QTIMER3 TIMER2	CT INPUT=1	SPDIF OUT	ENET 1588 EVENT2 OUT	GPIO1 IO18	USDHC1 CD B	KPP ROW06	GPT2 CLK	FLEXIO3 FLEXIO02
14/10	AD_D1_02	030_0101_10	QTIMENS_TIMENZ	IOMUXC_LPUART2_RX_SELE		ENET_1300_EVENT2_001	01101_1010	OSBNCI_CB_B	KI I _KOWOO	GI IZ_CER	TEEXIOS_TEEXIOOE
15/A1	AD B1 03	USB OTG1 OC	QTIMER3 TIMER3	CT INPUT=1	SPDIF IN	ENET 1588 EVENT2 IN	GPIO1 IO19	USDHC2 CD B	KPP COL06	GPT2 CAPTURE1	FLEXIO3 FLEXIO03
25//12	7.5_51_65	000_0101_00	QTITLE CONTROL OF THE	IOMUXC LPUART3 RX SELE		2.12.1_1500_212.1112	0.101_1015	0001102_00_0	M.1_00200	0. 12_0/11 101121	122/1100_122/11000
16/A2	AD B1 07	FLEXSPIB DATA00	LPI2C3_SCL	CT_INPUT=0	SPDIF EXT CLK	CSI_HSYNC	GPIO1_IO23	USDHC2 DATA3	KPP COL04	GPT2 COMPARE3	FLEXIO3 FLEXIO07
10/742	AD_D1_07	TEENSTIB_BATAGO	11203_301	IOMUXC_LPUART3_TX_SELE		CSI_IISTIVE	01101_1025	OSBNCZ_BANAS	KI I _COLO4	di 12_colvii Altes	TEEXIOS_TEEXIOO7
17/A3	AD R1 06	FLEXSPIB_DATA01	LPI2C3_SDA	CT_INPUT=0	SPDIF_LOCK	CSI_VSYNC	GPIO1_IO22	USDHC2_DATA2	KPP_ROW04	GPT2_COMPARE2	FLEXIO3_FLEXIO06
18/A4		USB_OTG1_PWR	QTIMER3_TIMER1	LPUART2_RTS_B	LPI2C1_SDA	CCM_PMIC_READY	GPIO1_IO17	USDHC1_VSELECT	KPP_COL07	ENET2_1588_EVENTO_IN	FLEXIO3_FLEXIO01
19/A5		USB_OTG2_ID	QTIMER3_TIMER0	LPUART2_CTS_B	LPI2C1_SCL	WDOG1_B	GPIO1_IO16	USDHC1_W	KPP ROW07	ENET2_1588_EVENT0_OUT	FLEXIO3_FLEXIO00
13/13	AD_BI_00	030_0102_10	QTIMENS_TIMENO	IOMUXC_LPUART8_TX_SELE		WDOGI_B	GF101_1010	USDITCI_W	KFF_ROWO7		TELXIOS_TELXIOOO
20/A6	AD R1 10	FLEXSPIA_DATA03	WDOG1_B	CT_INPUT=1	SAI1_RX_SYNC	CSI_DATA07	GPIO1_IO26	USDHC2_WP	KPP_ROW02	ENET2_1588_EVENT1_OUT	ELEVIOS ELEVIOSO
20/70	AD_B1_10	TELXSFIA_DATA05	WDOGI_B	IOMUXC_LPUART8_RX_SELE		CSI_DATAO7	GF101_1020	USDITEZ_WF	KFF_ROW02	ENET2_1388_EVENT1_001	TELNIOS_TELNIOTO
21/A7	AD D1 11	FLEXSPIA_DATA02	EWM_OUT_B	CT_INPUT=1	SAI1_RX_BCL	CSI DATA06	GPIO1_IO27	USDHC2_RESET_B	KPP COL02	ENET2_1588_EVENT1_IN	FLEXIO3 FLEXIO11
21/A7 22/A8		FLEXSPIA SS1 B	FLEXPWM4 PWMA00	FLEXCAN1 TX	CCM PMIC READY	CSI_DATA00	GPIO1_IO27	USDHC2_RESET_B	KPP ROW03	ENETZ_1388_EVENT1_IN	FLEXIO3 FLEXIO18
23/A9	AD_B1_00	FLEXSPIA_DQS	FLEXPWM4_PWMA01	FLEXCAN1_RX	SAI1 MCLK	CSI DATA08	GPIO1_IO25	USDHC2_CLK	KPP_COL03		FLEXIOS_FLEXIO09
24/A10	AD_B1_03		CCM_PMIC_READY	LPUART1 TX	WDOG2_WDOG_B	FLEXPWM1_PWMX02	GPIO1_IO12	ENET_1588_EVENT1_OUT	NMI_GLUE_NMI		TEEMIOS_TEEMIOOS
25/A11		LPI2C4_SDA	GPT1 CLK	LPUART1_RX	EWM_OUT_B	FLEXPWM1_PWMX03	GPIO1_IO13	ENET_1588_EVENT1_IN	REF CLK 24M		
26/A12		FLEXSPIA_SCLK	ACMP_OUT02	LPSPI3_SDO	SAI1_TX_BCLK	CSI_DATA03	GPIO1_IO30	USDHC2_DATA6	KPP ROW00	ENET2_1588_EVENT3_OUT	FLEXIO3_FLEXIO14
27/A13		FLEXSPIA SSO B	ACMP_OUT03	LPSPI3_SCK	SAI1_TX_SYNC	CSI DATA02	GPIO1_IO31	USDHC2_DATA7	KPP_COL00	ENET2 1588 EVENT3 IN	FLEXIO3_FLEXIO15
27/113	.10_01_13	. 22/3/ I/_330_B	7.0.01 _00100	IOMUXC_LPUART7_RX_SELE		CODATAO2	51101_1031	OSSITUE_DATA!	1_00100	2.12.2_1300_EVERY13_HV	· cc/ioo_i cc/ioio
28	EMC_32	SEMC DATA10	FLEXPWM3_PWMB01	CT INPUT=1	CCM PMIC RDY	CSI DATA21	GPIO3_IO18			ENET2 TX EN	
20	LIVIC_32	SEIVIC_DATATO	. CEXT VVIVIS_F VVIVIDOI	OMUXC_LPUART7_TX_SELE	CC.M_T WITC_RDT	CODATALL	31 103_1018				
29	EMC 31	SEMC DATA09	FLEXPWM3 PWMA01	CT INPUT=1	LPSPI1 PCS1	CSI DATA22	GPIO4_IO31			ENET2 TDATA01	
2.3	LIVIC_31	JEIVIC_DATA03	IOMUXC_XBAR1_IN23_SE	C1_1147 O1=1	LI 31 11_F C31	COI_DATAZZ	GF104_1031			LIVETZ_TOATAOT	
30	EMC_37	SEMC_DATA15	LECT_INPUT=0x0	GPT1_COMPARE3	SAI3_MCLK	CSI_DATA16	GPIO3_IO23	USDHC2_WP		ENET2_RX_EN	FLEXCAN3_RX
30	LIVIC_37	PEINIC_DATATS			JUID INICEK	C3I_DMIM10	GF103_1023	U3DITCZ_VVF		LINE IZ_RA_EIN	I LLACAINS_NA
31	EMC 36	SEMC DATA14	IOMUXC_XBAR1_IN22_SE LECT_INPUT=0x0	GPT1 COMPARE2	CAID TV DATA	CSI DATA17	GPIO3 IO22	USDHC1 WP		ENETS DDATAGS	FLEXCAN3 TX
32					SAI3_TX_DATA	_ _				ENET2_RDATA00	FLEXCANS_IX
32	B0_12	LCD_DATA08	XBAR1_INOUT10	ARM_TRACE_CLK	SAI1_TX_DATA01	FLEXIO2_FLEXIO12	GPIO2_IO12	SRC_BOOT_CFG08		ENET2_TDATA00	
22	ENAC 07	CEMC DATAO7	ELEVENAMA DIAMAROO	CAID MCIK	IOMUXC_XBAR1_IN09_SELE	FLEVIOL FLEVIOUS	CDIO4 1007				
33	EMC_07	SEMC_DATA07	FLEXPWM2_PWMB00	SAI2_MCLK	CT_INPUT=0x0	FLEXIO1_FLEXIO07	GPIO4_I007				
24	D4 42	WDOC4 D	IOMUXC_LPUART5_RX_S	CCL VICANIC	ENET AFOO EVENTO CUT	FLEVIOR FLEVIOR	CD102 1022	LICDUCA MID		CENAC DOCA	FLEVIOR FLEVIORS
34	B1_13	WDOG1_B	ELECT_INPUT=1	CSI_VSYNC	ENET_1588_EVENTO_OUT	FLEXIO2_FLEXIO29	GPIO2_IO29	USDHC1_WP		SEMC_DQS4	FLEXIO3_FLEXIO29
25			IOMUXC_LPUART5_TX_SE		ENET 4500 EVENTO	ELEMAN ELEMAN	68163 1655	LICELICA CE E			ELEVICA ELEVICA
35	B1_12		LECT_INPUT=1	CSI_PIXCLK	ENET_1588_EVENTO_IN	FLEXIO2_FLEXIO28	GPIO2_IO28	USDHC1_CD_B			FLEXIO3_FLEXIO28
36	B1_02	LCD_DATA14	XBAR1_INOUT16	LPSPI4_PCS2	SAI1_TX_BCLK	FLEXIO2_FLEXIO18	GPIO2_IO18	FLEXPWM2_PWMA03		ENET2_RDATA01	FLEXIO3_FLEXIO18
27	D4 65		IOMUXC_XBAR1_IN17_SE		5.14 TV 0.015	ELEVICO ELEVICATE	68105 151	ELEVELIA 40		ENETA DV EN	5151400 515145
37	B1_03	LCD_DATA15	LECT_INPUT=0x3	LPSPI4_PCS1	SAI1_TX_SYNC	FLEXIO2_FLEXIO19	GPIO2_IO19	FLEXPWM2_PWMB03	VDD DOLLOG	ENET2_RX_EN	FLEXIO3_FLEXIO19
38/A14	AD_B1_12	FLEXSPIA_DATA01	ACMP_OUT00	LPSPI3_PCS0	SAI1_RX_DATA00	CSI_DATA05	GPIO1_IO28	USDHC2_DATA4	KPP_ROW01	ENET2_1588_EVENT2_OUT	FLEXIO3_FLEXIO12

39/A5	AD B1 13	FLEXSPIA DATA00	ACMP OUT01	LPSPI3 SDI	SAI1 TX DATA00	CSI DATA04	GPIO1 IO29	USDHC2 DATA5	KPP COL01	ENET2 1588 EVENT2 IN	FLEXIO3 FLEXIO13
40/A16	AD B1 04	FLEXSPIB DATA03	ENET MDC	LPUART3 CTS B	SPDIF SR CLK	CSI PIXCLK	GPIO1 IO20	USDHC2 DATA0	KPP ROW05	GPT2 CAPTURE2	FLEXIO3 FLEXIO04
41/A17		_	ENET MDIO	LPUART3 RTS B	SPDIF OUT	CSI MCLK	GPIO1 IO21	USDHC2 DATA1	KPP COL05	GPT2 COMPARE1	FLEXIO3 FLEXIO05
,					IOMUXC XBAR1 IN07 SELE						
42	SD B0 03	USDHC1 DATA1	FLEXPWM1 PWMB01	LPUART8 RTS B	CT INPUT=0x1	LPSPI1 SDI	GPIO3 IO15			ENET2 RDATA00	SEMC CLK6
			_		IOMUXC XBAR1 IN06 SELE		_				
43	SD B0 02	USDHC1 DATA0	FLEXPWM1 PWMA01	LPUART8 CTS B	CT INPUT=0x1	LPSPI1 SDO	GPIO3 IO14			ENET2 RX ER	SEMC CLK5
					IOMUXC XBAR1 IN05 SELE						_
44	SD B0 01	USDHC1 CLK	FLEXPWM1 PWMB00	LPI2C3 SDA	CT INPUT=0x1	LPSPI1 PCS0	GPIO3 IO13	FLEXSPIB SS1 B		ENET2 TX CLK	ENET2 REF CLK2
					IOMUXC_XBAR1_IN04_SELE						
45	SD_B0_00	USDHC1_CMD	FLEXPWM1_PWMA00	LPI2C3_SCL	CT_INPUT=0x1	LPSPI1_SCK	GPIO3_IO12	FLEXSPIA_SS1_B		ENET2_TX_EN	SEMC_DQS4
				IOMUXC_LPUART8_RX_SELE	IOMUXC_XBAR1_IN09_SELE						
46	SD_B0_05	USDHC1_DATA3	FLEXPWM1_PWMB02	CT_INPUT=0	CT_INPUT=0x1	FLEXSPIB_DQS	GPIO3_IO17	CCM_CLKO2		ENET2_RX_EN	
				IOMUXC_LPUART8_TX_SELE	IOMUXC_XBAR1_IN08_SELE						
47	SD_B0_04	USDHC1_DATA2	FLEXPWM1_PWMA02	CT_INPUT=0	CT_INPUT=0x1	FLEXSPIB_SSO_B	GPIO3_IO16	CCM_CLKO1		ENET2_RDATA01	
				IOMUXC_LPUART5_RX_SELE							
48	EMC_24	SEMC_CAS	FLEXPWM1_PWMB00	CT_INPUT=0	ENET_TX_EN	GPT1_CAPTURE1	GPIO4_IO24			FLEXSPI2_A_SSO_B	
										IOMUXC_FLEXSPI2_IPP_INI)_
										IO_FA_BIT1_SELECT_INPUT	=
49	EMC_27	SEMC_CKE	FLEXPWM1_PWMA02	LPUART5_RTS_B	LPSPI1_SCK	FLEXIO1_FLEXIO13	GPIO4_IO27			1	
										IOMUXC_FLEXSPI2_IPP_INI)_
										IO_FA_BIT2_SELECT_INPUT	=
50	EMC_28	SEMC_WE	FLEXPWM1_PWMB02	LPUART5_CTS_B	LPSPI1_SDO	FLEXIO1_FLEXIO14	GPIO4_IO28			1	
51	EMC_22	SEMC_BA1	FLEXPWM3_PWMB03	LPI2C3_SCL	ENET_TDATA00	QTIMER2_TIMER3	GPIO4_IO22			FLEXSPI2_A_SS1_B of	
										IOMUXC_FLEXSPI2_IPP_INI)_
				IOMUXC_LPUART6_RX_SELE						IO_FA_BITO_SELECT_INPUT	=
52	EMC_26	SEMC_CLK	FLEXPWM1_PWMB01	CT_INPUT=0	ENET_RX_ER	FLEXIO1_FLEXIO12	GPIO4_IO26			1	
				IOMUXC_LPUART6_TX_SELE						IOMUXC_FLEXSPI2_IPP_INI)_
53	EMC_25	SEMC_RAS	FLEXPWM1_PWMA01	CT_INPUT=0	ENET_TX_CLK	ENET_REF_CLK	GPIO4_IO25			SCK_FA_SELECT_INPUT = 1	
										IOMUXC_FLEXSPI2_IPP_INI	
										IO_FA_BIT3_SELECT_INPUT	=
54	EMC_29	SEMC_CS0	FLEXPWM3_PWMA00	LPUART6_RTS_B	LPSPI1_SDI	FLEXIO1_FLEXIO15	GPIO4_IO29			1	