

Audio	Prop	Native	GPIO	Audio	FlexIO	Xbar	I2C	CAN	SPI	Serial	Analog	PWM	Digital		Digital	PWM	Analog	Serial	SPI	CAN	I2C	Xbar	FlexIO	Audio	GPIO	Native	Prop	Audio	
G	GND														Vin													5V	
		AD_B0_03	1.3			17		RX2	CS1	RX1		1X1	0		GND													G	G
		AD_B0_02	1.2			16		TX2	MISO1	TX1		1X0	1		3.3V 250mA max												3V	3.3	
S	EMC_04	4.4		O2	1:4	6						4A2	2		23	4A1	A9			RX1			3:9	MCL1	1.25	AD_B1_09	CS1_D8	A	
M	EMC_05	4.5		LR2	1:5	7						4B2	3		22	4A0	A8			TX1			3:08		1.24	AD_B1_08	CS1_D9		
A	EMC_06	4.6		BCL2	1:6	8						2A0	4		21		A7	RX5					3:11	BCL1	1.27	AD_B1_11	CS1_D6	A	
A	A-EN	EMC_08	4.8	IN2	1:8	17						2A1	5		20		A6	TX5					3:10	LRC1	1.26	AD_B1_10	CS1_D7	A	
M-CS	B0_10	2.10		O1D	2:10							2A2, Q41	6		19	Q30	A5	CTS3			SCL0		3:00		1.16	AD_B1_00	S	C	
L-EN	B1_01	2.17		O1A	2:17, 3:17	15				RX2		1B3	7		18	Q31	A4				SDA0		3:01		1.17	AD_B1_01	S	C	
	B1_00	2.16		IN1	2:16, 3:16	14	sda0			TX2		1A3	8		17		A3	TX4			SDA1		3:06		1.22	AD_B1_06	CS1_VSYNC		
	B0_11	2.11		O1C	2:11							2B2,Q42	9		16		A2	RX4			SCL1		3:07		1.23	AD_B1_07	CS1_HSYNC		
S	B0_00	2.0		MQR	2:0				CS0			Q10	10		15	Q33	A1	RX3					3:03	SPDI	1.19	AD_B1_03		V	
SM	M/L	B0_02	2.2		2:2			TX1	MOSI0			Q12	11		14	Q32	A0	TX3					3:02	SPDO	1.18	AD_B1_02			
SM	M	B0_01	2.1		MQL	2:1			MISO0			Q11	12		13	Q20	LED		SCK0	rx1			2:03		2.3	B0_03	M	SM	
												3.3V			GND														
		AD_B0_12	1.12				SCL2			TX6	A10-1	1X2	24		41	G21	A17						3:5		1.21	AD_B1_05	CS1_MCLK		
		AD_B0_13	1.13				SDA2			RX6	A11-1	1X3	25		40		A16						3:4		1.20	AD_B1_04	CS1_PIXCLK		
CSI_D3	AD_B1_14	1.30			3:14				MOSI1		A12-2		26		39		A15-2		MISO1				3:13		1.29	AD_B1_13	CS1_D4		
CSI_D2	AD_B1_15	1.31			3:15				SCK1		A13-2		27		38		A14-2		CS1-0				3:12		1.28	AD_B1_12	CS1_D5		
EMC_32	3.18									RX7		3B1	28		37	2B3			CS0-1			17	2:19,3:19		2.19	B1_03			
EMC_31	4.31									TX7		3A1	29		36	2A3			CS0-2			16	2:18,3:18		2.18	B1_02			
EMC_37	3.23					23			RX3			G13	30		35			TX8					2:28,3:28		2.28	B1_12	CS1_PIXCLK		
EMC_36	3.22					22			TX3			G12	31		34			RX8		RX1			2:29,3:29		2.29	B1_13	CS1_VSYNC		
	B0_12	2.12		O1B	2:12	10							32		33	2B0				TX1		9	1:7	MCL2	4.7	EMC_07			
SDIO Pins																													
	SD_B0_03	3.15		DATA1	7				MISO2			1B1	42		47	1A2		TX5				8	DATA2	3.16	SD_B0_04				
	SD_B0_02	3.14		DATA0	6				MOSI2	CTS5		1A1	43		46	1B2		RX5				9	DATA3	3.17	SD_B0_05				
	SD_B0_01	3.13		CLK	5	SDA1		CS2				1B0	44		45	1A0			SCK2		SCL1		4	CMD	3.12	SD_B0_00			
Back Memory Chips																													
	EMC_26	4.26			1:12					RX1		1B1	52		GND														
	EMC_25	4.25								TX1		1A1	53		50	1B2		CTS8	MOSI2				1:14		4.28	EMC_28			
	EMC_29	4.29			1:15				MISO2			3A0	54		49	1A2			SCK2				1:13		4.27	EMC_27			
															51	3B3,Q23					SCL1				4.22	EMC_22			
	EMC_26	4.26			1:12					RX1		1B1	52		GND														
	EMC_25	4.25								TX1		1A1	53		50	1B2		CTS8	MOSI2				1:14		4.28	EMC_28			
	EMC_29	4.29			1:15				MISO2			3A0	54		49	1A2			SCK2				1:13		4.27	EMC_27			
															48	1B0		RX8							4.24	EMC_24			