Audio	Prop	Native	GPIO	Audio	FlexIO	Xbar	Quad Encdr	CAN	SPI	Serial	Analog	PWM	Digital		Digital	PWM	Analog	Serial	IdS	[SC	Quad Encdr Xbar	FlexIO	Audio	Native	Prop	Audio
G	GND												GND	00 SV 5V 0	Vin										5V	
		AD_B0_03				17		RX		RX1		1X1	0		GND										G	G
		AD_B0_02				16		TX	2 MISO	TX1		1X0	1			250mA m									3V	3.3
_	S	EMC_04	4.4	O2	1:4	6						4A2	2	_ <b>√</b> □ □ □ □	23	4A1	A9		RX			3:9	MCL1 1.25		CSI_D8	Α
М		EMC_05	4.5	LR2	1:5	7						4B2	3		22	4A0	A8		T	(1		3:08		AD_B1_08	CSI_D9	_
_A		EMC_06		BCL2	1:6	8						2A0	4		21		A7	RX5				3:11		AD_B1_11	CSI_D6	Α
_A	A-EN	EMC_08	4.8	IN2	1:8	17						2A1	5	- 6 111 6 b	20		A6	TX5				3:10	LRC1 1.26		CSI_D7	Α
	M-CS	B0_10		O1D	2:10							2A2, Q41	6	111 [""] 6	19	Q30	A5	CTS3		SCL0		3:00		AD_B1_00	S	С
	L-EN	B1_01		_	2:17, 3:17					RX2		1B3	7	- Comment	18	Q31	A4			SDA0		3:01		AD_B1_01	S	С
		B1_00	2.16		2:16, 3:16	14	sda0			TX2		1A3	8		17			TX4		SDA1		3:06		AD_B1_06	CSI_VSYNC	_
		B0_11			2:11							2B2,Q42	9	- 🙀 · 🍃 🙀 -	16		A2	RX4		SCL1		3:07		AD_B1_07	CSI_HSYNC	_
S		B0_00		MQR	2:0				CS0			Q10	10	MIMXRT1862	15	Q33	A1	RX3				3:03	SPDI 1.19			V
SM		B0_02	2.2		2:2			TX	1 MOSIO			Q12	11	DVJ6A 0N00X	14	Q32	A0	TX3				3:02	SPDO 1.18			
SM	М	B0_01	2.1	MQL	2:1				MISO	)		Q11	12	CTAB1912J	13	Q20	LED		SCK0 rx	1		2:03	2.3	B0_03	М	SM
													3.3V		GND											_
		AD_B0_12					SCL				A10-1	1X2	24	- Carrier 1		GPT2-1	A17					3:5		AD_B1_05	CSI_MCLK	_
	201 52	AD_B0_13					SDA	2		_	A11-1	1X3	25	- A		GPT2-2	A16					3:4		AD_B1_04	CSI_PIXCLK	_
	CSI_D3	AD_B1_14			3:14				MOSI		A12-2		26	- S	39		A15-2		MISO1			3:13		AD_B1_13	CSI_D4	_
	CSI_D2	AD_B1_15			3:15				SCK1		A13-2		27		38		A14-2		CS1-0			3:12		AD_B1_12	CSI_D5	
		EMC_32								RX7		3B1	28		37	2B3			CS0-1		17	2:19,3:19	2.19	B1_03		
			4.31							TX7		3A1	29		36	2A3			CS0-2		16	2:18,3:18	2.18	B1_02		
		EMC_37	3.23			23		RX				G13	30	_ 🥳 105	35			TX8				2:28,3:28	2.28	B1_12	CSI_PIXCLK	_
		EMC_36	3.22			22		TX	3			G12	31		34			RX8				2:29,3:29	2.29	B1_13	CSI_VSYNC	
		B0_12	2.12	O1B	2:12	10							32	CHILLIAN CO.	33	2B0					9	1:7	MCL2 4.7	EMC_07		
														O INTELLEGIA												
														SDIO Pins												
		SD_B0_03	3.15		DATA1	7			MISO	2		1B1	42		47	1A2		TX5			8	DATA2	3.16	SD_B0_04		
		SD_B0_02	3.14	ı	DATA0	6			MOSI	CTS5		1A1	43		46	1B2		RX5			9	DATA3	3.17	SD_B0_05		
													GND		45	1A0			SCK2	SCL1	4	CMD	3.12	SD_B0_00		
		SD_B0_01	3.13	(	CLK	5	SDA	1	CS2			1B0	44				3.3V									
														Rack Memony Chins												
	F2A D0	EMC_26	4.26		1:12					RX1		1B1	52		GND											
		EMC_25	4.25			_				TX1		1A1	53		50	1B2		CTS8	MOSI2			1:14	4 28	EMC_28	F2A_D2	
	F2A_D3	EMC_29	4.29		1:15				MISO	2		3A0	54		49	1A2		0.00	SCK2			1:13			F2A_D1	
	1 2A_D3	LIVIC_23	4.23		1.10				IVIIOO	_			3.3V	دول ا		3B3,Q23			SCINE	SCL1		1.13			F2A_SS1_B	
														elic	91					JULI			4.22	LIVIU_ZZ	1 ZA_001_B	
	F24 - D2	EMC 20	4.00		1:12					RX1		1B1	F.0		GND											
	F2A_D0	EMC_26	4.26		1:12							1A1	52			1B2		OTOS	110010				4.55	E110 00	F04 B0	
	F2A_SCLK		4.25							TX1		3A0	53		50	1A2		CTS8				1:14			F2A_D2	
	F2A_D3	EMC_29	4.29		1:15				MISO	2			54 3.3V		49	1B0			SCK2			1:13	_		F2A_D1	_
													J.J V		48	160		RX8					4.24	EMC_24	F2A_SSO_B	