Audio	Prop	Native	GPIO	Audio	FlexIO	Xbar	15C	CAN	SPI	Serial	Analog	PWM	Digital	e l	Digital	PWM	Analog	Serial	SPI	CAN	12C	Xbar	FlexIO	Audio	GPIO	Native	Prop	Audio
G	GND												GND	■ VS	Vin												5V	
		AD_B0_03	1.3			17		RX2	CS1	RX1		1X1	0		GNE)											G	G
		AD_B0_02	1.2			16		TX2	MISO1	TX1		1X0	1	×1111.	3.3V	250m	A max										3V	3.3
	S	EMC_04	4.4	02	1:4	6						4A2	2		23	4A1	A9			RX1			3:9	MCL1	1.25	AD-B1_09		Α
М		EMC_05	4.5	LR2	1:5	7						4B2	3	20	22	4A0	A8			TX1			3:08		1.24	AD_B1_08		
Α		EMC_06	4.6	BCL2	1:6	8						2A0	4		21		A7	RX5					3:11	BCL1	1.27	AD_B1_11		Α
Α	A-EN	EMC_08	4.8	IN2	1:8	17						2A1	5	062 20	20		A6	TX5					3:10	LRC1	1.26	AD_B1_10		Α
	M-CS	B0_10	2.10	O1D	2:10							2A2, Q41	6	X 🕳 🔿	19	Q30	A5	CTS3			SCL0		3:00		1.16	AD_B1_00	S	С
	L-EN	B1_01	2.17	O1A	2:17, 3:17	15				RX2		1B3	7	co 🔵	18	Q31	A4				SDA0		3:01		1.17	AD_B1_01	S	С
		B1_00	2.16	IN1	2:16, 3:16	14	sda0			TX2		1A3	8	70	17		А3	TX4			SDA1		3:06		1.22	AD_B1_06		
		B0_11	2.11	01C	2:11							2B2,Q42	9	n a 🕦	16		A2	RX4			SCL1		3:07		1.23	AD_B1_07		
S		B0_00	2.0	MQR					CS0			Q10	10	5 0	15	Q33	A1	RX3					3:03	SPDI		AD_B1_03		V
SM	M/L	B0_02	2.2		2:2			TX1	MOSI0			Q12	11	= 0	14	Q32	A0	TX3					3:02	SPDO		AD_B1_02		
SM	М	B0_01	2.1	MQL	2:1				MISO0			Q11	12	000	13	Q20	LED		SCK0	rx1			2:03		2.3	B0_03		SM
														G ON														Ė
										Bottom	SMT Pir	าร																
		AD_B0_12	1.12				SCL2				A10-1	1X2	24	17 18	25	1X3	A11-1	RX6			SDA2				1.13	AD_B0_13		
		AD_B1_14	_		3:14				MOSI1		A12-2		26		27	_	A13-2		SCK1				3:15		1.31			
		EMC_32	3.18							RX7		3B1	28	1	29	3A1		TX7							4.31	EMC_31		
		EMC_37	3.23			23		RX3				G13	30			G12				TX3		22			3.22	EMC_36		
		B0_12	2.12	O1B	2:12	10							32			2B0				TX1		9	1:7	MCL2		EMC_07		
										Bottom	SDIO P	ins																
										Dotton	05.0																	
		SD_B0_03	3.15		DATA1	7			MISO2			1B1	34	.													\vdash	
		SD_B0_02			DATA0	6			MOSI2			1A1	35	(U														
							004						GND	6 5-													-	-
		SD_B0_01	3.13		CLK	5	SDA1		CS2			1B0	36	- tr													-	-
			I	I								0mA max		0+						-							-	-
		SD_B0_00			CMD	4	SCL1		SCK2			1A0	37	- 6													-	<u> </u>
		SD_B0_05			DATA3	9				RX5		1B2	38														_	<u> </u>
		SD_B0_04	3.16		DATA2	8				TX5		1A2	39															_
																											_	<u> </u>
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