

(\*) Building an 8-bit Breadboard Computer

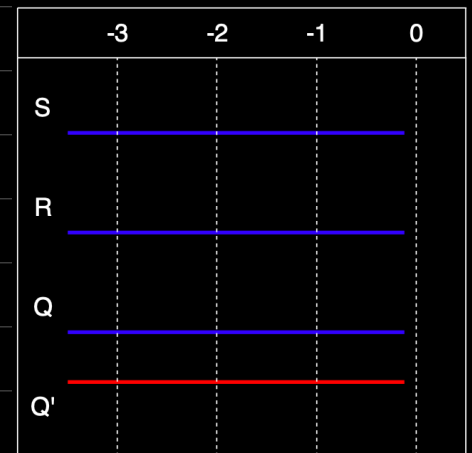
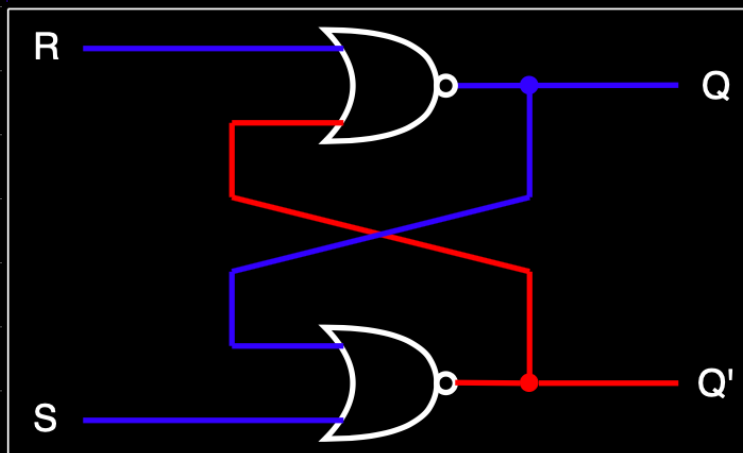
by Ben Eater

(<https://youtube.com/playlist?list=PLowKtXNTBypGqImE405J2565dvjafg1HU>)

^ SR Latch

(<https://youtu.be/KM0DdEaY5sY>)

Let the initial STABLE state be  $S = 0$ ,  $R = 0$ ,  $Q = 0$  and  $Q' = 1$ .



Let the PROPAGATION delays of the upper and lower NOR gates be 2 units and 3 units, respectively. So, the EFFECTS of the changed inputs will be visible only AFTER the propagation delays.

Now, let S get changed to 1 at  $t = 0$ .

