



Kushagra

Electronics Engineer

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SUMMARY

Diligent and well-trained combat professional with core VLSI expertise, skilled in maintaining equipment, finishing patrols, and briefing leadership. Safety-conscious and experienced in risk assessment, terrain evaluation, and implementing safety measures. Experienced in designing, testing, and integrating advanced VLSI systems for high-tech applications. Focused on ensuring optimal system performance and exceeding customer expectations, whether working independently or as part of a team.

SKILLS & TOOL'S

- Verilog
- VLSI
- C++
- Java
- C
- DBMS
- Communication skill
- Decision Making
- Xilinx ISE Design Suit
- Cadence
- Microwind3
- BlueJ IDE
- Arduino IDE
- Xilinx Vivado

CERTIFICATION

- Digital Electronics Certification**
Advance institute of Technology and management -2023
- Python Fundamentals Certification**
Great Learning Academy - 2020

ACHIVEMENT'S

- CodeChef silver Badge Div.4 Hackathon
- Certificate Earned 3* silver on Hackerrank for SQL Language
- 100+ programmes using Leet Code
- Xilinx verilog
- Cadence
- Xilinx Vivado
- NEO 6.0 certification

EXTRA-CURRICULAR

- KABADDI PLAYER**
- COMPETED AT THE INTER-SCHOOL AND STATE LEVELS, PLAYED KABADDI FOR MY SCHOOL FROM SIXTH TO TENTH
- CREATIVE INTEREST**
- USED TO DRAW ALOT IN MY LEISURE TIME.I DON'T STRIVE FOR EXCELLENCE IN MY SKETCHES, BUT IT GIVES ME ENERGY.
- VOLUNTEER EXPERIENCE**
(CHILDLINE INDIA FOUNDATION)
- ASSIST IN PREPARING AND GIVING AWAY FREE MEALS TO ELDERLY AND ORPHAN SHELTERS.
- LEAD A GROUP OF NUMEROUS OTHERS IN RAISING MONEY FOR THEIR IMPROVEMENT.

INTERNSHIP

Hexnbit EdTech Pvt. Ltd

23rd,May2022 to 3rd.July2022 (6Week)

Learning Outcome:

- Verilog Language, Digital basics & Xilinx tool Learning
- Hybrid experience working world
- Personality development
- Highly motivating experience

Goldwyn Limited Pvt. Ltd

From 14th June2019 to 16thJuly2019 (4Week)

Learning outcome:

- Worked on maintaining and fixing circuits
- Hands on multiple circuit components
- Analog 3-step Dimmer circuit deployment
- Worked on developing an analogue 3-step dimmer circuit.

PROJECTS

Router 1x3(Xilinx Verilog) 17th ,May2022 to 1st,July2022

- Among computer networks, a router is a networking device that forwards data packets.
- In the Internet, routers are responsible for "traffic directing" tasks.
- Transfers packets data from 1 user multiple users
- Used multilayered programming for planning architecture.

FLASH ADC(Cadence Virtuoso) 1st,Nov2022 to 8th,Dec2022

- Flash ADC on Cadence using Threshold Inverting Quantization (TIQ) comparators.
- A particular kind of analog-to-digital converter is a flash ADC, commonly referred to as a direct-conversion ADC.
- It primarily performs comparisons between analogue input voltage and a set of reference voltages.
- Presented this project for my CMOS VLSI design ETP exam, and received excellent feedback and "O" grade.

Traffic Light Controller(Cadence Verilog)

1st,June2022-18th,July2022

- If there are any vehicles on the way, a sensor will identify them and alter the traffic light to let them cross the highway.
- Since the highway has a higher priority than the farm, the light is always green otherwise.
- The traffic light controller is the first of the three key components of the traffic light system since it serves as the system's brain.
- The second part is the signal visualization or in simple words is signal face.
- The third part is the detector or sensor

EDUCATIONAL BACKGROUND

Perusing Bachelor of Technology

Institution: Lovely Professional University

Year of Graduation: 2024

02/2023-Present



CGPA:9.04

- Diploma: Electronics Engineering (Modern Consumer electronic Appliances)** Gained 84% and received a First Division with Honors from BTE-UP at Sunder Deep Group of Institution in Ghaziabad.

Completed in 2021

- High School: St. Paul's Academy, Ghaziabad**
With 89.64 % From ICSE Board

Completed in 2018.