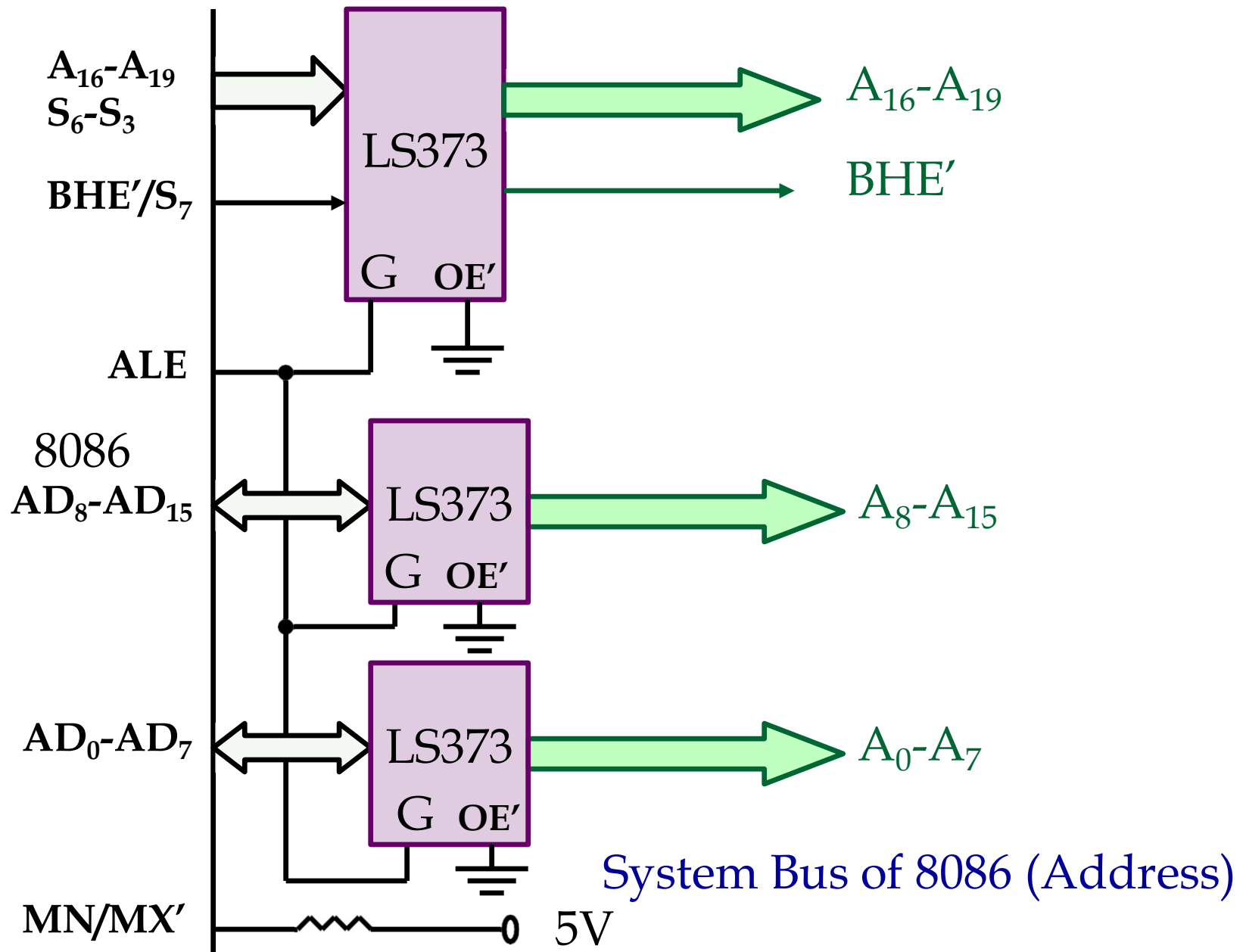
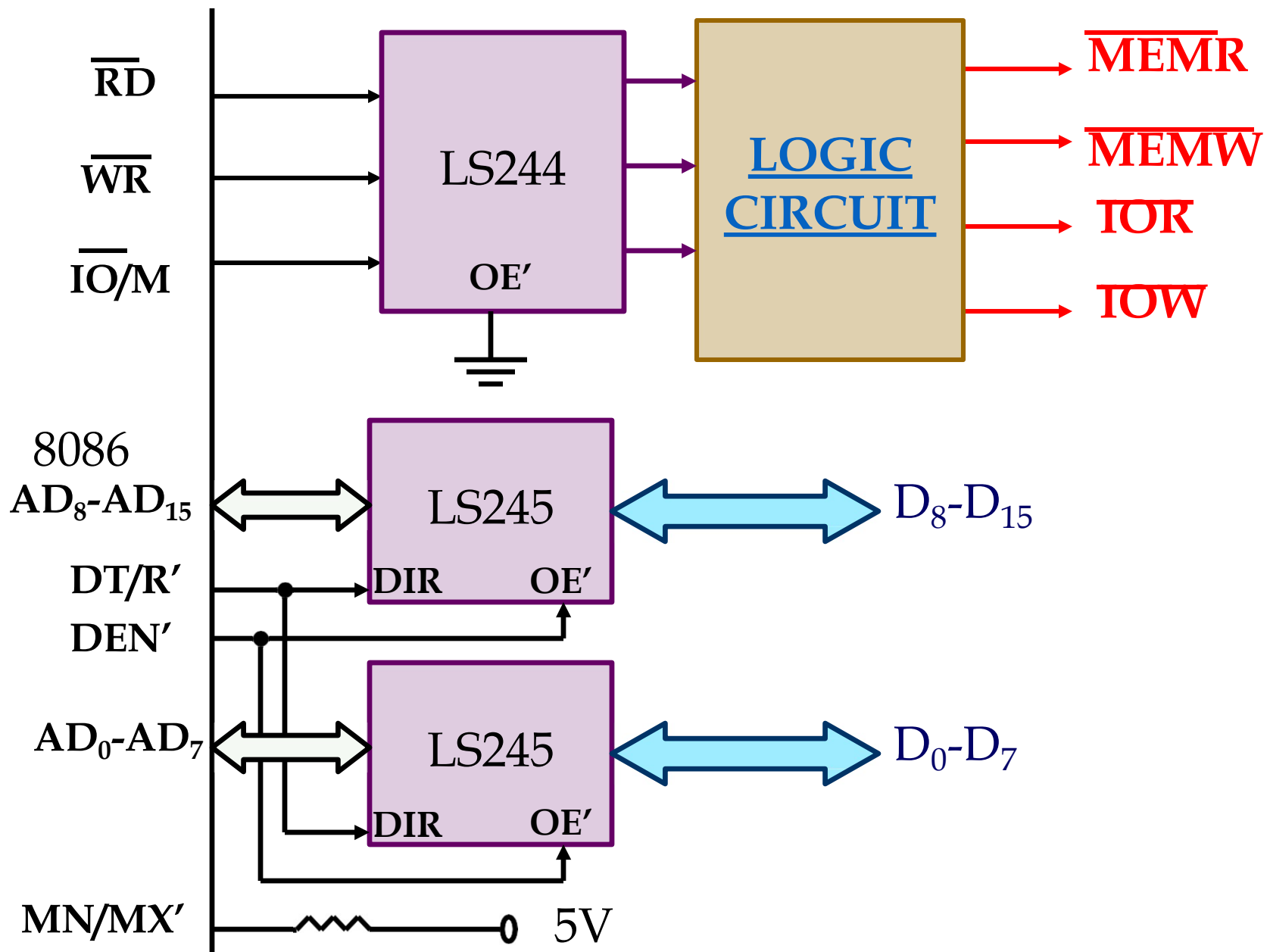
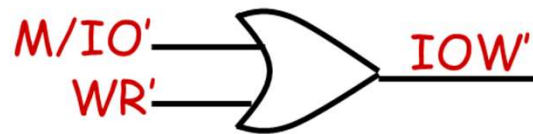
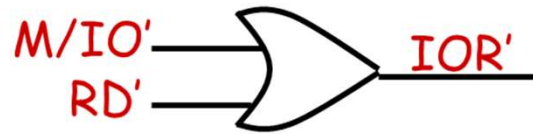


8086 Inputs

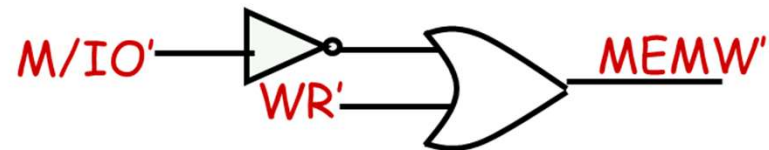
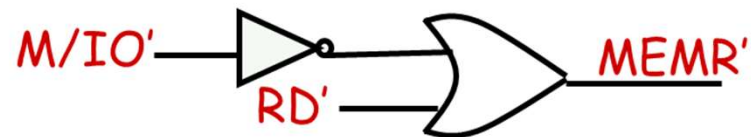


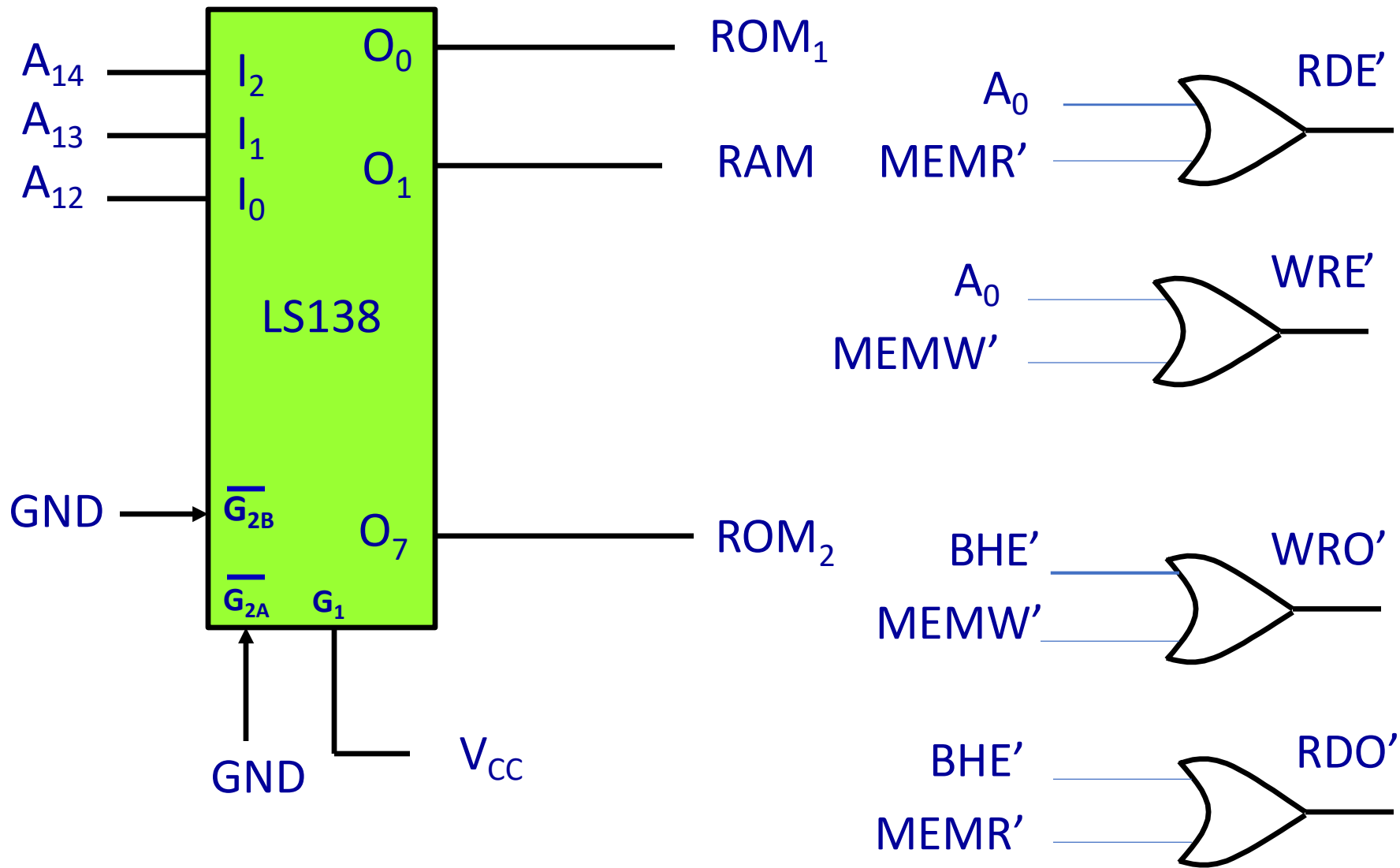


System Bus of 8086(Data + Control)

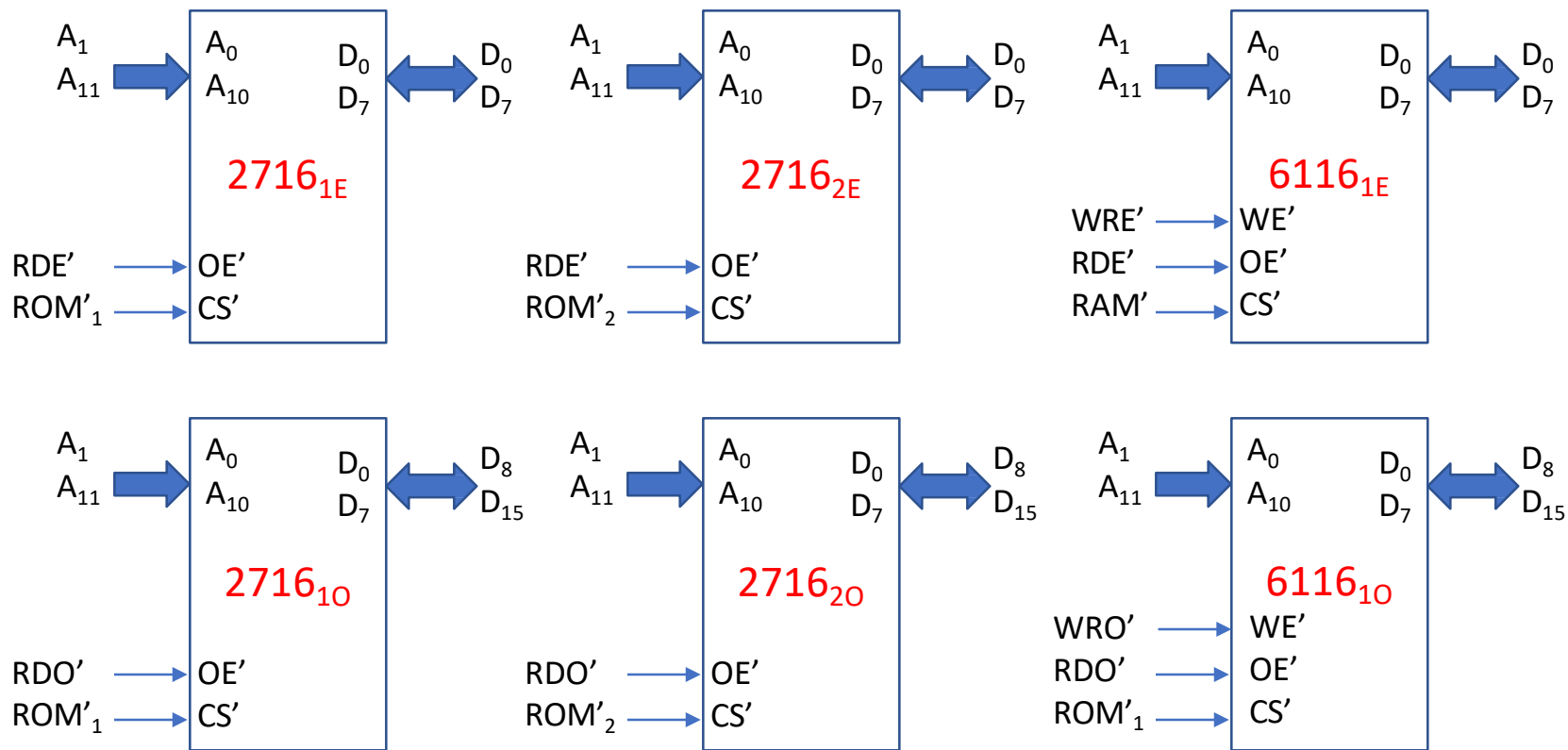


M/IO'	RD'	WR'	Bus cycle
1	0	1	$MEMR'$
1	1	0	$MEMW'$
0	0	1	IOR'
0	1	0	IOW'

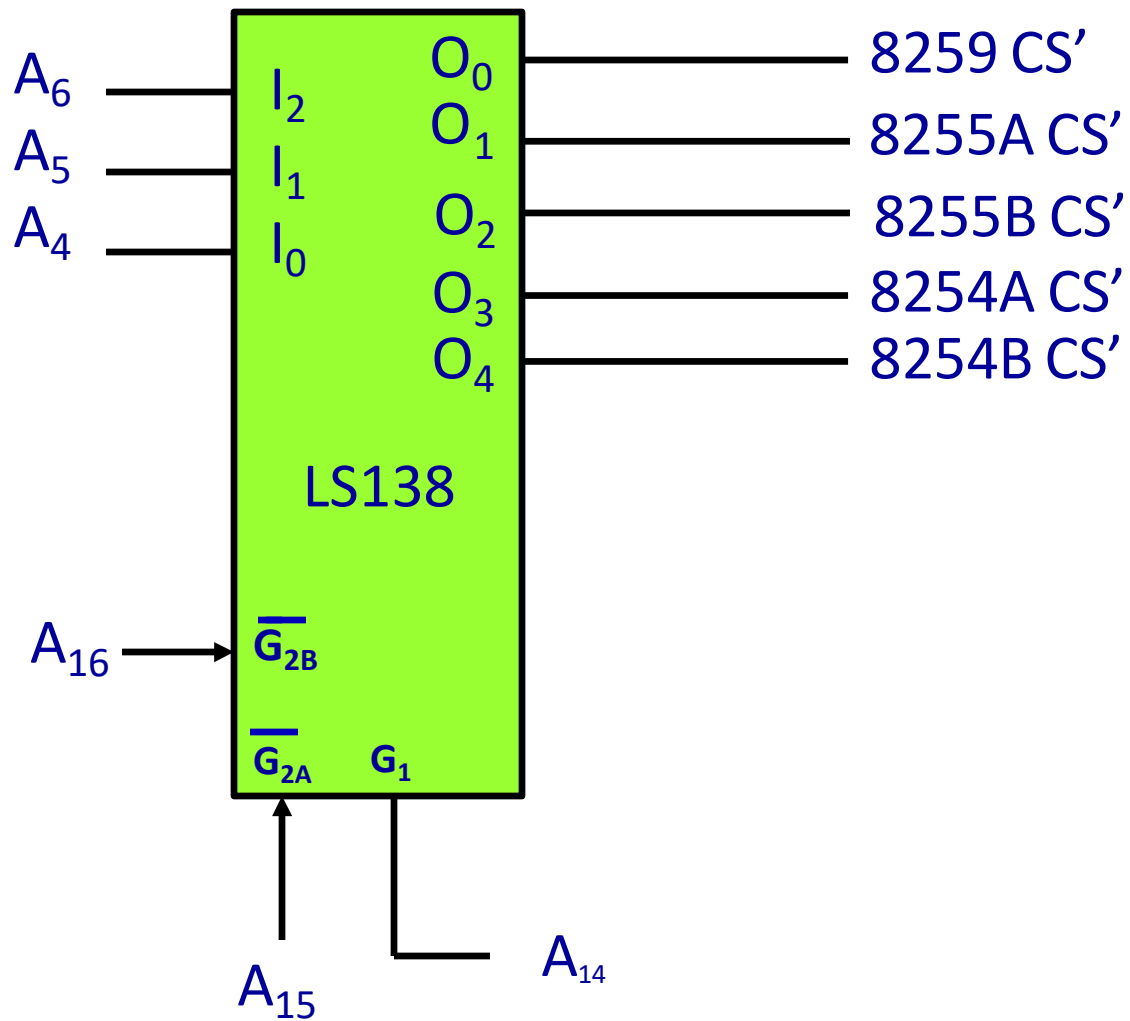




Memory Decoder

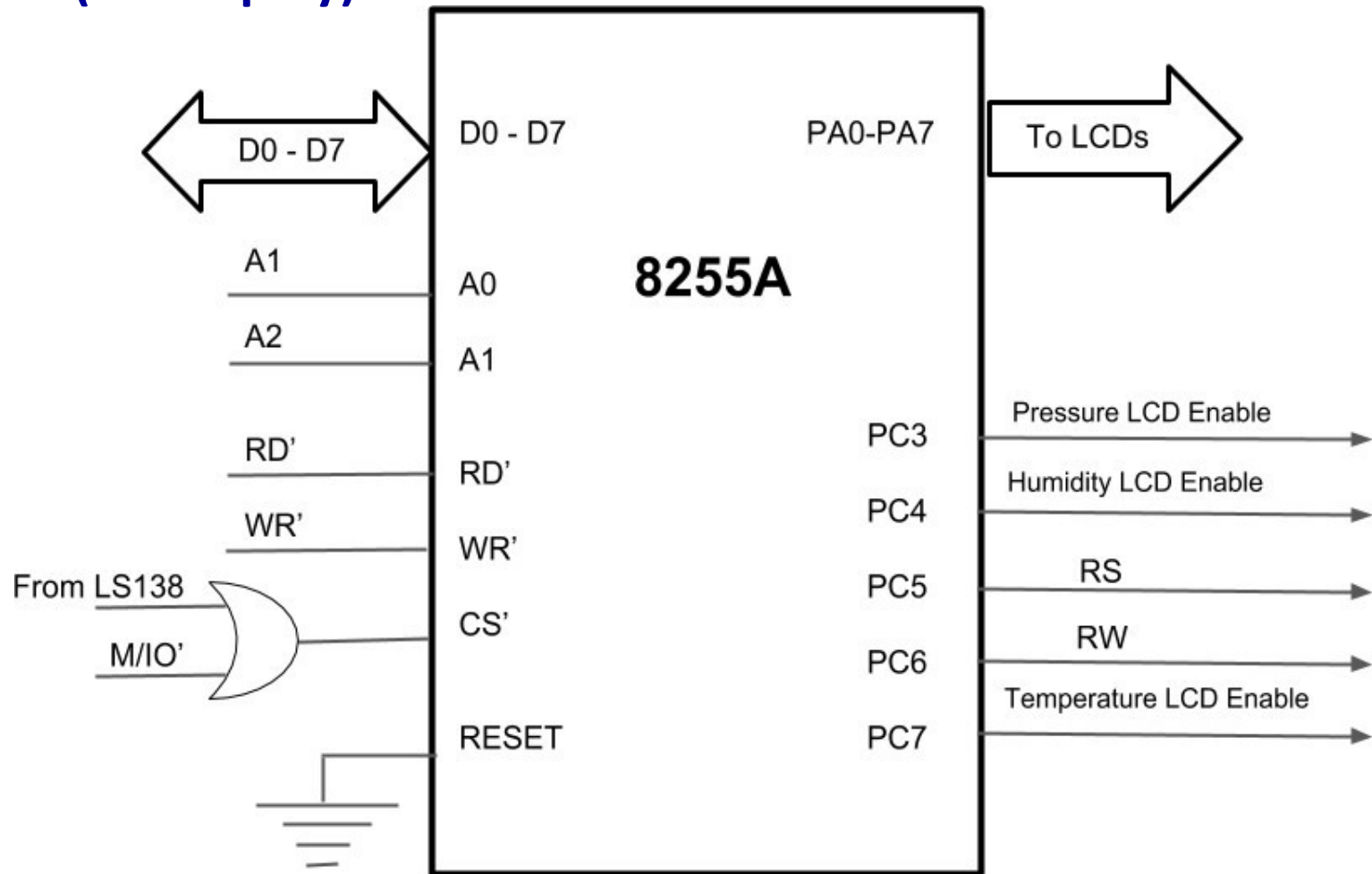


Memory Interfacing

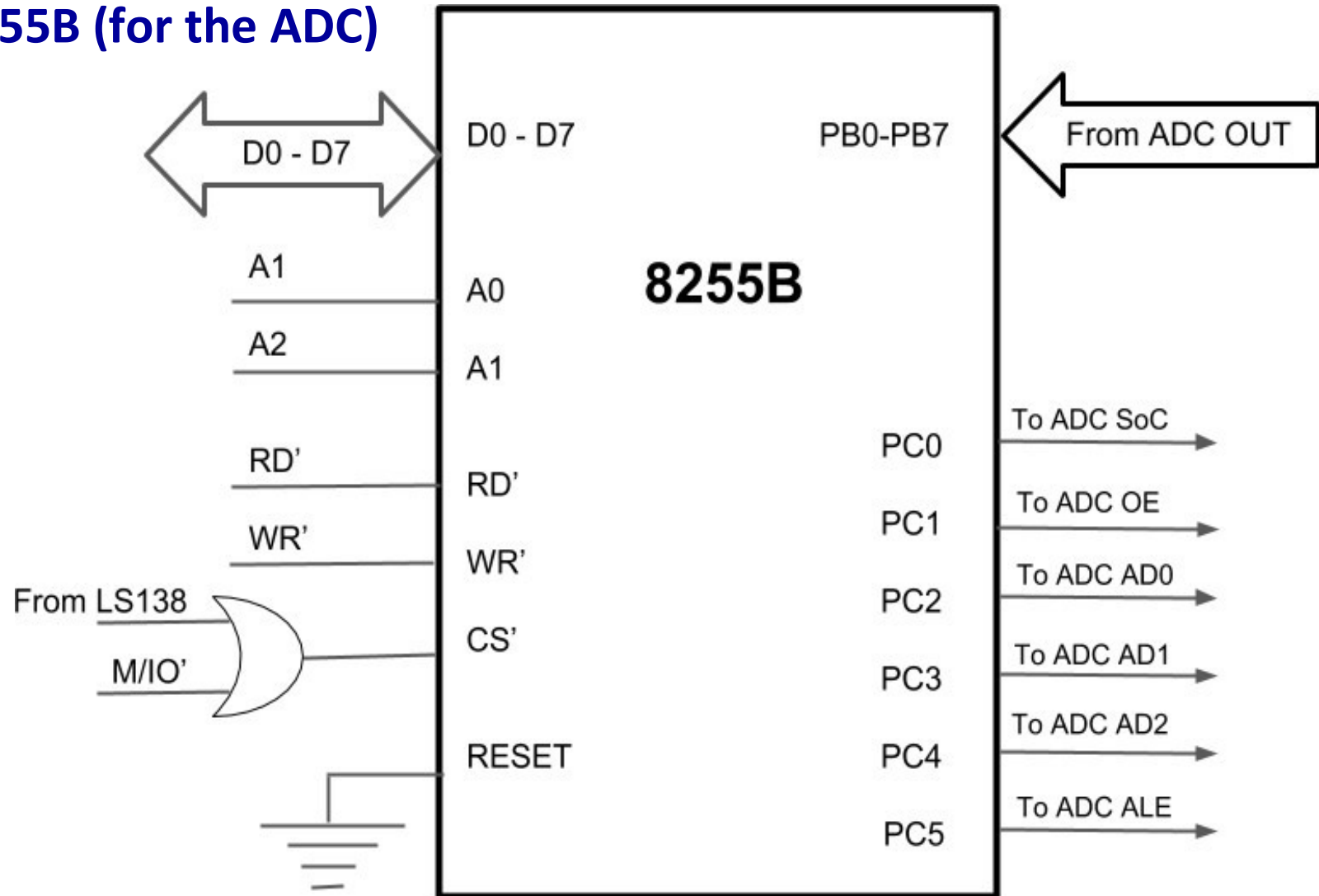


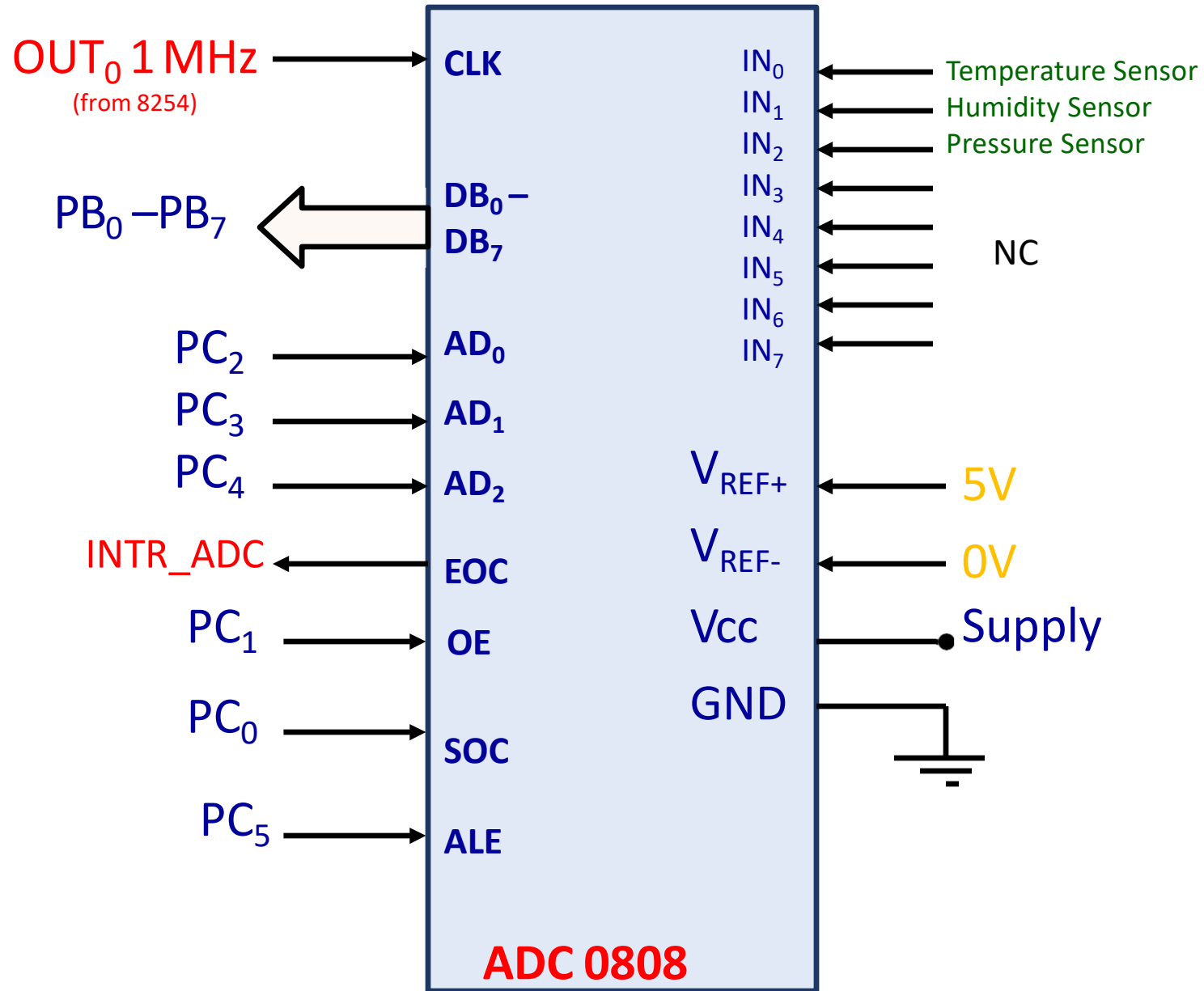
I/O Decoder

8255A (for display)

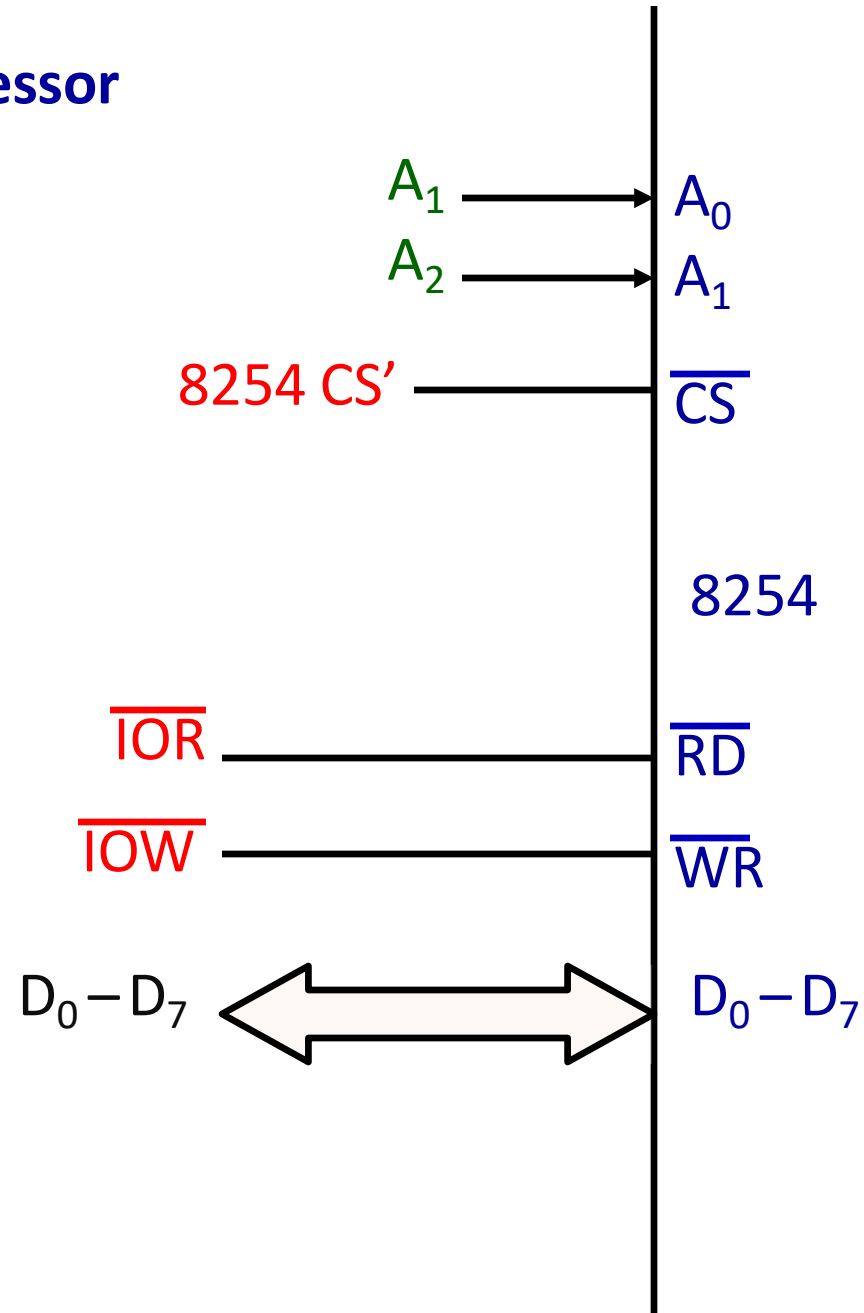


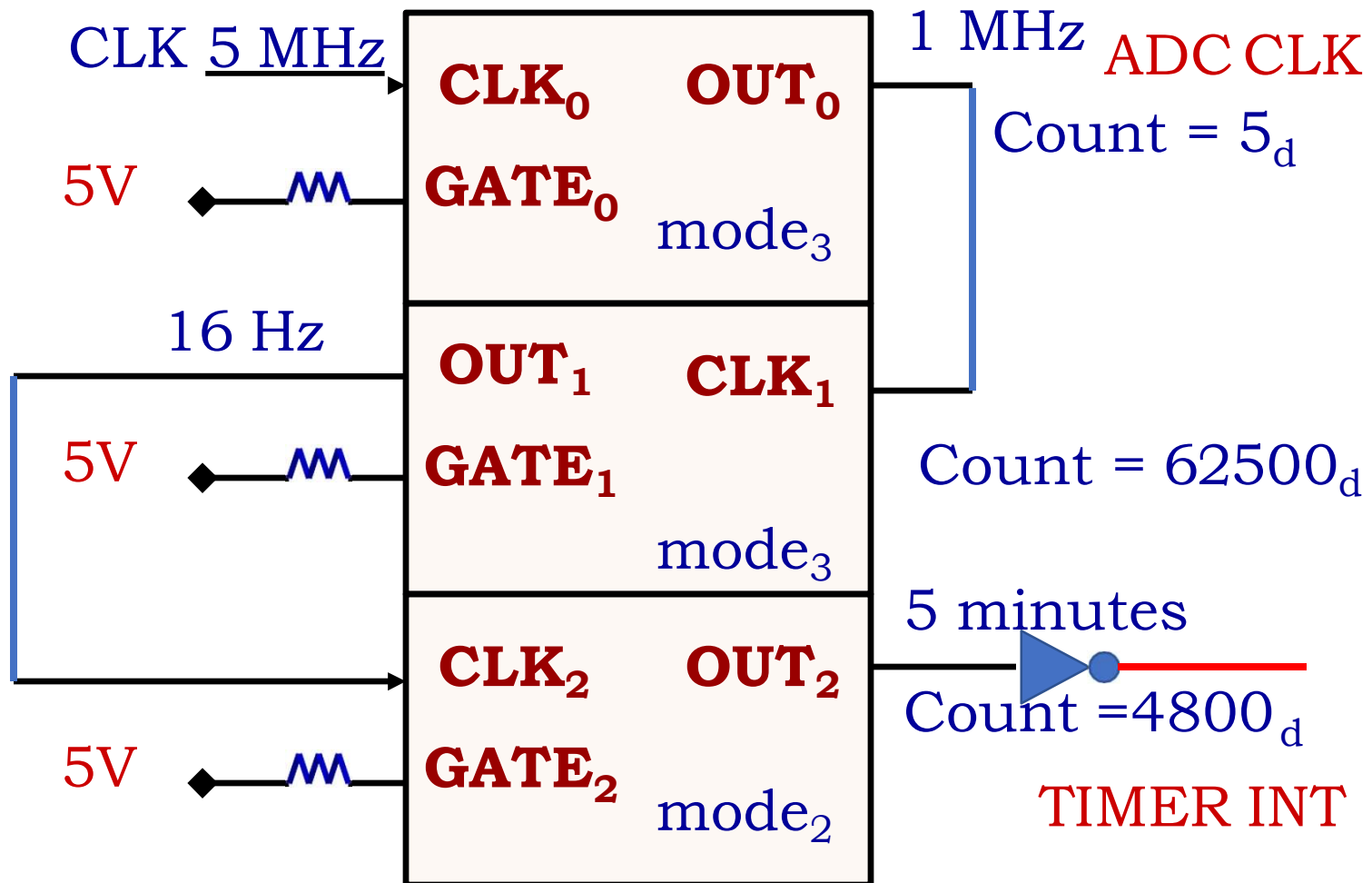
8255B (for the ADC)



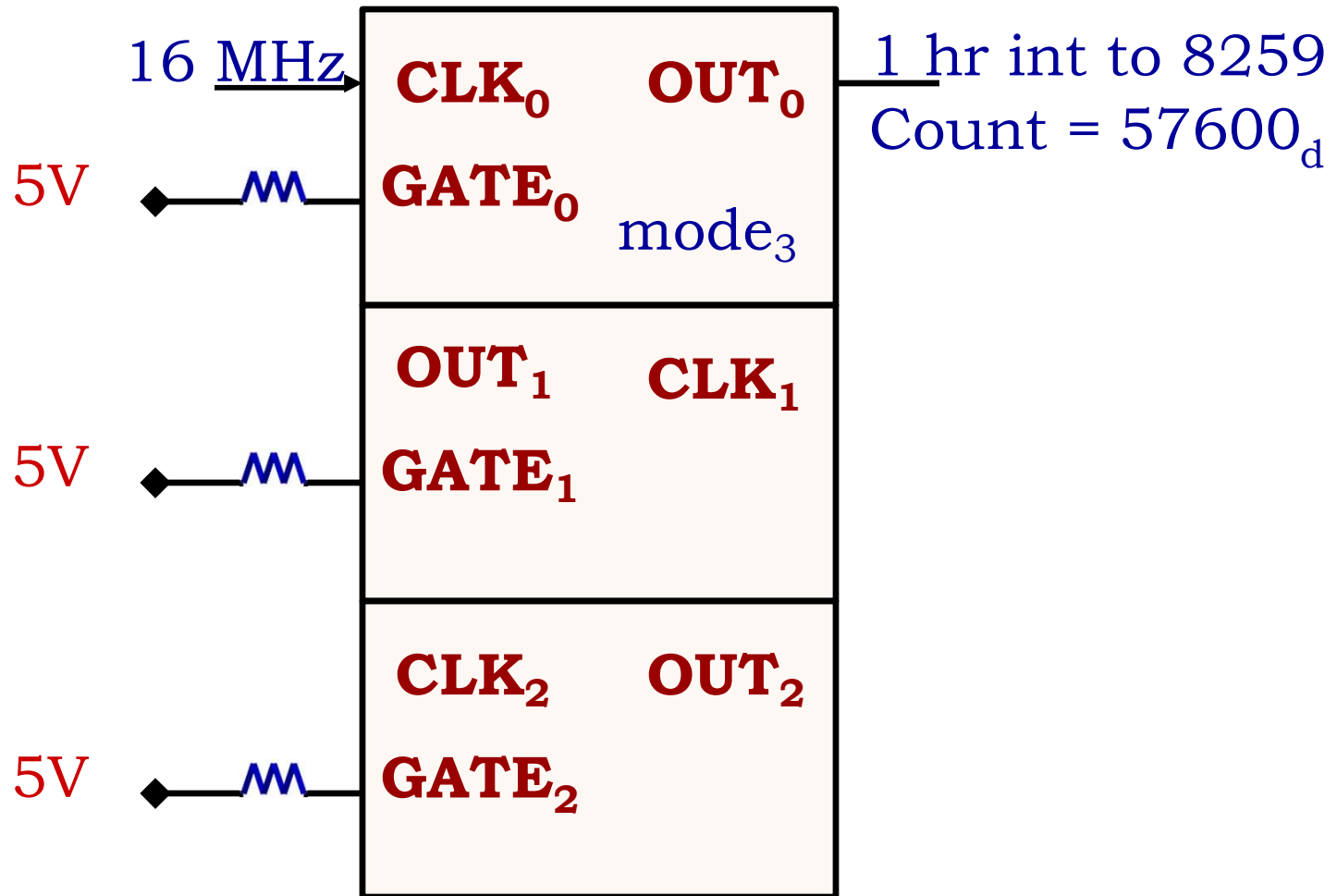


8254 Interface to the processor

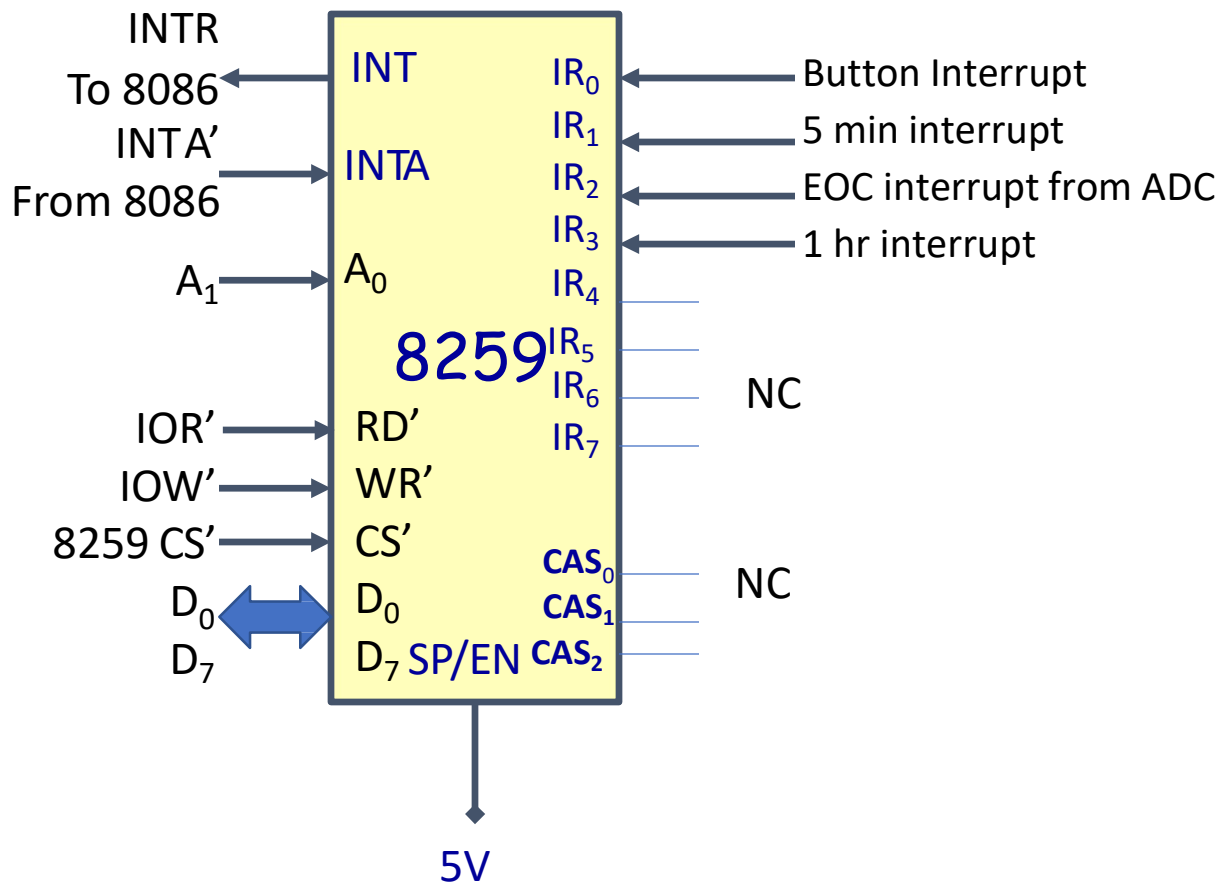




8254A

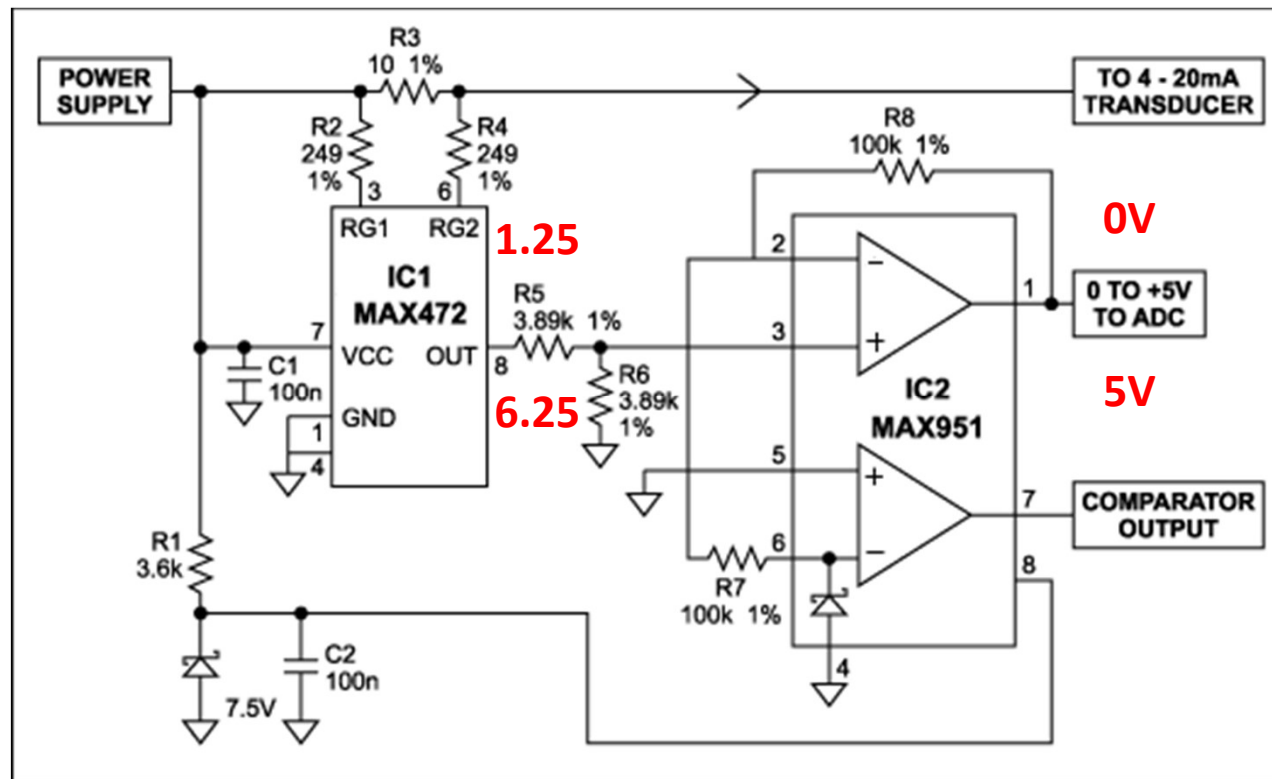


8254B

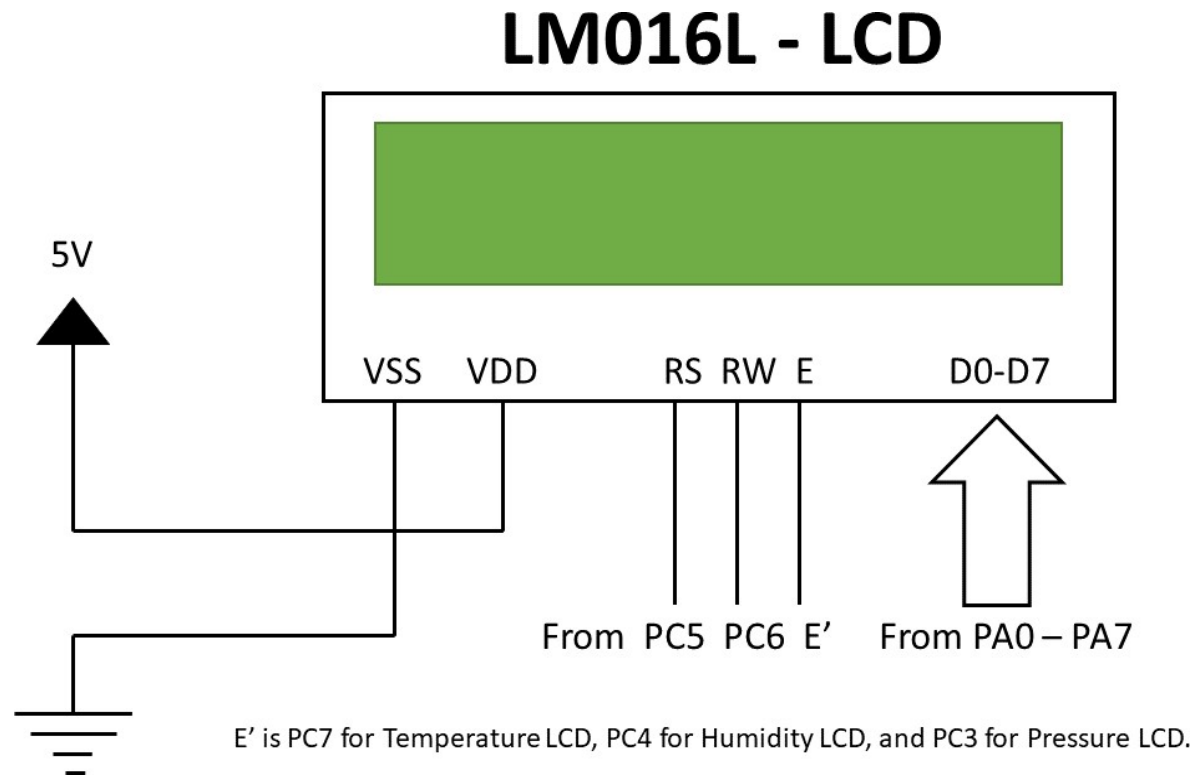


8259

MAX472



LCD Display



Button

