



**CHARUSAT**  
CHAROTAR UNIVERSITY OF SCIENCE AND TECHNOLOGY

NAAC  
GRADE **A+**



# SEMICONDUCTOR

## Summit 2.0

**From RTL to Silicon-Skills, Systems, Careers**

**17-19**  
**March, 2026**



Department of Electronics and  
Communication Engineering. - CSPIT,  
A6 Building, CHARUSAT



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## Summit 2.0

**From RTL to Silicon-Skills, Systems, Careers**

Semiconductor Summit 2.0 is a focused three-day technical event exploring the complete semiconductor journey from RTL design to silicon realization.

Aligned with India's rapidly expanding chip ecosystem and the India Semiconductor Mission, the summit emphasizes practical semiconductor skills, VLSI and system-level understanding, and career readiness for roles in ASIC, VLSI, Embedded, and Fabless domains.

Through technical talks, hands-on workshops, interactive sessions, and innovation-driven activities, participants gain clear insight into how chips are designed, built, and deployed in real industry workflows.

Learn semiconductor design the way industry practices it.



**Scan Now. Secure Your Spot in Silicon**

**17-19**  
**March, 2026**



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Email us on : [semiconductorsummit.ec@charusat.ac.in](mailto:semiconductorsummit.ec@charusat.ac.in) | [www.semiconductorsummitcharusat.ac.in](http://www.semiconductorsummitcharusat.ac.in)



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**Panel discussion – Fabless Startups and Fabless SMEs: Amazing Semiconductor Growth**  
Hear industry leaders decode India's silicon ambitions, challenges, and future roadmap.

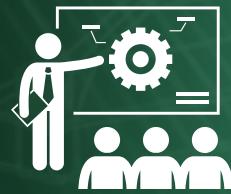
Date: 11/16/2028 | Time: 14:00  
Venue: Main Hall



**Workshop – RTL to GDS II (Open Source)**

Experience the complete chip design journey—from RTL coding to final silicon.

Date: 11/16/2028 | Time: 14:00  
Venue: Main Hall



**Stall Visit – Wafer to Chip Demo (Monk Tech)**

Witness how a silicon wafer transforms into a working semiconductor chip.

Date: 11/16/2028 | Time: 14:00  
Venue: Main Hall



**One Device. Two Invisible Worlds. Embedded Systems vs VLSI — What Should You Choose?**

Explore career paths and skills that shape embedded systems and VLSI design.

Date: 11/16/2028 | Time: 14:00  
Venue: Main Hall



**Silicon Shark Tank: Industry-Driven Idea Pitching**

Pitch bold ideas, face industry experts, and think like a semiconductor innovator.

Date: 11/16/2028 | Time: 14:00  
Venue: Main Hall



**Technical Treasure Hunt | Discover the Treasure of Aptitude & Digital Logic**

Crack clues, test logic, and uncover the fundamentals of digital design.

Date: 11/16/2028 | Time: 14:00  
Venue: Main Hall



**Silent Silicon Ideas Gallery**

Witness how a silicon wafer transforms into a working semiconductor chip.

Date: 11/16/2028 | Time: 14:00  
Venue: Main Hall



**Game-Based Technical Activity & Peer Learning Session**

Learn semiconductor fundamentals through fun, game-based challenges and peer-driven learning.

Date: 11/16/2028 | Time: 14:00  
Venue: Main Hall



## Discover What Semiconductor Summit 2.0 Offers



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From RTL to Silicon-Skills, Systems, Careers

Presented By

V. T. Patel Department of Electronics & Communication Engineering

Jointly Organized By

ChipXplorer – VLSI Club



IEEE CASS Student Branch  
Chapter – CHARUSAT

CHARUSAT IEEE Student Branch

"A Student Chapter of the IEEE Circuits and Systems Society"

OCAS  
IEEE CIRCUITS AND SYSTEMS SOCIETY



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**SOLVE THE LOGIC | TRACE THE CIRCUIT | FIND THE TREASURE**

*Where Reasoning Meets Circuits*  
**Technical Treasure Hunt**

Discover the Treasure of Aptitude & Digital Logic

MARCH 18, 2026

11:30 PM ONWARDS

**SEMICONDUCTOR**  
**Summit 2.0**

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