# Exploring the Energy Consumption of Highly Parallel Software on Windows

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#### **Abstract**

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## 1 Introduction

Research questions:

- What is the best measuring instrument for Windows?
- How does parallelism affect the energy consumption
- How does P-cores and E-cores affect the execution of parallelism in a process, versus only P-Cores?

## 2 Related Work

#### 2.1 Previous Work

This paper builds upon the knowledge gathered in our previous work "A Comparison Study of Measuring Instruments" [1] where different measuring instruments were compared to explore whether a viable software-based measuring instrument was available for Windows. It was found that Intel Power Gadget (IPG) and Libre Hardware Monitor (LHM) on Windows have similar correlation to hardware-based measuring instruments as Intel's Running Average Power

Limit (RAPL) has on Linux. This chapter builds upon the related work chapter of the previous work and as such will not be repeated, however, it will be expanded upon.

### 2.2 Parallel Software

In [2], the energy consumption for sequential and parallel genetic algorithms is explored, where one research question aims to explore the impact on energy consumption when using different numbers of cores. They find that a larger number of cores in the execution pool results in a lower running time and energy consumption, and conclude that parallelism can help reduce energy consumption. Parallelism's ability to reduce energy consumption is argued to be due to the large number of cores working to solve the problem simultaneously, where the combination of more cores, and more parallel operations per time unit will require less energy.

When considering parallel software, [2] also find asynchronous implementations to use less energy, because there are no idle cores waiting for data in asynchronous implementations, while in synchronous implementations cores can be blocked during runtime, while waiting for responses from other cores.

In [3], three different thread management constructs from Java are explored and analyzed regarding energy consumption.

They found a relationship between energy consumption and the number of threads used which was that generally energy consumption increased as the number of threads increased and then at some point the energy consumption starts to decrease as the number of threads start to approach the number of cores in the CPU. However, the peak of this energy consumption is application dependent.[3]

They found that in eight out of nine benchmarks, there was a decrease in execution time going from sequential execution on one thread to using multiple threads. However, it should be noted that four of their benchmarks are embarrassingly Parallel whereas only one is embarrassingly serial. However decreased execution time does not necessarily mean decreased energy consumption, because in six out of nine benchmarks the lowest energy consumption was found in the sequential version using one thread.[3]

Furthermore, they investigated the energy-performance trade-off using the Energy-Delay-Product (EDP), which is the product between energy consumption and execution time. Using EDP they generally found parallel execution to be more favorable however depending on the benchmark increasing the number of threads may not be aligned with an improvement of EDP.

In [4], the behavior of parallel applications and the relationship between execution time and energy consumption are explored. They test four different language constructs which can be used to implement parallelism in C#. Furthermore, they use varying amounts of threads and a sample of micro- and macro-benchmarks. [4]

They found that workload size has a large influence on running time and energy efficiency and that a threshold limit for the workload must be reached for there to be improvements when changing a sequential program into a parallel one. Additionally, it was found that execution time and energy consumption of parallel benchmarks do not always correlate. Comparing micro- and macro-benchmarks the findings remain consistent, although the impact becomes low for the macrobenchmarks due to their being an overall larger energy consumption. Furthermore, they have included some recommendations, which should be considered:[4]

- Shield cores: Avoid unintended threads running on the cores used in the benchmarking
- PowerUp: Can be used to ensure that benchmark is not optimized away during compilation
- Static clock: Make the clock rate of the CPU as static as possible
- Interrupt request: Avoid interrupt requests being sent to cores used in the benchmarking
- Turn off CPU turbo boost
- Turn off hyperthreading

## 2.3 Compilers

In [5] the language C++ and different compilers <sup>2</sup><sub>3</sub> are explored and compared to find the impact of us-4 ing different coding styles and compilers, where the <sup>5</sup> goal is to find a balance between performance and <sup>6</sup> energy efficiency. The different coding styles intro-<sup>8</sup> duced explore the impact of splitting CPU and IO <sup>9</sup>

operations and interrupting the CPU-intensive instructions with sleep statements. The C++ compilers used in [5] include MinGW GCC, Cygwin GCC, Borland C++, and Visual C++, and the energy measurements are performed using Windows Performance Analyses (WPA). All compilers are used with default settings, and no optimizations were chosen. This decision was made based on works like [6], where it was found how mainstream compilers will apply multiple optimizations to the final code, where these optimizations in the worst case will result in worse performance and increased energy consumption. The issue of optimizations being very machine dependent was also shown in [7], where analysis and optimizations were done on a Texas Instruments C6200 DSP CPU. In [7] it was found that a large portion of the energy is used by fetching instructions which was addressed by introducing a fetch packet mechanism, and also find loop-unrolling to reduce energy consumption. While these optimizations decrease the energy consumption for the Texas Instruments C6200 DSP CPU, they note that for other CPUs varying results are expected. A similar conclusion is also found in [5], where they find that when choosing a compiler and coding style the energy reduction depends on the nature of the target machine and application. Based on the test case used, this being an election sort algorithm, they find the best performance with the Borland compiler, and the lowest energy with the Visual C++ compiler. When considering the coding styles, they find that both separating IO and CPU operations and interrupting the CPU-intensive instructions with sleep statements also decrease the energy consumption.

# 3 Background

#### 3.1 CPU States

## 3.2 CPU Affinity

Affinity is a feature in operating systems(OSs) that enables processes to be bound to specific cores in a multi-core processor. In OSs, jobs and threads are constantly rescheduled for optimal system performance, which means that the same process can be assigned to different cores of the CPU. Processor affinity allows applications to bind or unbind a process to a specific core or range of cores/CPUs. When a process is pinned to a core, the OS ensures it only executes on the assigned core(s) or CPU(s) each time it is scheduled.[8]

```
void ExecuteWithAffinity(string path)
{
   var process = new Process();
   process.StartInfo.FileName = path
   process.Start();

   // Set affinity for the process
   process.ProcessorAffinity =
        new IntPtr(0b0000_0011)
```

10 }

**Listing 1:** An example of how to set affinity for a process in C#

Processor affinity is particularly useful for scaling performance on multi-core processor architectures that share the same global memory and have local caches referred to as the Uniform memory access architecture. Processor affinity is also useful for this study, as this allows the framework to assign a single or a set of cores and threads to a process.[8]

An example of how affinity can be set for a pro10 cess in C# can be seen in Listing 1. When setting 1:

the affinity for the processor, the input is a bitmask, where each bit represents a CPU core. In Listing 1, the 14 process is thus allowed to execute in core #0 and #1.

## 3.3 Scheduling Priority

When scheduling threads on Windows, it is done based on each thread's scheduling priority level and the priority class of the process. For the priority class of the process, the value can be either IDLE, BELOW NORMAL NORMAL, ABOVE NORMAL, HIGH or REALTIME, where the default is NORMAL. It is noted that HIGH priority should be used with care, as other threads in the system will not get any processor time while that process is running. If a process needs HIGH priority, it is recommended to raise the priority class temporarily. For the REALTIME priority class, this should only be used for applications that "talk" to hardware directly, as this class will interrupt threads managing mouse input, keyboard inputs, ect.[9]

For the priority level, the levels can be either IDLE, LOWEST, BELOW NORMAL, NORMAL, ABOVE NORMAL, HIGHEST and TIME CRITICAL, where the default is NORMAL. A typical strategy is to increase the level of the input threads for applications to ensure they are responsive, and to decrease the level for background processes, meaning they can be interrupted as needed.[9]

The scheduling priority is assigned to each thread as a value from zero to 31, where this value is called the base priority. The base priority is decided using both the thread priority level and the priority class and can be found in [9]. When assigning a base priority where both the priority class and thread priority are the default values, e.i.NORMAL, the base priority is 8.[9]

The idea of having different priorities is to treat threads with the same priority equally, by assigning time slices to each thread in a round-robin fashion, starting with the highest priority. In the case of none of the highest priority threads being ready to run, the lower priority threads will be assigned time slices. The lower-priority threads will then execute until a higherpriority thread is available, in which case the system will assign a full time slice to the thread, and stop executing the lower-priority threads, without time to finish using its time slice.[9]

```
void ExecuteWithPriority(string path)
{
  var process = new Process();
  process.StartInfo.FileName = path
  process.Start();

// Set priority class for process
  process.PriorityClass =
    ProcessPriorityClass.High;

// Set priority level for threads
  foreach (var t in process.Threads)
  {
    thread.PriorityLevel =
        ThreadPriorityLevel.Highest;
  }
}
```

**Listing 2:** An example of how to set priorities for a process in C#

A thing to note is that when setting priority class and priority level for a process through C#, the priority class is supported for both Windows and Linux, while the priority level is only supported for Windows. An example of how both the priority class and priority level can be set for a process and its threads can be seen in Listing 2

## 4 Method

## 4.1 Measuring Instruments

This section present the different measuring instruments utilized in this work. The measuring instruments utilized in the previous work will only be briefly introduced, however more detail can be found in our previous work[1]. In this paper, four software-based measuring instruments and one hardware-based measuring instrument is used, where the hardware-based measuring instrument represents the ground truth.

Running Average Power Limit Intel's RAPL is a commonly used software-based measuring instrument seen in the literature.[1] It uses model-specific-registers (MSRs) and Hardware performance counters to calculate how much energy the processor uses. The MSRs RAPL uses include MSR\_PKG\_ENERGY\_STATUS, MSR\_DRAM\_ENERGY\_STATUS, MSR\_PPO\_ENERGY\_STATUS and MSR\_PP1\_ENERGY\_STATUS. Which corresponds to the power domains, PKG, DRAM, PP0, and PP1 which are explained in our previous work[1]. RAPL has previously only been directly accessible on Linux and Mac. In our previous work we found that RAPL had a high correlation of 0.81 with our ground truth on Linux.[1]

Intel Power Gadget IPG is a software tool created by Intel, which can estimate the power of Intel processors. It contains a command line version called Powerlog which allows accessing the energy consumption using callable APIs. It uses the same hardware counters and MSRs as RAPL[10], therefore it is expected to observe similar measurements to that of RAPL. Which is also shown in our previous work where we found that IPG had a high correlation of 0.78 with our ground truth on Windows. We also found that IPG had a high correlation of 0.83 with RAPL, although the measurements is on different operating systems.[1]

Libre Hardware Monitor LHM is a fork of Open Hardware Monitor, where the difference is that LHM does not have a UI. Both projects are open source. LHM can use the same hardware counters and MSRs as RAPL and IPG and as such can measure the power domains PKG, DRAM, PP0, and PP1. Since it uses the methods to read energy consumption, a similar measurement is expected between LHM and IPG. We found that LHM correlated 0.76 with our ground truth on Windows. LHM was also found to have a high correlation of 0.85 with IPG.[1]

AC Current Clamp Severing as our ground truth measurement is our hardware-based measuring setup which is comprised of an MN60 AC clamp that is connected to the phase of the wire that goes into the PSU. It is also connected to an Analog Discovery 2 which is used as an oscilloscope which in turn is then connected to a Raspberry Pi 4. This setup allows us to continuously measure and log our data. For more detail see our previous work[1].

**Scaphandre** One measuring instrument not used in our previous work is Scaphandre[11]. Scaphandre is described as a monitoring agent which can measure energy consumption and is made for Linux where it can use Powercap RAPL which is a Linux kernel subsystem where data can be read from RAPL. It also has the functionality of measuring the energy consumption of some virtual machines, specifically Qemu and KVM hypervisors. A driver also exists which allows for installing RAPL on Windows.[12] Doing so allows using Scaphandre on a Windows computer where the sensor is RAPL which is utilizing the MSRs to update its counters. The Windows version of Scaphandre has some limitations but is able to report the energy consumption of the power domain PKG, using the MSR MSR\_PKG\_ENERGY\_STATUS. Furthermore, it can also give an estimation of the energy consumption for individual processes. It does so by storing CPU usage statistics alongside the values of the energy counters. Then it is able to calculate the ratio of the CPU time for each Process ID (PID). With the calculated ratio a new calculation is made to get the subset of the energy

consumption which is estimated to belong to a specific PID. A Linux exclusive feature is that the monitoring system Prometheus can be used with Scaphandre to get the energy consumption of an application which consist of several PIDs.

- 5 Experiments
- 6 Results
- 7 Discussion
- 8 Conclusion

## Acknowledgements

## 9 Future Works

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