

2025 Korea-Finland Chip Design Symposium

“Chips Shaping the Future”



When: Friday, September 12, 2025
09:00 ~ 17:30










Organizer:
Prof. Kwantae Kim
Aalto University, Finland



Where: Aalto University
H304, Otakaari 1, 02150 Espoo, Finland



Host: Embassy of the Republic of Korea in Finland
Aalto University
Aalto Microelectronics Research Center (METKA)

Time	Topic	Speaker	
08:30	Reception / Morning Coffee		
Session 1: Opening			
9:00	Introduction to the Symposium	Prof. Kwantae Kim Aalto University, Finland	
9:15	Congratulatory Remarks	Ambassador Jung-Ha Kim Embassy of the Republic of Korea in Finland	
9:20	IC Design Research at Aalto University	Prof. Jussi Ryyänen Aalto University, Finland	
9:40	Break / Networking		
Session 2: Innovations in Sensor Interfaces			
9:50	Energy-Efficient Analog/Mixed-Signal Circuits	Prof. Taekwang Jang ETH Zurich, Switzerland	
10:10	Analog/Mixed-Signal ICs for Next Generation Integrated System	Prof. Dong-Woo Jee Ajou University, South Korea	
10:30	Break / Networking		
Session 3: AI Accelerators and Systems			
10:40	Automotive System, Computing-in-Memory	Prof. Kyuho Lee Yonsei University, South Korea	
11:00	SoC team from Nokia	(TBD) Nokia, Finland	
11:20	LLM Accelerator, Neuromorphic Algorithms	Prof. Sangyeob Kim Yonsei University, South Korea	
11:40	Lunch / Networking		

Session 4: Highlights on Microelectronics and Automatic Generation of Analog ICs			
13:00	Microelectronics in Finland Today - and How We Got Here	Dr. Toni Mattila Business Finland, Finland	
13:20	Programmatic Design Methodologies for Mixed-Mode Systems and Analog Circuits	Prof. Marko Kosunen Aalto University, Finland	
13:40	Automatic Generation of High-Performance Circuits	Prof. Jaeduk Han Hanyang University, South Korea	
14:00	Break		
Session 5: ICs for Neural Recording and Neuromorphic Applications			
14:10	High-Density Neural Recording ICs for Next-Generation Neurotechnology	Dr. Yoontae Jung imec, Belgium	
14:30	Analog Neuromorphic ICs for Extreme Edge AI	Dr. Jacek Flak VTT, Finland	
14:50	Break		
Session 6: Innovations in mmWave ICs			
15:00	Progress in Design and Integration for Near-THz Wireless Communications Systems	Dr. Yves Baeyens Nokia Bell Labs, United States	
15:20	mmWave/sub-THz Transceivers for Advanced 5G/6G Applications	Prof. Aarno Pärssinen University of Oulu, Finland	
15:40	Break		
Session 7: Advances in Quantum Circuits and System			
15:50	KQCircuits - Open-Source Layout Library for Designing Chips with Super Conducting Qubits.	Pavel Smirnov-Ylikangas IQM, Finland	
16:10	(TBD)	Dr. Leif Roschier Bluefors, Finland	
16:30	Quantum Technology at VTT	Dr. Jae-Wung Lee VTT, Finland	
Session 8: Closing			
16:50	Closing Remarks / Group Photo	Prof. Kwantae Kim Aalto University, Finland	
17:00	Banquet	-	



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