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When: Friday, September 12, 2025

09:00 ~ 17:30

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Organizer:

Prof. Kwantae Kim Aalto University, Finland

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Where: Aalto University

H304, Otakaari 1, 02150 Espoo, Finland

Host:

Embassy of the Republic of Korea in Finland

Aalto University

Aalto Microelectronics Research Center (METKA)

Time	Торіс	Speaker			
08:30	Reception / Morning Coffee				
Session 1: Opening					
9:00	Introduction to the Symposium	Prof. Kwantae Kim Aalto University, Finland	Aalto University		
9:15	Congratulatory Remarks	Ambassador Jung-Ha Kim Embassy of the Republic of Korea in Finland	Ministry of Foreign Affairs		
9:20	IC Design Research at Aalto University	Prof. Jussi Ryynänen Aalto University, Finland	Aalto University		
Session 2: Innovations in Sensor Interfaces					
9:40	Analog Circuits Beyond the Believed Fundamental Limits	Prof. Taekwang Jang ETH Zurich, Switzerland	ETH zürich		
10:00	From Single-Function Sensors to Smart Sensor Systems	Markus Hiienkari Bosch Sensortec, Finland	⊜ BOSCH		
10:20	Break / Networking				
10:30	Analog/Mixed-Signal ICs for Next Generation Integrated System	Prof. Dong-Woo Jee Ajou University, South Korea	1973 20 1973 20 1973		
10:50	Towards Future Sensor Integration	Dr. Jerry Lemberg Saab Finland, Finland	SAAB		
11:10	Break / Networking				
	Session 3: Al Accelerators and Systems				
11:20	Various Design Approaches to AI Acceleration: CIM to NPU	Prof. Kyuho Lee Yonsei University, South Korea			
11:40	Mixing Deep-Neural-Networks and Spiking-Neural-Networks for the Best of Both Worlds	Prof. Sangyeob Kim Yonsei University, South Korea			
12:00	Lunch at Silinteri (Restaurant Alvari)	Otakaari 1 D, 02150 E	spoo		

Session 4: Highlights on Microelectronics and Automatic Generation of Analog ICs					
13:00	Microelectronics in Finland Today - and How We Got Here	Dr. Toni Mattila Business Finland, Finland	BUSINESS FINLAND		
13:20	Programmatic Design Methodologies for Mixed-Mode Systems and Analog Circuits	Prof. Marko Kosunen Aalto University, Finland	Alto University		
13:40	Automatic Generation of High- Performance Circuits	Prof. Jaeduk Han Hanyang University, South Korea	(6.81 (6.81)		
14:00	Break				
Session 5: ICs for Telecommunication, Neural Recording, and Neuromorphic Applications					
14:10	Novel Microprocessor Architectures for Telecom — Challenges and Enablers	Matthew Goode Nokia, Finland	VO <iy< td=""></iy<>		
14:30	High-Density Neural Recording ICs for Next-Generation Neurotechnology	Dr. Yoontae Jung imec, Belgium	imec		
14:50	Analog Neuromorphic ICs for Extreme Edge AI	Dr. Jacek Flak VTT, Finland	VTT		
15:10	Break				
Session 6: Innovations in mmWave ICs					
15:20	Progress in Design and Integration for Near-THz Wireless Communications Systems	Dr. Yves Baeyens Nokia Bell Labs, United States	NOSIA BELL LABS		
15:40	mmWave/sub-THz Transceivers for Advanced 5G/6G Applications	Prof. Aarno Pärssinen University of Oulu, Finland	UNIVERSITY OF OULU		
16:00	Break				
Session 7: Advances in Quantum Circuits and System					
16:10	KQCircuits - Open-Source Layout Library for Designing Chips with Super Conducting Qubits.	Pavel Smirnov-Ylikangas IQM, Finland	IQM		
16:30	Cryogenic Solutions and Challenges	Dr. Leif Roschier Bluefors, Finland	°BLUE FORS		
16:50	Quantum Technology at VTT	Dr. Jae-Wung Lee VTT, Finland	VTT		
Session 8: Closing					
17:10	Closing Remarks / Group Photo	Prof. Kwantae Kim Aalto University, Finland	Aalto University		
17:30	Banquet at Fat Lizard	Tietotie 1, 02150 Espo	00		





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