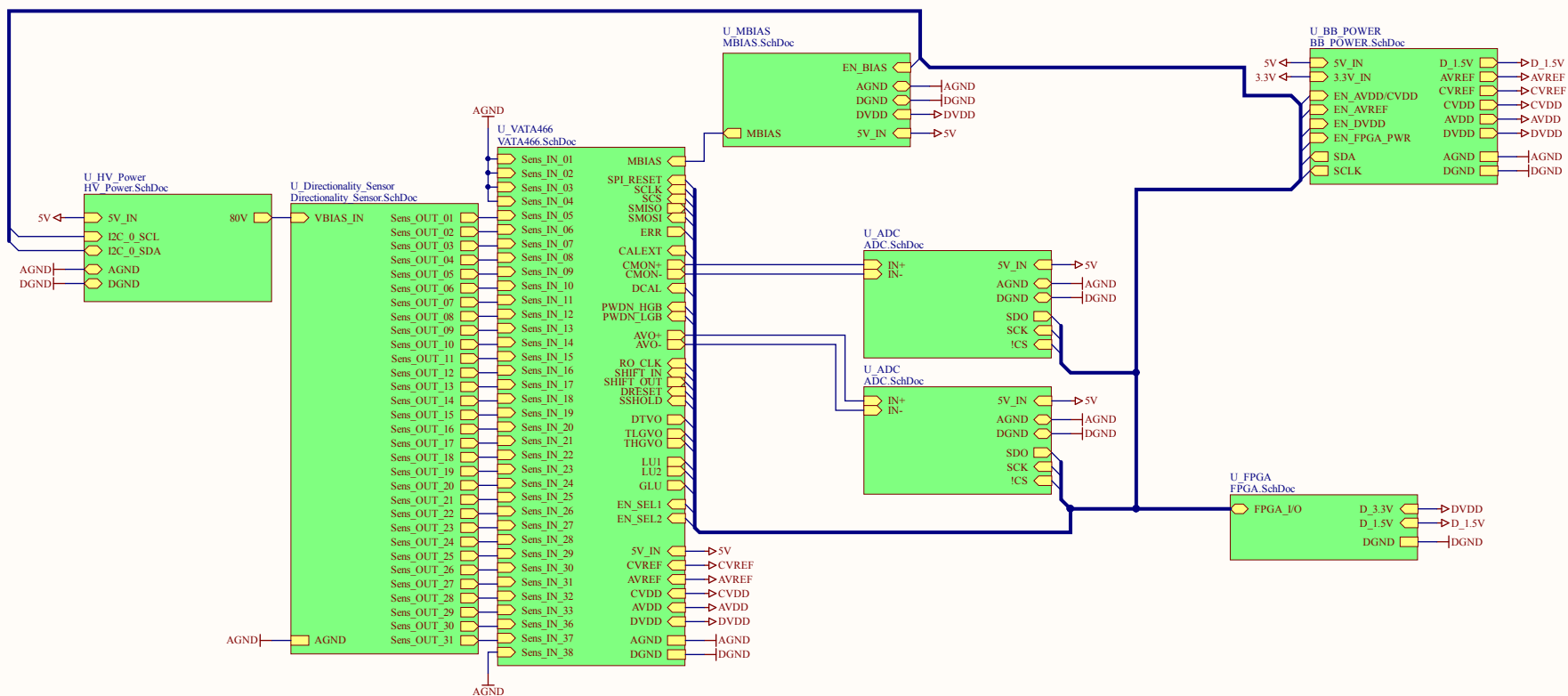
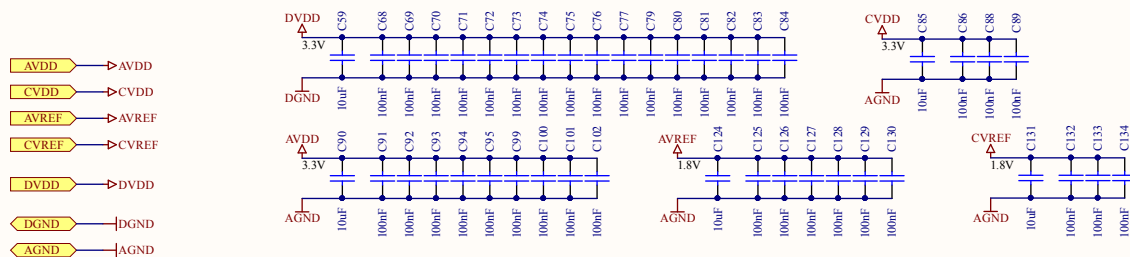
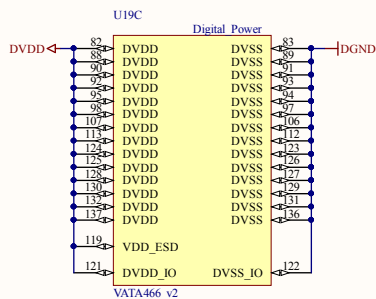
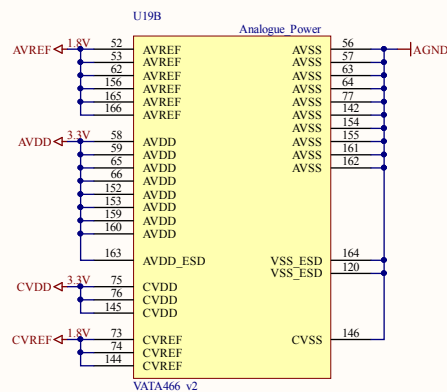
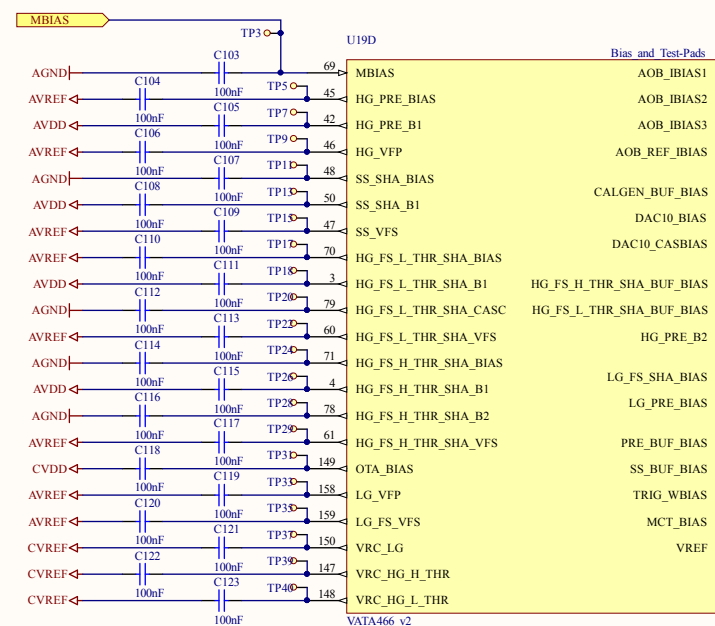
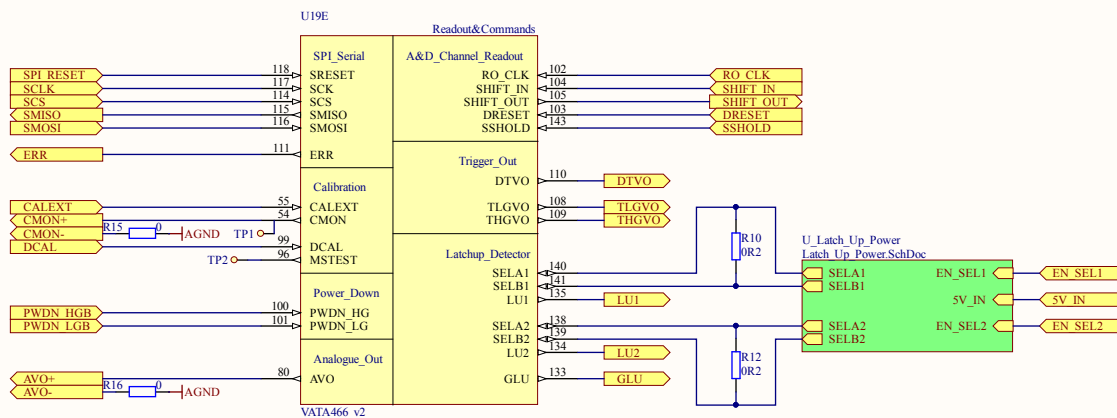
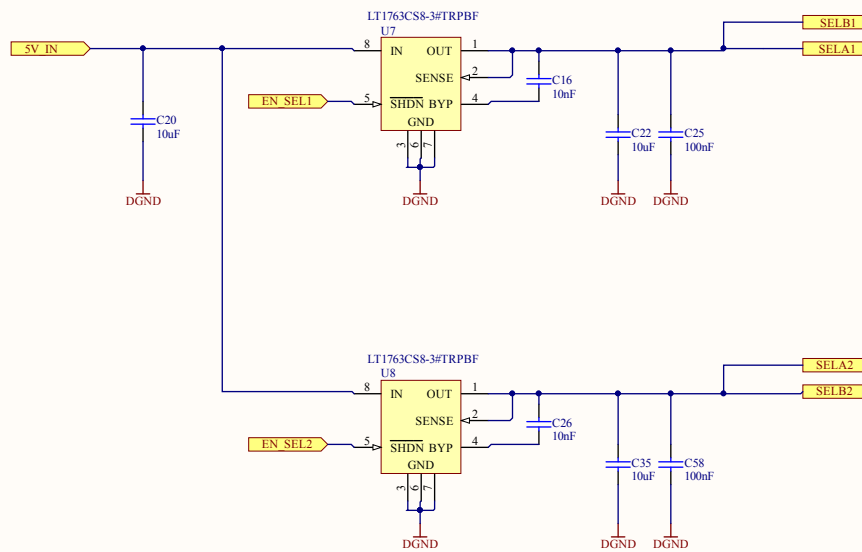


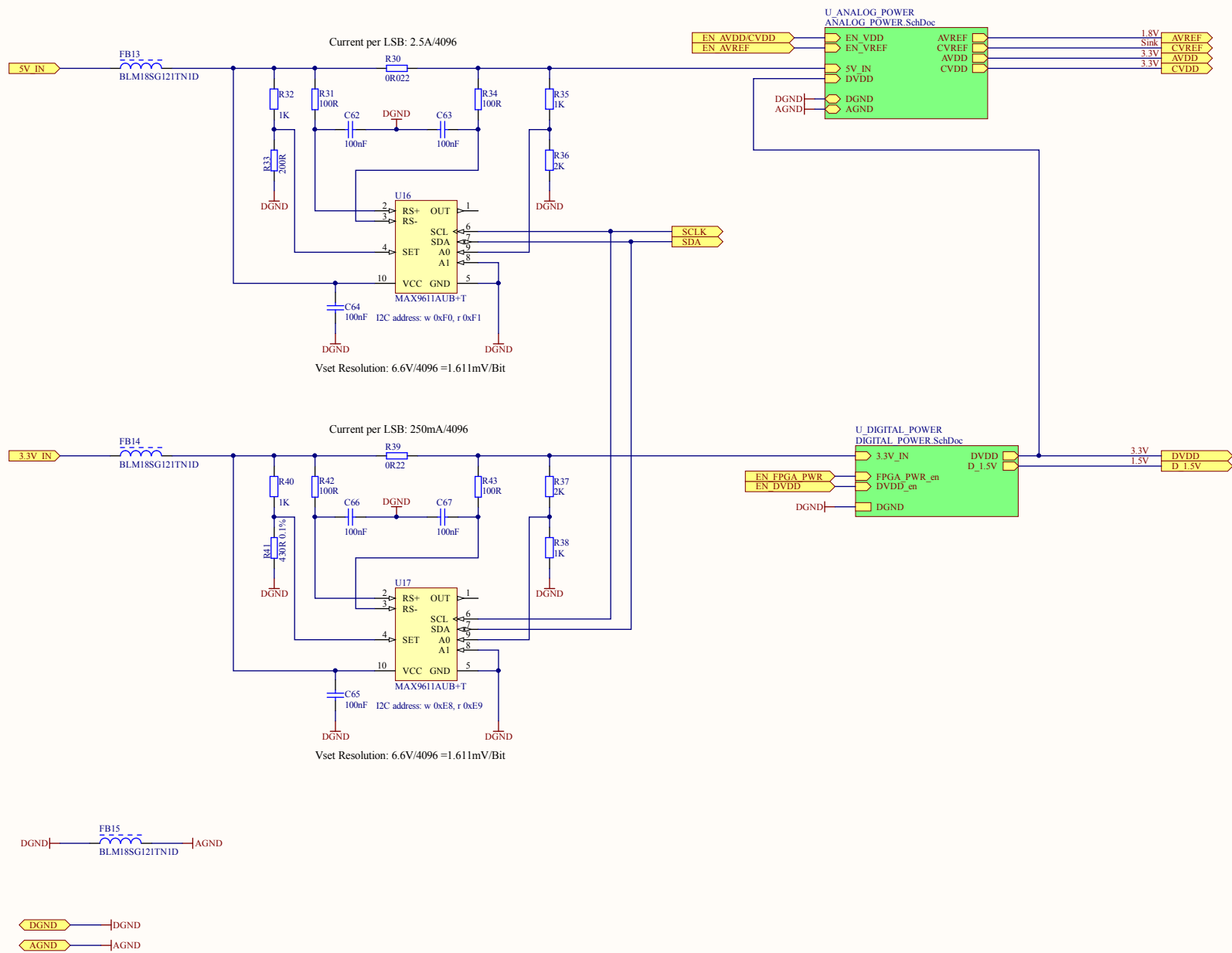
U_Interface_CubeSat
Interface_CubeSat.SchDoc

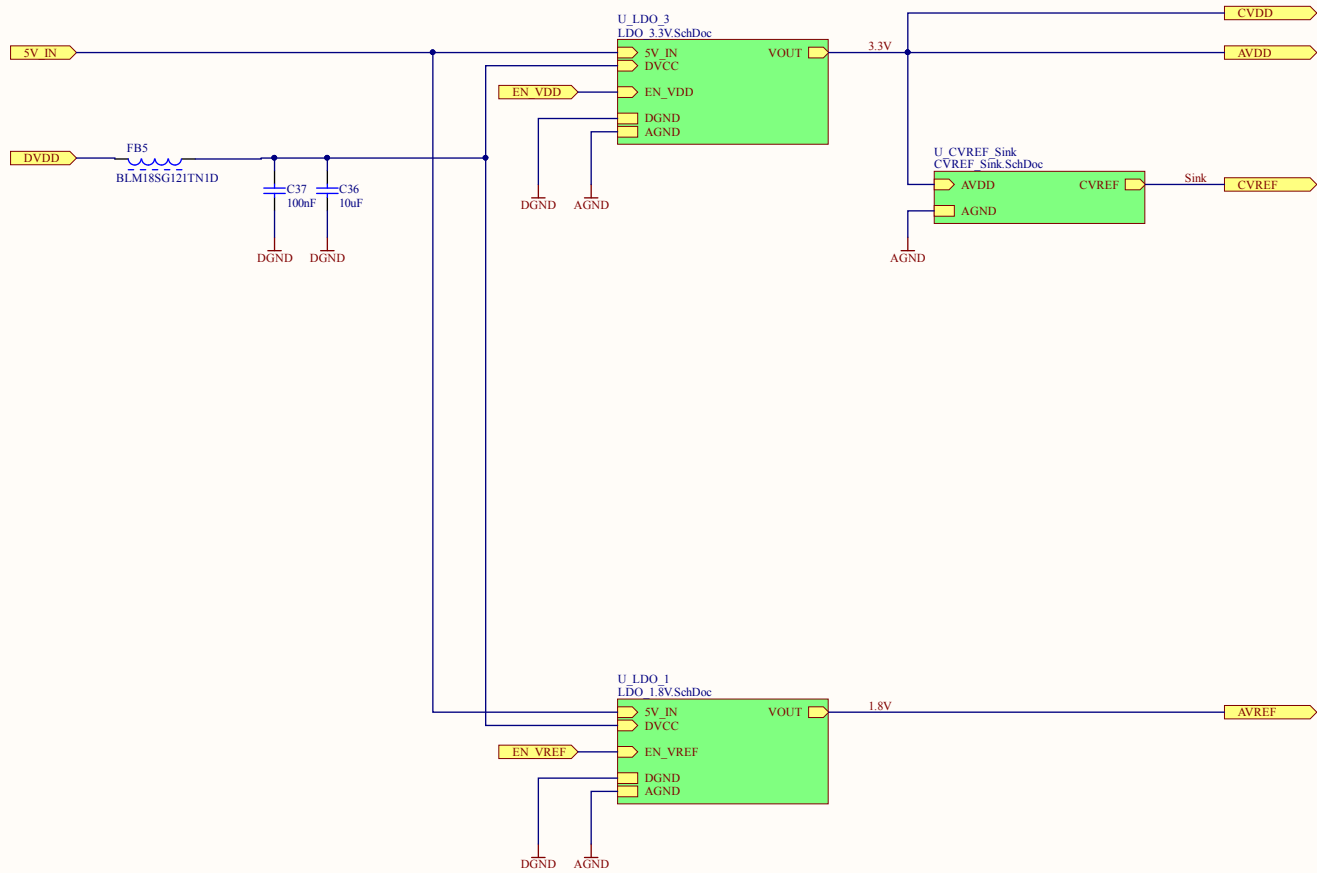




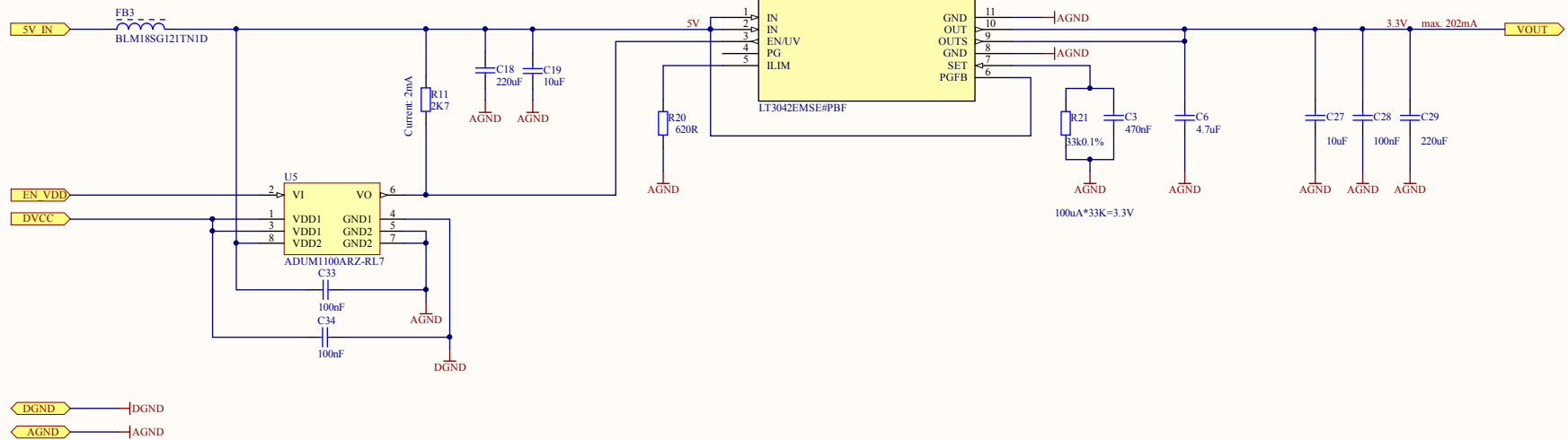


Tantalum: VA466 BASE BOARD		
PSI-Directionality_Sensor.PrjPcb		
Cannot open file C:\Users\Dominik\Desktop\esp ace combinedEPFLWithText v	Page: 3 of 17	Revision: 0.2
Drawn by: D. Reukauf	Date: 1/12/2017	
Filename: Latch_Up_Power.SchDoc		

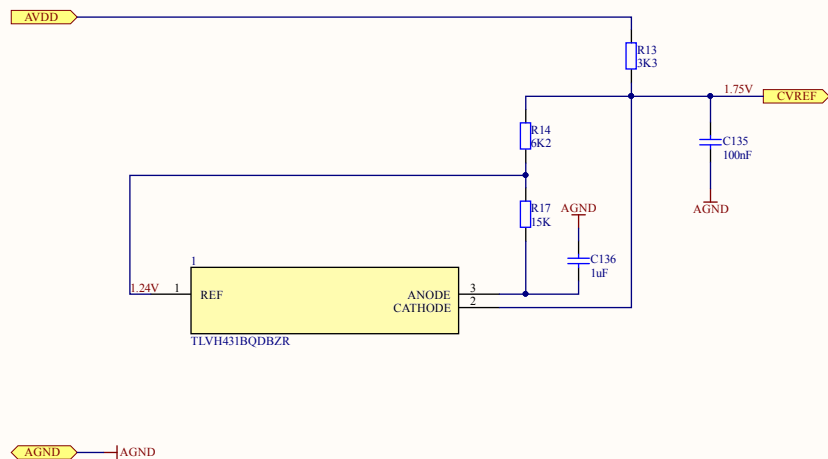




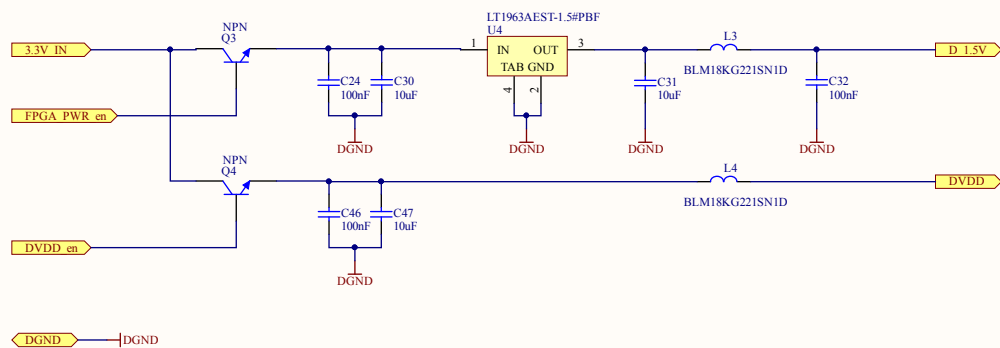
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Drawn by: D. Reukauf	Date: 1/12/2017	
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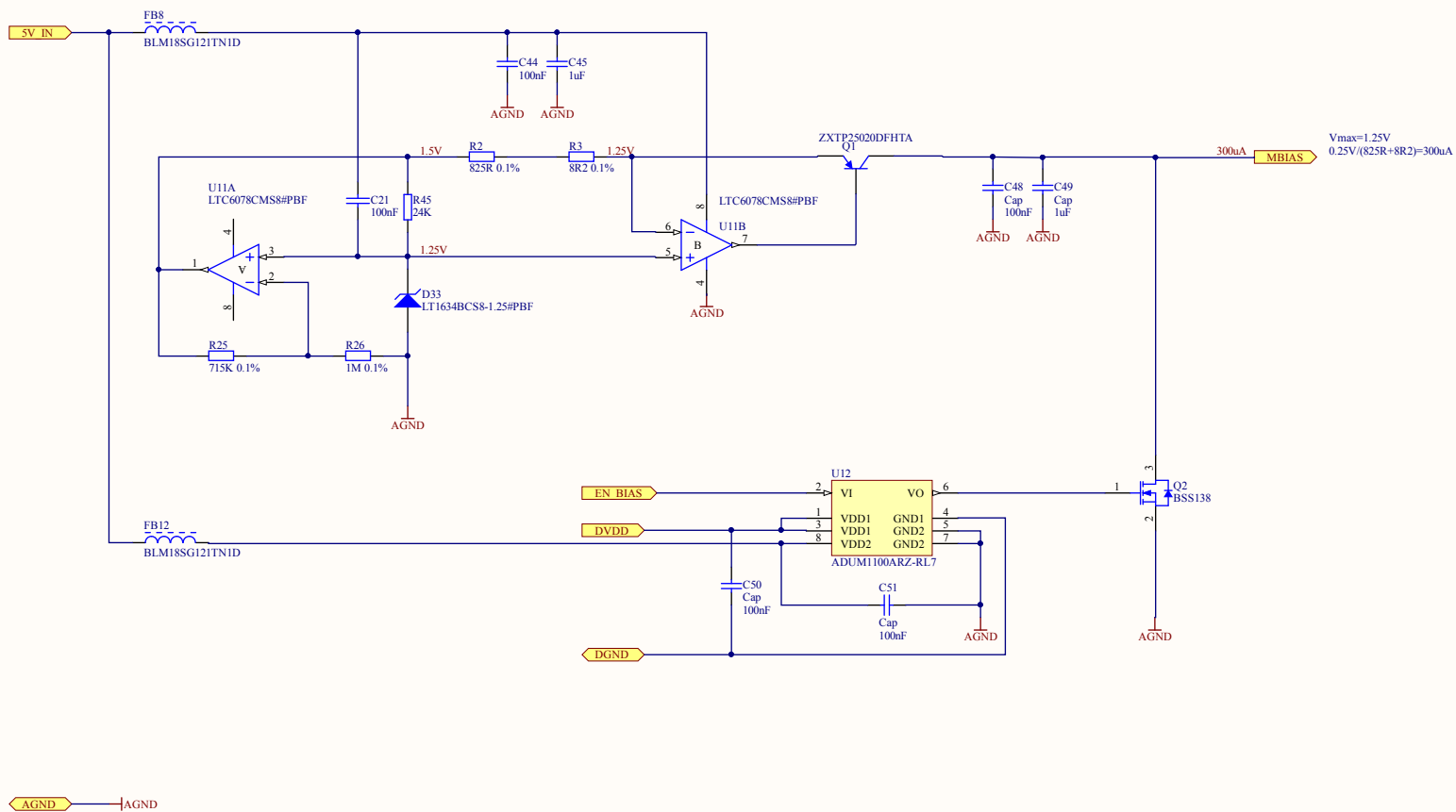
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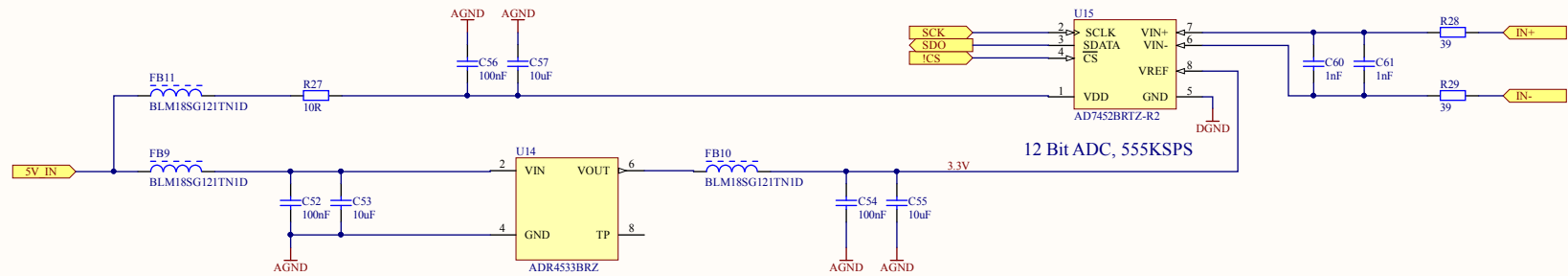
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Drawn by: D. Reukauf	Date: 1/12/2017	
Filename: CVREF_Sink.SchDoc		



Tantalum: VA466 BASE BOARD		
PSI-Directionality_Sensor.PrjPcb		
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Drawn by: D. Reukauf	Date: 1/12/2017	
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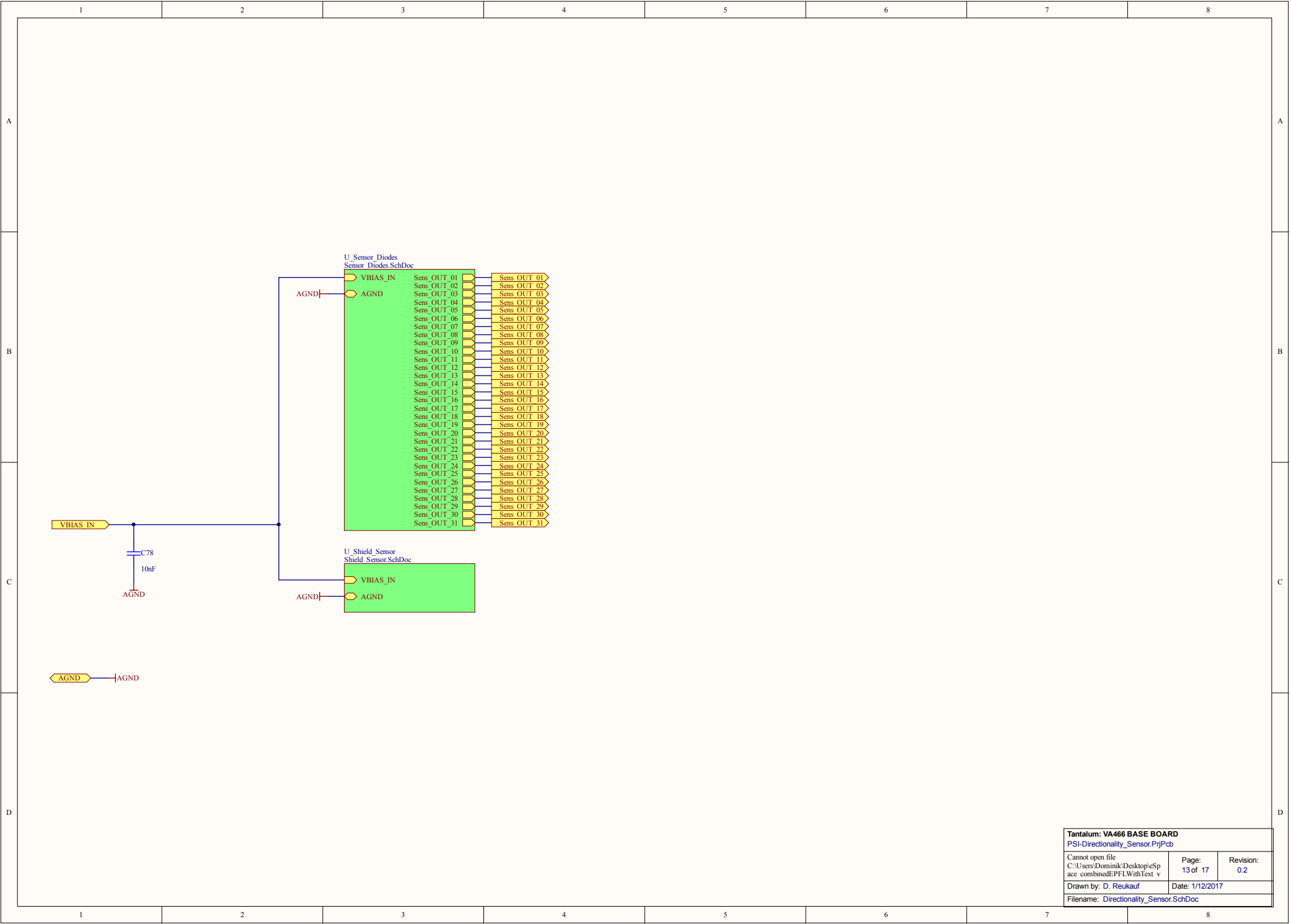
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Filename: MBIAS.SchDoc		

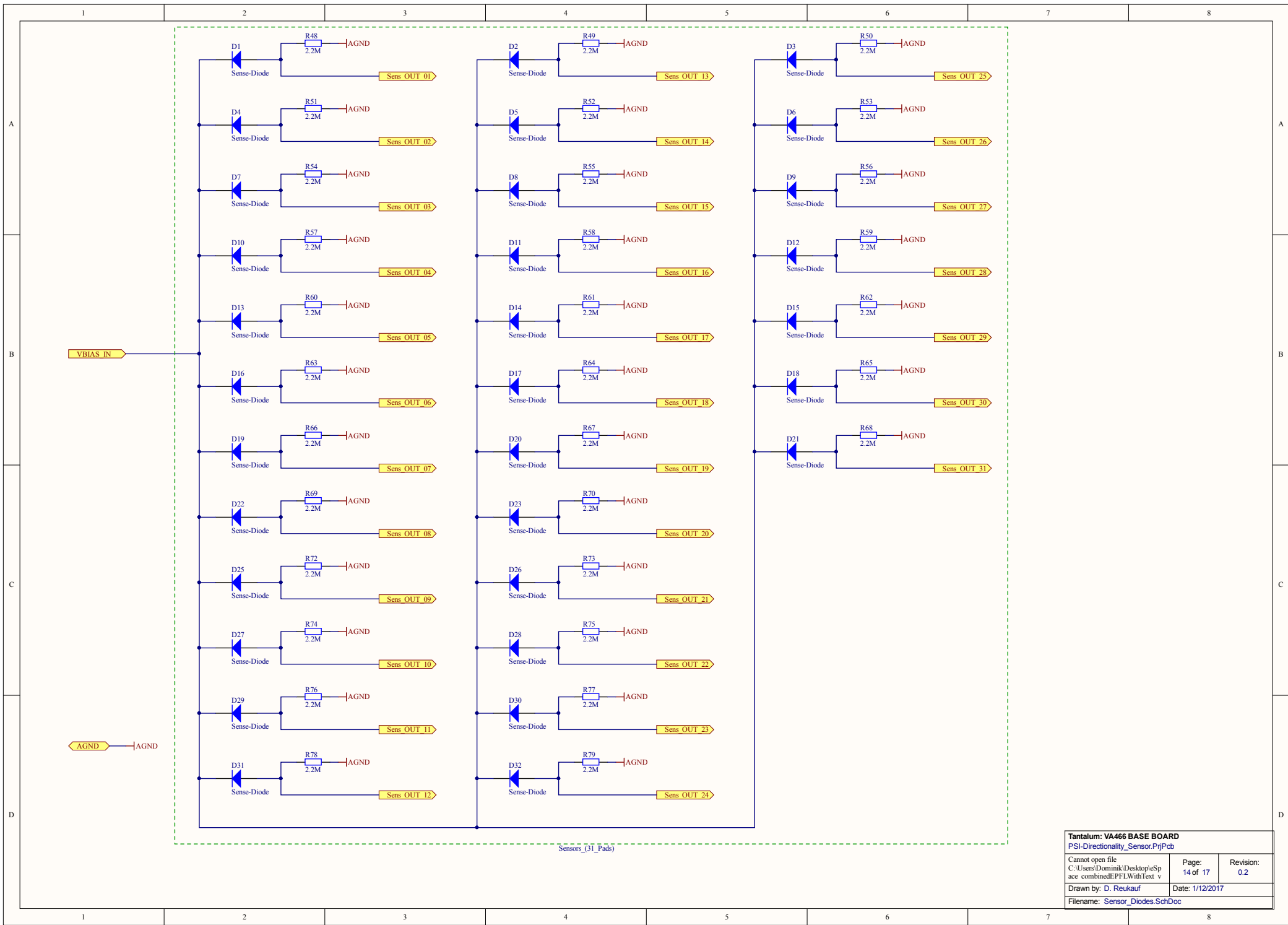


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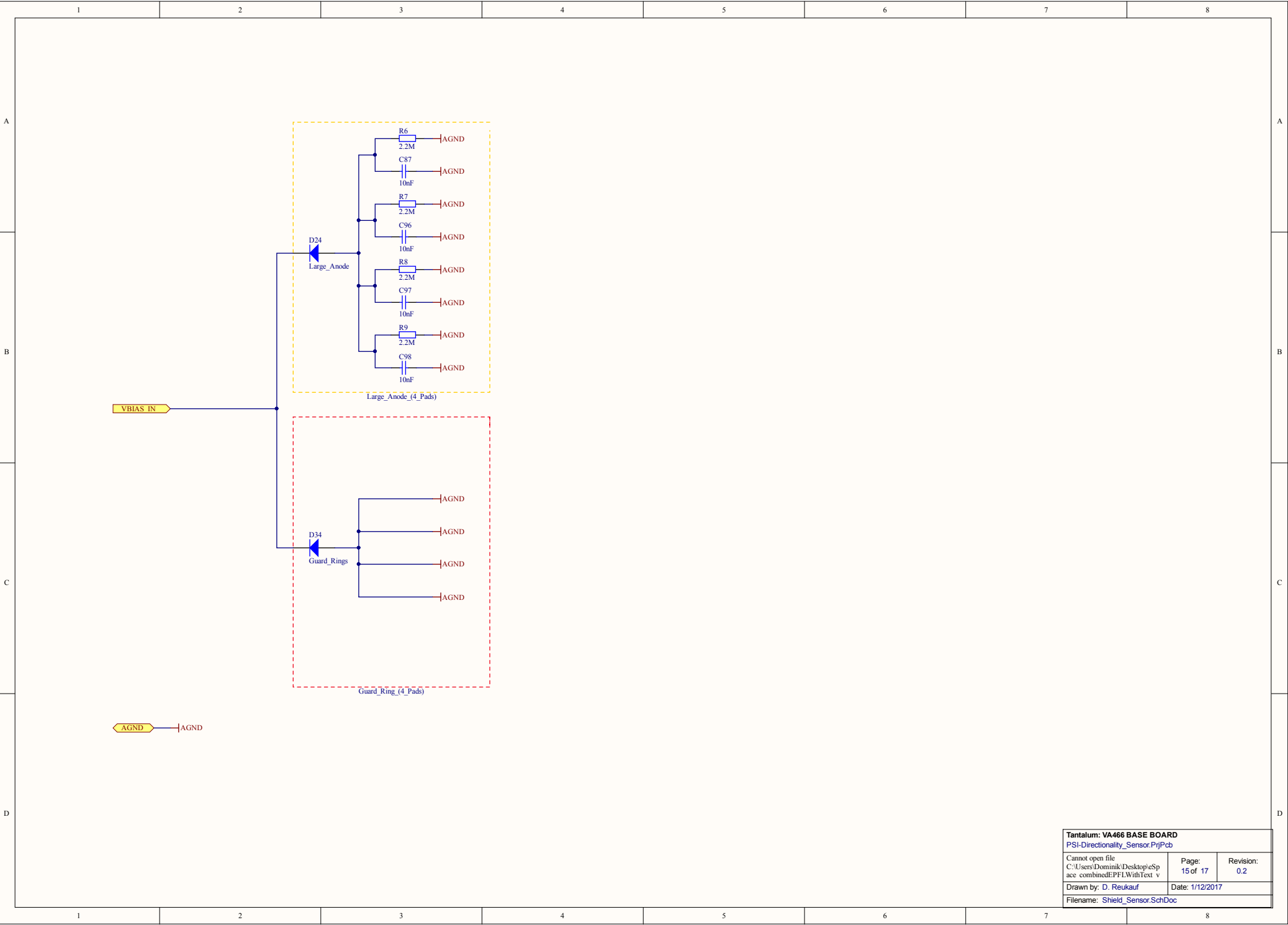
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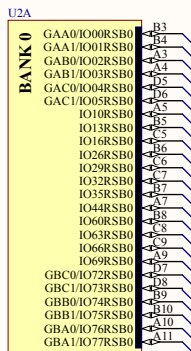
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Drawn by: D. Reukauf	Date: 1/12/2017	
Filename: ADC.SchDoc		



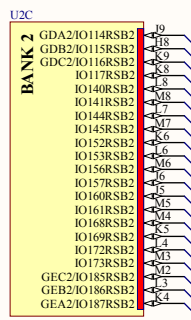


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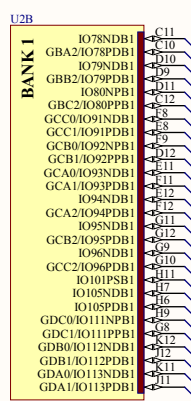




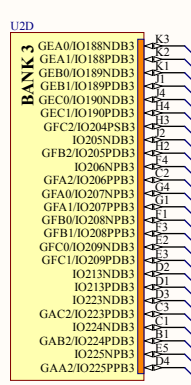
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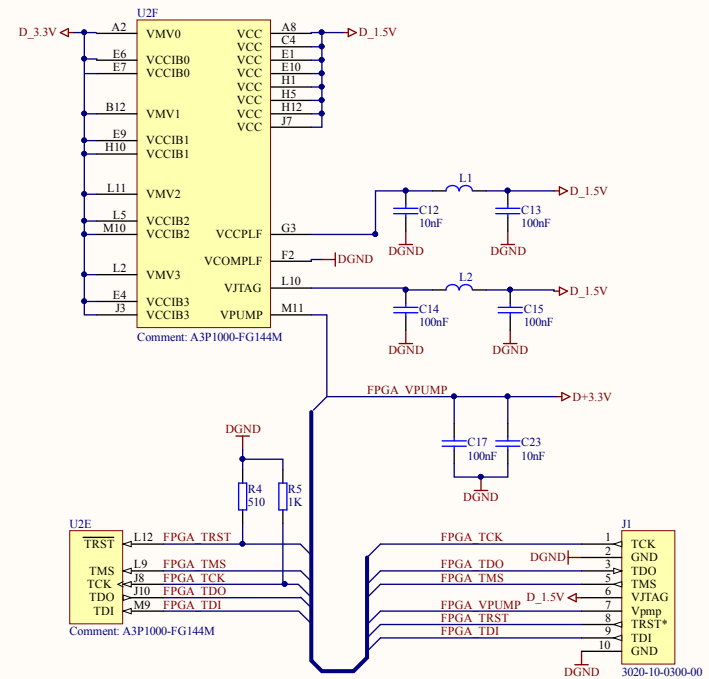


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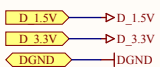
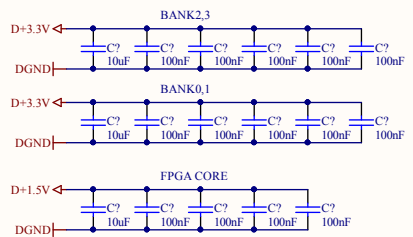
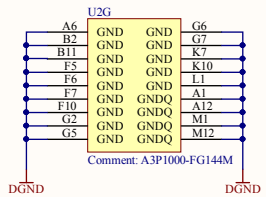


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FPGA I/O



NOTE: TMS, TDI, & TRST HAVE INTERNAL 10K PULLUPS



Tantalum: VA466 BASE BOARD		
PSI-Directionality_Sensor.PrjPcb		
Cannot open file C:\Users\Dominik\Desktop\esp ace combinedEPLWithText v	Page: 16 of 17	Revision: 0.2
Drawn by: D. Reukauf	Date: 1/12/2017	
Filename: FPGA.SchDoc		

