

CSU22022 Computer Architecture I

Prof. Michael Manzke

Assignment: Milestone Datapath

Checklist

30th October 2024

Version 1.0

All files must be submitted to Blackboard as individual files, no zipfiles. **With one exception: the "Processor_XXXXXXXXX.srcs" must be a zip-file.** Submit only entities that are working and are fully tested. You must follow the entity order on the checklist. You cannot skip entities.

The entities must be programmed and tested in the following order. Please confirm by ticking all the boxes, next to the items, that all required files are available on Blackboard and that the entity is fully working. You must print this checklist, tick the boxes, sign it, and submit it with your milestone assignment to Blackboard.

All files must be submitted in the checklist order:

- DP_Datapath_XXXXXXXX.vhd
- DP_Datapath_XXXXXXXX_TB.vhd
- DP_Datapath_XXXXXXXX_SchematicXX.jpg
- DP_Datapath_XXXXXXXX_TDXX.jpg
- DP_Datapath_XXXXXXXX_Doc.jpg

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00	My submission complies with the “Instructions for the Blackboard	
	Submission of the Assignment” CSU22022 Milestone Datapath 2024-2025 Instruction V1.0.pdf)	Yes

01	DP_Datapath_XXXXXXXX.vhd	Yes
	DP_Datapath_XXXXXXXX_TB.vhd	Yes
	DP_Datapath_XXXXXXXX_SchematicXX.pdf	Yes
	DP_Datapath_XXXXXXXX_TDXX.png	Yes
	DP_Datapath_XXXXXXXX_Doc.pdf (can be any file format)	Yes
	Is working	Yes

02	Processor_XXXXXXXX.srscs (including all subdirectories and files)	Yes
	Processor_XXXXXXXX.xpr	Yes

	CSU22022 Milestone Register File 2024-2025 Checklist V2.0	Yes
	CSU22022 Milestone Function Unit 2024-2025 Checklist V2.0	Yes

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Signature

01/11/24 Date