

CSU22022 Computer Architecture I

Prof. Michael Manzke

Assignment: Project Milestone Register File

Checklist

30th October 2024

Version 2.0

All files must be submitted to Blackboard as individual files, no zip files. Submit only entities that are working and are fully tested. You must follow the entity order on the checklist. You cannot skip entities.

The entities must be programmed and tested in the following order. Please confirm by ticking all the boxes next the items that all required files are available on Blackboard and that the entity is fully working. You must print this checklist, tick the boxes, sign it, and submit it to Blackboard.

1	RF_Mux3_1Bit_XXXXXXXX.vhd	Yes
	RF_Mux3_1Bit_XXXXXXXX_TB.vhd	Yes
	RF_Mux3_1Bit_XXXXXXXX_SchematicXX.pdf	Yes
	RF_Mux3_1Bit_XXXXXXXX_TDXX.png	Yes
	RF_Mux3_1Bit_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

2	RF_Mux3_32Bit_XXXXXXXX.vhd	Yes
	RF_Mux3_32Bit_XXXXXXXX_TB.vhd	Yes
	RF_Mux3_32Bit_XXXXXXXX_SchematicXX.pdf	Yes
	RF_Mux3_32Bit_XXXXXXXX_TDXX.png	Yes
	RF_Mux3_32Bit_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

3	RF_Mux16_1Bit_XXXXXXXX.vhd	Yes
	RF_Mux16_1Bit_XXXXXXXX_TB.vhd	Yes
	RF_Mux16_1Bit_XXXXXXXX_SchematicXX.pdf	Yes
	RF_Mux16_1Bit_XXXXXXXX_TDXX.png	Yes
	RF_Mux16_1Bit_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

4	RF_Mux16_32Bit_XXXXXXXX.vhd	Yes
	RF_Mux16_32Bit_XXXXXXXX_TB.vhd	Yes
	RF_Mux16_32Bit_XXXXXXXX_SchematicXX.pdf	Yes
	RF_Mux16_32Bit_XXXXXXXX_TDXX.png	Yes
	RF_Mux16_32Bit_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

5	RF_Mux32_1Bit_XXXXXXXX.vhd	Yes
	RF_Mux32_1Bit_XXXXXXXX_TB.vhd	Yes
	RF_Mux32_1Bit_XXXXXXXX_SchematicXX.pdf	Yes
	RF_Mux32_1Bit_XXXXXXXX_TDXX.png	Yes
	RF_Mux32_1Bit_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

6	RF_Mux32_32Bit_XXXXXXXX.vhd	Yes
	RF_Mux32_32Bit_XXXXXXXX_TB.vhd	Yes
	RF_Mux32_32Bit_XXXXXXXX_SchematicXX.pdf	Yes
	RF_Mux32_32Bit_XXXXXXXX_TDXX.png	Yes
	RF_Mux32_32Bit_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

7	RF_DFlipFlop_XXXXXXXX.vhd	Yes
	RF_DFlipFlop_XXXXXXXX_TB.vhd	Yes
	RF_DFlipFlop_XXXXXXXX_SchematicXX.pdf	Yes
	RF_DFlipFlop_XXXXXXXX_TDXX.png	Yes
	RF_DFlipFlop_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

8	RF_Register32Bit_XXXXXXXX.vhd	Yes
	RF_Register32Bit_XXXXXXXX_TB.vhd	Yes
	RF_Register32Bit_XXXXXXXX_SchematicXX.pdf	Yes
	RF_Register32Bit_XXXXXXXX_TDXX.png	Yes
	RF_Register32Bit_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

9	RF_DestReg_Decoder_XXXXXXXX.vhd	Yes
	RF_DestReg_Decoder_XXXXXXXX_TB.vhd	Yes
	RF_DestReg_Decoder_XXXXXXXX_SchematicXX.pdf	Yes
	RF_DestReg_Decoder_XXXXXXXX_TDXX.png	Yes
	RF_DestReg_Decoder_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes


10	RF_TempDestReg_Decoder_XXXXXXXX.vhd	Yes
	RF_TempDestReg_Decoder_XXXXXXXX_TB.vhd	Yes
	RF_TempDestReg_Decoder_XXXXXXXX_SchematicXX.pdf	Yes
	RF_TempDestReg_Decoder_XXXXXXXX_TDXX.png	Yes
	RF_TempDestReg_Decoder_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

11	RF_RegisterFile_32_15_XXXXXXXX.vhd	Yes
	RF_RegisterFile_32_15_XXXXXXXX_TB.vhd	Yes
	RF_RegisterFile_32_15_XXXXXXXX_SchematicXX.pdf	Yes
	RF_RegisterFile_32_15_XXXXXXXX_TDXX.png	Yes
	RF_RegisterFile_32_15_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

12	RF_Test_RegisterFile_32_15_XXXXXXXX.vhd	Yes
	RF_Test_RegisterFile_32_15_XXXXXXXX_TB.vhd	Yes
	RF_Test_RegisterFile_32_15_XXXXXXXX_SchematicXX.pdf	Yes
	RF_Test_RegisterFile_32_15_XXXXXXXX_TDXX.png	Yes
	RF_Test_RegisterFile_32_15_XXXXXXXX_Doc.tex (can be any file format)	Yes
	Is working	Yes

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 01/11/24 Date
 Signature