

<!-- About me

Ph.D. student in Department of Electronics and Electrical Engineering, Keio University, Japan. -->

Research Interests

- Sparse signal recovery
- Learning in the presence of outliers
- Convex optimization
- Manifold learning

Education

- Ph.D. in Integrated Design Engineering (Major: Electronics and Electrical Engineering) (Sept. 2021 - present)
 - Keio University
 - Supervisor: Prof. Masahiro Yukawa
- M.E. in Integrated Design Engineering (Major: Electronics and Electrical Engineering) (Apr. 2020 - Sept. 2021)
 - Keio University
 - Supervisor: Prof. Masahiro Yukawa
 - GPA 4.00/4.00
- B.E. in Electronics and Electrical Engineering (Apr. 2016 - Mar. 2020)
 - Keio University
 - Supervisor: Prof. Masahiro Yukawa

Awards

- **信号処理若手奨励賞** (Nov. 2023)
- **信号処理研究会賞** (Aug. 2023)
- Best Undergraduate Research Award (Mar. 2020)
 - The best undergraduate research award in Dept. Electronics and Electrical Engineering, Keio Univ.

Employment

- JSPS Research fellow DC1 (Apr. 2022 - present)
- Research Assistant at Dept. Electronics and Electrical Engineering, Keio (Nov. 2019 -- Feb. 2020, Nov. 2020 -- Mar. 2022)
- Teaching Assistant at Dept. Electronics and Electrical Engineering, Keio (Nov. 2020 -- Feb. 2021)

Last update: Jan. 2024