CS1550 Lab 4

Memory layout

```
int t = 0; // Data
int m; // BSS
int main() {
                             // Stack
         int i;
         static int j;
                             // BSS
         // ptr: Stack
         // 4B pointed by ptr: Heap
         char * ptr = (char*)malloc(4);
          // mptr: Stack
         // 4K pointed by mptr: memory Mapping
         char * mptr = (char*)mmap(...,4096,...);
```

High address

Kernel Space	
Stack	Stack/local
Mapping	mmap()
A	
Неар	malloc()
BSS	uninitialized globals and statics
Data	initialized globals and statics
Text	instructions

Program break

High address Kernel Space Stack Mapping Program break 0x8000000 Heap BSS Data Text

Program break: sbrk

High address

Kernel Space

Stack



Mapping

cur_brk: 0x8000000

void *cur_brk = sbrk(0); void *old_brk = sbrk(4096); void *new_brk = sbrk(0);

Program break Heap BSS Data Text

Program break: sbrk

void *cur_brk = sbrk(0); **void** ***old_brk** = **sbrk(4096)**; void *new_brk = sbrk(0);

High address

Kernel Space Stack **Mapping** Heap BSS Data **Text**

Program break

0x8001000: increase 0x8000000 by 4K

old_brk, cur_brk: 0x8000000

Program break: sbrk

void *cur_brk = sbrk(0);
void *old_brk = sbrk(4096);
void *new_brk = sbrk(0);

High address

Kernel Space Stack Mapping Heap BSS Data Text

Program break

new_brk: 0x8001000 cur_brk, old_brk: 0x8000000

Program break: brk

```
High address
```

Kernel Space Stack Mapping int ret = brk(0x8001000); if (ret != 0) { Set program break to — // error Heap BSS Data **Text**

New: **0x8001000** Old: 0x8000000

Sbrk on XV6

The sys_sbrk() in sysproc.c is a XV-6 implementation for sbrk.

```
addr = myproc()->sz;  // get current brk
if(growproc(n) < 0)  // increase brk by n
    return -1;
...
return addr;  // return old brk</pre>
```

growproc

The growproc() in proc.c:

Physical memory allocation

Given 4KB per page, and allocating an array with size of 100 pages: char * ptr = (char*) malloc (4096 * 100);

Note:

This only allocates virtual memory: ptr to ptr+4096*100 How about physical memory?

XV6: Immediately allocate all 100 physical page frames Problems?

Physical memory allocation

```
Given 4KB per page, and allocating an array with size of 100 pages:

char * ptr = (char*) malloc (4096 * 100);

// assume ptr is 0x8000000, i.e., a page-aligned virtual address

Note: malloc() only allocates virtual memory: ptr to ptr+4096*100

How about physical memory?
```

Lab 4: allocate physical page frame upon the 1^{st} access on that page. ptr[4096*99 + 50] = 'a'; // the 1^{st} access on the 100^{th} page

Page Fault

Page Table: Page table stores mapping from virtual page to physical page frame E.g., Virtual Page 0x8000000 -> Physical 0x400000

Translating a virtual address to physical address:

Virtual address \rightarrow (TLB \rightarrow) Page Table \rightarrow Physical address

Given 4KB page-size, translating virtual address 0x0080005:

- 1. Get its page number 0x0080, and page offset is 5.
- Search (TLB &) Page Table to find the mapping of 0x0080 (how it works in multilevel page tables?)
- If found, e.g., 0x0080->0x4000, then physical address is 0x4000005. If not found, Page Fault

Page Fault

```
char *ptr = (char*) malloc(4096*100);
ptr[4096*99 + 50] = 'a'; // 1<sup>st</sup> access, no physical page frame: Page Fault
```

In XV6, Page Fault on ptr[4096*99 + 50] (inside 100th page):

- 1. Issue Page Fault trap. All traps are handled by trap() in trap.c.
- 2. Handle Page Fault (*Hint: T_PGFLT, how to find the faulting addr*) in trap():
 - 1) Allocate a physical page frame for this 100th page
 - 2) Update page table

Allocate physical pages, update page table

Recall the growproc() in proc.c.