

Chapter 1: Basic Concepts

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Chapter Overview

- **Welcome to Assembly Language**
- Virtual Machine Concept
- Data Representation
- Boolean Operations

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Welcome to Assembly Language

- How does assembly language (AL) relate to machine language?
- How do C++ and Java relate to AL?
- Is AL portable?
- Why learn AL?

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Assembly Language Applications

- Some representative types of applications:
 - Business application for single platform
 - Hardware device driver
 - Business application for multiple platforms
 - Embedded systems & computer games

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Comparing ASM to High-Level Languages

Type of Application	High-Level Languages	Assembly Language
Business application software, written for single platform, medium to large size.	Formal structures make it easy to organize and maintain large sections of code.	Minimal formal structure, so one must be imposed by programmers who have varying levels of experience. This leads to difficulties maintaining existing code.
Hardware device driver.	Language may not provide for direct hardware access. Even if it does, awkward coding techniques must often be used, resulting in maintenance difficulties.	Hardware access is straightforward and simple. Easy to maintain when programs are short and well documented.
Business application written for multiple platforms (different operating systems).	Usually very portable. The source code can be recompiled on each target operating system with minimal changes.	Must be recoded separately for each platform, often using an assembler with a different syntax. Difficult to maintain.
Embedded systems and computer games requiring direct hardware access.	Produces too much executable code, and may not run efficiently.	Ideal, because the executable code is small and runs quickly.

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What's Next

- Welcome to Assembly Language
- **Virtual Machine Concept**
- Data Representation
- Boolean Operations

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Virtual Machine Concept

- Virtual Machines
- Specific Machine Levels

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Virtual Machines

- Tanenbaum: [Virtual machine concept](#)
- Programming Language analogy:
 - Each computer has a native machine language (language L0) that runs directly on its hardware
 - A more human-friendly language is usually constructed above machine language, called Language L1
- Programs written in L1 can run two different ways:
 - [Interpretation](#) – L0 program interprets and executes L1 instructions one by one
 - [Translation](#) – L1 program is completely translated into an L0 program, which then runs on the computer hardware

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Translating Languages

English: Display the sum of A times B plus C.

C++: `cout << (A * B + C);`

Assembly Language:

```
mov eax,A
mul B
add eax,C
call WriteInt
```

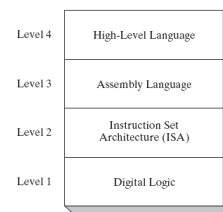
Intel Machine Language:

```
A1 00000000
F7 25 00000004
03 05 00000008
E8 00500000
```

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Specific Machine Levels



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High-Level Language

- Level 4
- Application-oriented languages
 - C++, Java, Pascal, Visual Basic . . .
- Programs compile into assembly language (Level 4)

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Assembly Language

- Level 3
- Instruction mnemonics that have a one-to-one correspondence to machine language
- Programs are translated into Instruction Set Architecture Level - machine language (Level 2)

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Instruction Set Architecture (ISA)

- Level 2
- Also known as **conventional machine language**
- Executed by Level 1 (Digital Logic)

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Digital Logic

- Level 1
- CPU, constructed from digital logic gates
- System bus
- Memory
- Implemented using bipolar transistors

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What's Next

- Welcome to Assembly Language
- Virtual Machine Concept
- **Data Representation**
- Boolean Operations

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Data Representation

- Binary Numbers
 - Translating between binary and decimal
- Binary Addition
- Integer Storage Sizes
- Hexadecimal Integers
 - Translating between decimal and hexadecimal
 - Hexadecimal subtraction
- Signed Integers
 - Binary subtraction
- Character Storage

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Binary Numbers

- Digits are 1 and 0
 - 1 = true
 - 0 = false
- MSB – most significant bit
- LSB – least significant bit
- Bit numbering:

MSB	1	0	1	1	0	0	1	0	1	1	0	0	LSB
15													0

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Binary Numbers

- Each digit (bit) is either 1 or 0
- Each bit represents a power of 2:

1	1	1	1	1	1	1	1
2^7	2^6	2^5	2^4	2^3	2^2	2^1	2^0

Every binary number is a sum of powers of 2

Table 1-3 Binary Bit Position Values.

2^n	Decimal Value	2^n	Decimal Value
2^0	1	2^8	256
2^1	2	2^9	512
2^2	4	2^{10}	1024
2^3	8	2^{11}	2048
2^4	16	2^{12}	4096
2^5	32	2^{13}	8192
2^6	64	2^{14}	16384
2^7	128	2^{15}	32768

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Translating Binary to Decimal

Weighted positional notation shows how to calculate the decimal value of each binary bit:

$$dec = (D_{n-1} \times 2^{n-1}) + (D_{n-2} \times 2^{n-2}) + \dots + (D_1 \times 2^1) + (D_0 \times 2^0)$$

D = binary digit

binary 00001001 = decimal 9:

$$(1 \times 2^3) + (1 \times 2^0) = 9$$

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Translating Unsigned Decimal to Binary

- Repeatedly divide the decimal integer by 2. Each remainder is a binary digit in the translated value:

Division	Quotient	Remainder
37 / 2	18	1
18 / 2	9	0
9 / 2	4	1
4 / 2	2	0
2 / 2	1	0
1 / 2	0	1

$$37 = 100101$$

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Binary Addition

- Starting with the LSB, add each pair of digits, include the carry if present.

carry: 1							
0	0	0	0	0	1	0	0
+	0	0	0	0	0	1	1
0	0	0	0	0	1	1	1
bit position: 7 6 5 4 3 2 1 0							

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Integer Storage Sizes

Standard sizes:

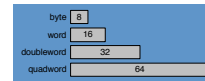


Table 1-4 Ranges of Unsigned Integers.

Storage Type	Range (low-high)	Powers of 2
Unsigned byte	0 to 255	0 to $(2^8 - 1)$
Unsigned word	0 to 65,535	0 to $(2^{16} - 1)$
Unsigned doubleword	0 to 4,294,967,295	0 to $(2^{32} - 1)$
Unsigned quadword	0 to 18,446,744,073,709,551,615	0 to $(2^{64} - 1)$

What is the largest unsigned integer that may be stored in 20 bits?

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Hexadecimal Integers

Binary values are represented in hexadecimal.

Table 1-5 Binary, Decimal, and Hexadecimal Equivalents.

Binary	Decimal	Hexadecimal	Binary	Decimal	Hexadecimal
0000	0	0	1000	8	8
0001	1	1	1001	9	9
0010	2	2	1010	10	A
0011	3	3	1011	11	B
0100	4	4	1100	12	C
0101	5	5	1101	13	D
0110	6	6	1110	14	E
0111	7	7	1111	15	F

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Translating Binary to Hexadecimal

- Each hexadecimal digit corresponds to 4 binary bits.
- Example: Translate the binary integer 0001011010011110010100 to hexadecimal:

1	6	A	7	9	4
0001	0110	1010	0111	1001	0100

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Converting Hexadecimal to Decimal

- Multiply each digit by its corresponding power of 16:

$$\text{dec} = (D_3 \times 16^3) + (D_2 \times 16^2) + (D_1 \times 16^1) + (D_0 \times 16^0)$$

- Hex 1234 equals $(1 \times 16^3) + (2 \times 16^2) + (3 \times 16^1) + (4 \times 16^0)$, or decimal 4,660.
- Hex 3BA4 equals $(3 \times 16^3) + (11 \times 16^2) + (10 \times 16^1) + (4 \times 16^0)$, or decimal 15,268.

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Powers of 16

Used when calculating hexadecimal values up to 8 digits long:

16^n	Decimal Value	16^n	Decimal Value
16^0	1	16^4	65,536
16^1	16	16^5	1,048,576
16^2	256	16^6	16,777,216
16^3	4096	16^7	268,435,456

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Converting Decimal to Hexadecimal

Division	Quotient	Remainder
422 / 16	26	6
26 / 16	1	A
1 / 16	0	1

decimal 422 = 1A6 hexadecimal

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Hexadecimal Addition

- Divide the sum of two digits by the number base (16). The quotient becomes the carry value, and the remainder is the sum digit.

36	28	¹ 28	¹ 6A
42	45	58	4B
78	6D	80	B5

↑
21 / 16 = 1, rem 5

Important skill: Programmers frequently add and subtract the addresses of variables and instructions.

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Hexadecimal Subtraction

- When a borrow is required from the digit to the left, add 16 (decimal) to the current digit's value:

	16 + 5 = 21
	↓
C6	75
A2	47
24	2E

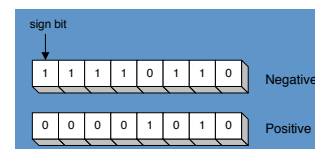
Practice: The address of var1 is 00400020. The address of the next variable after var1 is 0040006A. How many bytes are used by var1?

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Signed Integers

The highest bit indicates the sign. 1 = negative, 0 = positive



If the highest digit of a hexadecimal integer is > 7, the value is negative. Examples: 8A, C5, A2, 9D

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Forming the Two's Complement

- Negative numbers are stored in two's complement notation
- Represents the **additive Inverse**

Starting value	00000001
Step 1: reverse the bits	11111110
Step 2: add 1 to the value from Step 1	11111110 +00000001
Sum: two's complement representation	11111111

Note that $00000001 + 11111111 = 00000000$

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Binary Subtraction

- When subtracting $A - B$, convert B to its two's complement
- Add A to $(-B)$

```

      _____
00001100      00001100
-00000011  + 11111101
            00001001
  
```

Practice: Subtract 0101 from 1001.

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Learn How To Do the Following:

- Form the two's complement of a hexadecimal integer
- Convert signed binary to decimal
- Convert signed decimal to binary
- Convert signed decimal to hexadecimal
- Convert signed hexadecimal to decimal

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Ranges of Signed Integers

The highest bit is reserved for the sign. This limits the range:

Storage Type	Range (low-high)	Powers of 2
Signed byte	-128 to +127	-2^7 to $(2^7 - 1)$
Signed word	-32,768 to +32,767	-2^{15} to $(2^{15} - 1)$
Signed doubleword	-2,147,483,648 to 2,147,483,647	-2^{31} to $(2^{31} - 1)$
Signed quadword	-9,223,372,036,854,775,808 to +9,223,372,036,854,775,807	-2^{63} to $(2^{63} - 1)$

Practice: What is the largest positive value that may be stored in 20 bits?

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Character Storage

- Character sets
 - Standard ASCII (0 – 127)
 - Extended ASCII (0 – 255)
 - ANSI (0 – 255)
 - Unicode (0 – 65,535)
- Null-terminated String
 - Array of characters followed by a *null byte*

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Numeric Data Representation

- pure binary
 - can be calculated directly
- ASCII binary
 - string of digits: "01010101"
- ASCII decimal
 - string of digits: "65"
- ASCII hexadecimal
 - string of digits: "9C"

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ASCII Code (7-bit)

American Standard Code for Information Interchange

Dec	Hex	Oct	Char	Dec	Hex	Oct	Char	Dec	Hex	Oct	Char	Dec	Hex	Oct	Char
0	0000	000	NUL (null)	32	20 040	4#32	Space	64	40 100	4#64	@	96	60 140	4#96	z
1	0001	001	SOF (start of heading)	33	21 041	4#33	!	65	41 101	4#65	A	97	61 141	4#97	a
2	0002	002	STX (start of text)	34	22 042	4#34	"	66	42 102	4#66	B	98	62 142	4#98	b
3	0003	003	ETX (end of text)	35	23 043	4#35	#	67	43 103	4#67	C	99	63 143	4#99	c
4	0004	004	EOF (end of transmission)	36	24 044	4#36	\$	68	44 104	4#68	D	100	64 144	4#100	d
5	0005	005	ENQ (enquiry)	37	25 045	4#37	%	69	45 105	4#69	E	101	65 145	4#101	e
6	0006	006	ACK (acknowledge)	38	26 046	4#38	&	70	46 106	4#70	F	102	66 146	4#102	f
7	0007	007	DEL (delete)	39	27 047	4#39	'	71	47 107	4#71	G	103	67 147	4#103	g
8	0010	010	BS (backspace)	40	28 050	4#40	(72	48 110	4#72	H	104	68 148	4#104	h
9	0011	011	TAB (horizontal tab)	41	29 051	4#41)	73	49 111	4#73	I	105	69 149	4#105	i
10	0012	012	LF (line feed, new line)	42	30 052	4#42	*	74	4A 112	4#74	J	106	6A 150	4#106	j
11	0013	013	VT (vertical tab)	43	31 053	4#43	+	75	4B 113	4#75	K	107	6B 151	4#107	k
12	0014	014	FF (form feed, new page)	44	32 054	4#44	,	76	4C 114	4#76	L	108	6C 152	4#108	l
13	0015	015	CR (carriage return)	45	33 055	4#45	-	77	4D 115	4#77	M	109	6D 153	4#109	m
14	0016	016	SO (shift out)	46	34 056	4#46	.	78	4E 116	4#78	N	110	6E 154	4#110	n
15	0017	017	SI (shift in)	47	35 057	4#47	/	79	4F 117	4#79	O	111	6F 155	4#111	o
16	0020	020	DLE (data link escape)	48	36 060	4#48	:	80	50 120	4#80	P	112	70 160	4#112	p
17	0021	021	DC1 (device control 1)	49	37 061	4#49	;	81	51 121	4#81	Q	113	71 161	4#113	q
18	0022	022	DC2 (device control 2)	50	38 062	4#50	<	82	52 122	4#82	R	114	72 162	4#114	r
19	0023	023	DC3 (device control 3)	51	39 063	4#51	=	83	53 123	4#83	S	115	73 163	4#115	s
20	0024	024	DC4 (device control 4)	52	3A 064	4#52	>	84	54 124	4#84	T	116	74 164	4#116	t
21	0025	025	NAK (negative acknowledge)	53	3B 065	4#53	?	85	55 125	4#85	U	117	75 165	4#117	u
22	0026	026	SYN (synchronous idle)	54	3C 066	4#54	@	86	56 126	4#86	V	118	76 166	4#118	v
23	0027	027	ETB (end of trans. block)	55	3D 067	4#55	A	87	57 127	4#87	W	119	77 167	4#119	w
24	0030	030	CAN (cancel)	56	3E 070	4#56	[88	58 130	4#88	X	120	78 170	4#120	x
25	0031	031	EM (end of medium)	57	3F 071	4#57	\	89	59 131	4#89	Y	121	79 171	4#121	y
26	0032	032	SUB (substitute)	58	3A 072	4#58]	90	5A 132	4#90	Z	122	7A 172	4#122	z
27	0033	033	ESC (escape)	59	3B 073	4#59	^	91	5B 133	4#91	[123	7B 173	4#123	[
28	0034	034	FS (file separator)	60	3C 074	4#60	_	92	5C 134	4#92	\	124	7C 174	4#124	\
29	0035	035	GS (group separator)	61	3D 075	4#61	`	93	5D 135	4#93]	125	7D 175	4#125]
30	0036	036	RS (record separator)	62	3E 076	4#62	~	94	5E 136	4#94	^	126	7E 176	4#126	^
31	0037	037	US (unit separator)	63	3F 077	4#63	>	95	5F 137	4#95	_	127	7F 177	4#127	_

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Extended ASCII Code (8-bit)

128	Ç	144	È	160	À	176	Ï	192	Ł	208	À	224	à	240	•
129	U	145	É	161	Á	177	Ï	193	Ł	209	Á	225	á	241	•
130	U	146	Ê	162	Â	178	Ï	194	Ł	210	Â	226	â	242	•
131	U	147	Ë	163	Ã	179	Ï	195	Ł	211	Ã	227	ã	243	•
132	U	148	Ì	164	Ä	180	Ï	196	Ł	212	Ä	228	ä	244	•
133	U	149	Í	165	Å	181	Ï	197	Ł	213	Å	229	å	245	•
134	U	150	Î	166	Æ	182	Ï	198	Ł	214	Æ	230	æ	246	•
135	U	151	Ï	167	Ç	183	Ï	199	Ł	215	Ç	231	ç	247	•
136	U	152	Ï	168	È	184	Ï	200	Ł	216	È	232	è	248	•
137	U	153	Ï	169	É	185	Ï	201	Ł	217	É	233	é	249	•
138	U	154	Ï	170	Ê	186	Ï	202	Ł	218	Ê	234	ê	250	•
139	U	155	Ï	171	Ë	187	Ï	203	Ł	219	Ë	235	ë	251	•
140	U	156	Ï	172	Ì	188	Ï	204	Ł	220	Ì	236	ì	252	•
141	U	157	Ï	173	Í	189	Ï	205	Ł	221	Í	237	í	253	•
142	U	158	Ï	174	Î	190	Ï	206	Ł	222	Î	238	î	254	•
143	U	159	Ï	175	Ï	191	Ï	207	Ł	223	Ï	239	ï	255	•

Source: www.LeskyTables.com

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What's Next

- Welcome to Assembly Language
- Virtual Machine Concept
- Data Representation
- Boolean Operations**

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Boolean Operations

- NOT
- AND
- OR
- Operator Precedence
- Truth Tables

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Boolean Algebra

- Based on **symbolic logic**, designed by George Boole
- Boolean expressions created from:

Expression	Description
$\neg X$	NOT X
$X \wedge Y$	X AND Y
$X \vee Y$	X OR Y
$\neg X \vee Y$	(NOT X) OR Y
$\neg(X \wedge Y)$	NOT (X AND Y)
$X \wedge \neg Y$	X AND (NOT Y)

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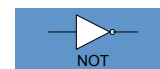
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NOT

- Inverts (reverses) a boolean value
- Truth table for Boolean NOT operator:

X	$\neg X$
F	T
T	F

Digital gate diagram for NOT:



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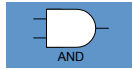
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AND

- Truth table for Boolean AND operator:

X	Y	$X \wedge Y$
F	F	F
F	T	F
T	F	F
T	T	T

Digital gate diagram for AND:



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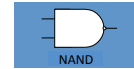
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NAND

- Truth table for Boolean NAND operator:

X	Y	$X \wedge Y$
F	F	T
F	T	T
T	F	T
T	T	F

Digital gate diagram for NAND:



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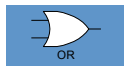
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OR

- Truth table for Boolean OR operator:

X	Y	$X \vee Y$
F	F	F
F	T	T
T	F	T
T	T	T

Digital gate diagram for OR:



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NOR

- Truth table for Boolean NOR operator:

X	Y	$X \vee Y$
F	F	T
F	T	F
T	F	F
T	T	F

Digital gate diagram for NOR:



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Operator Precedence

- Examples showing the order of operations:

Expression	Order of Operations
$\neg X \vee Y$	NOT, then OR
$\neg(X \vee Y)$	OR, then NOT
$X \vee (Y \wedge Z)$	AND, then OR

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Truth Tables (1 of 3)

- A **Boolean function** has one or more Boolean inputs, and returns a single Boolean output.
- A **truth table** shows all the inputs and outputs of a Boolean function

Example: $\neg X \vee Y$

X	$\neg X$	Y	$\neg X \vee Y$
F	T	F	T
F	T	T	T
T	F	F	F
T	F	T	T

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Truth Tables (2 of 3)

- Example: $X \wedge \neg Y$

X	Y	$\neg Y$	$X \wedge \neg Y$
F	F	T	F
F	T	F	F
T	F	T	T
T	T	F	F

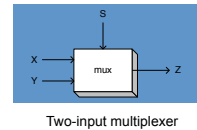
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Truth Tables (3 of 3)

- Example: $(Y \wedge S) \vee (X \wedge \neg S)$

X	Y	S	$Y \wedge S$	$\neg S$	$X \wedge \neg S$	$(Y \wedge S) \vee (X \wedge \neg S)$
F	F	F	F	T	F	F
F	T	F	F	T	F	F
T	F	F	F	T	T	T
T	T	F	F	T	T	T
F	F	T	F	F	F	F
F	T	T	T	F	F	T
T	F	T	F	F	F	F
T	T	T	T	F	F	T



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Summary

- Assembly language helps you learn how software is constructed at the lowest levels
- Assembly language has a one-to-one relationship with machine language
- Each layer in a computer's architecture is an abstraction of a machine
 - layers can be hardware or software
- Boolean expressions are essential to the design of computer hardware and software

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54 68 65 20 45 6E 64

What do these numbers represent?

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