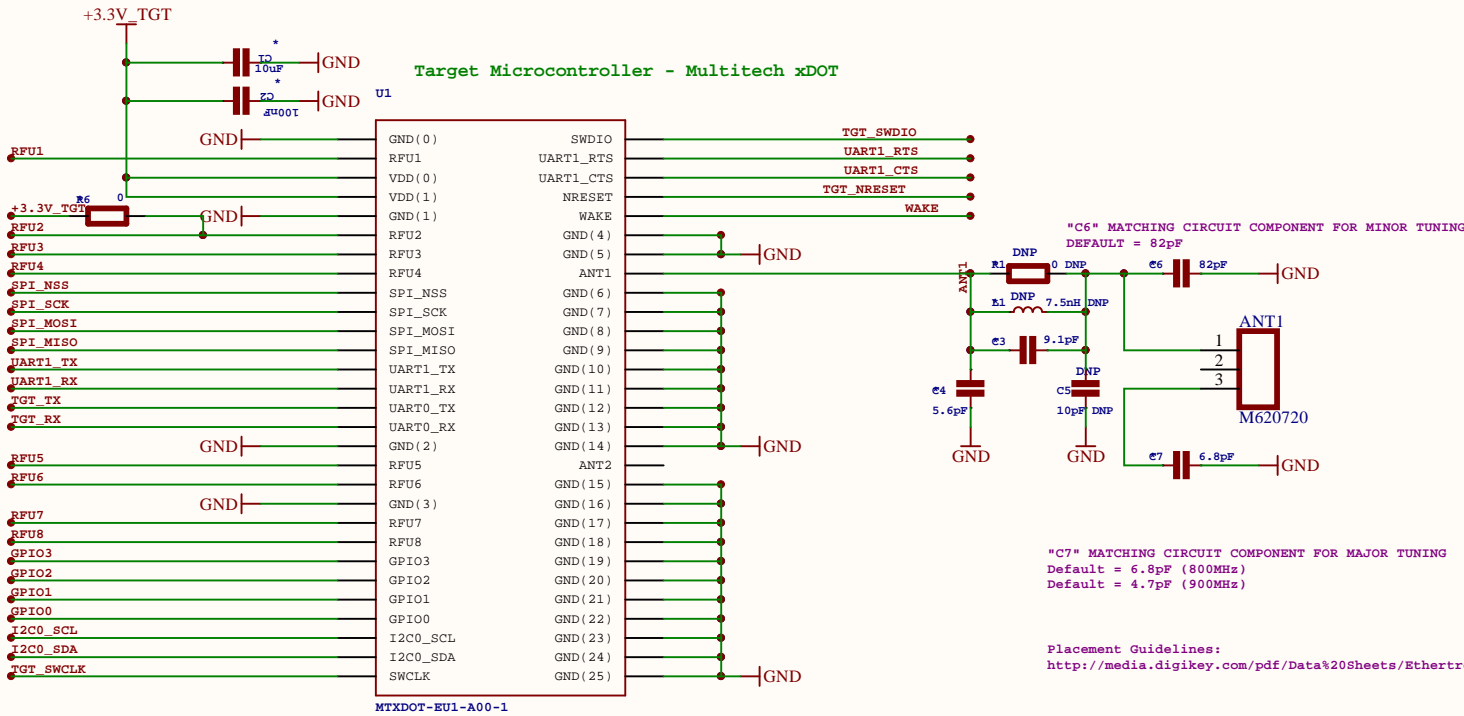
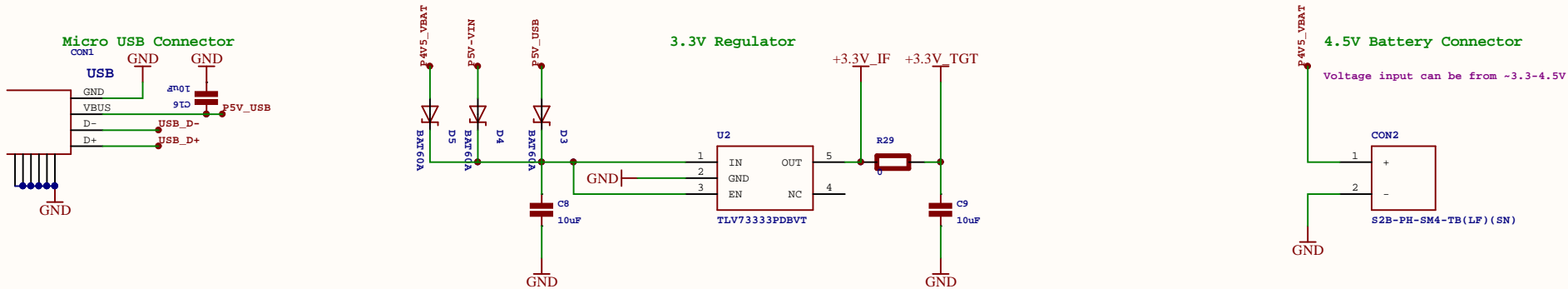


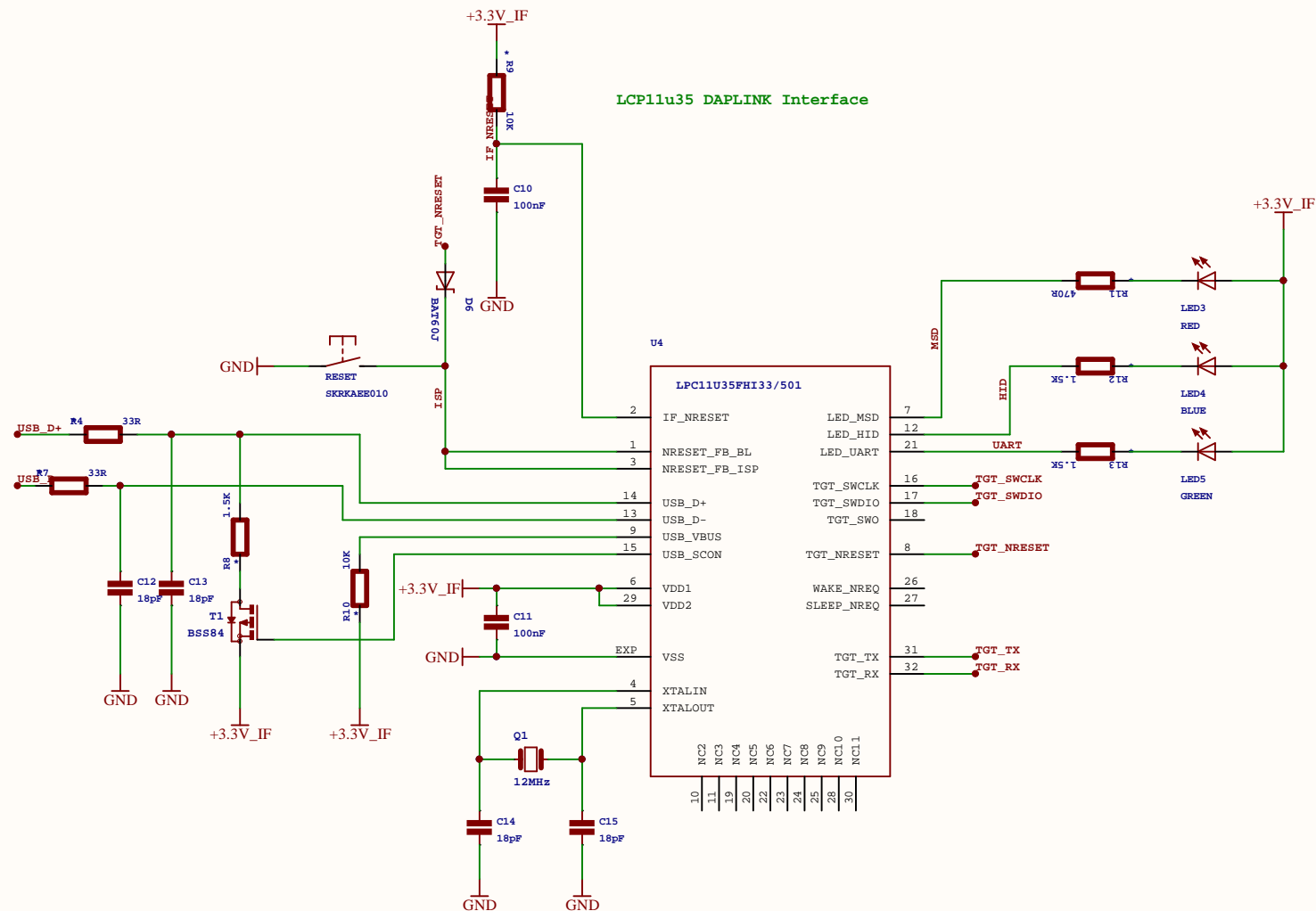
TARGET MCU



POWER



DAPLINK INTERFACE

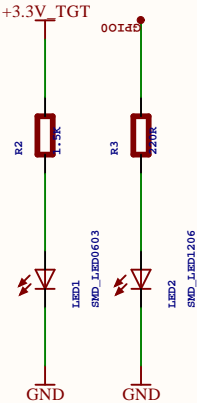
***FF1705***

mbedXdotShield-V_1_2_0_2.SchDoc	Revision: v1.2.1
Drawn By: Marko Stušek	Approved By: Matej Slapšak
Checked By: Rok Sinkovec	Sheet 3 of 5
Date: 1. 09. 2017	www.ltek.si info@ltek.si



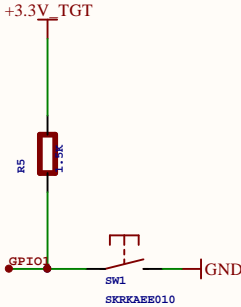
PERIPHERALS

Power LED

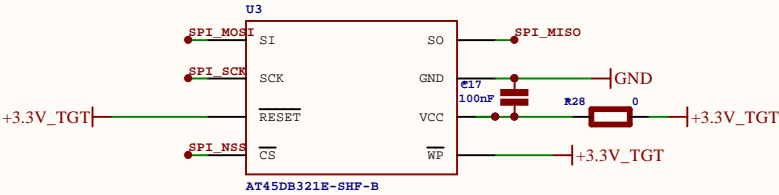


User LED

SW2



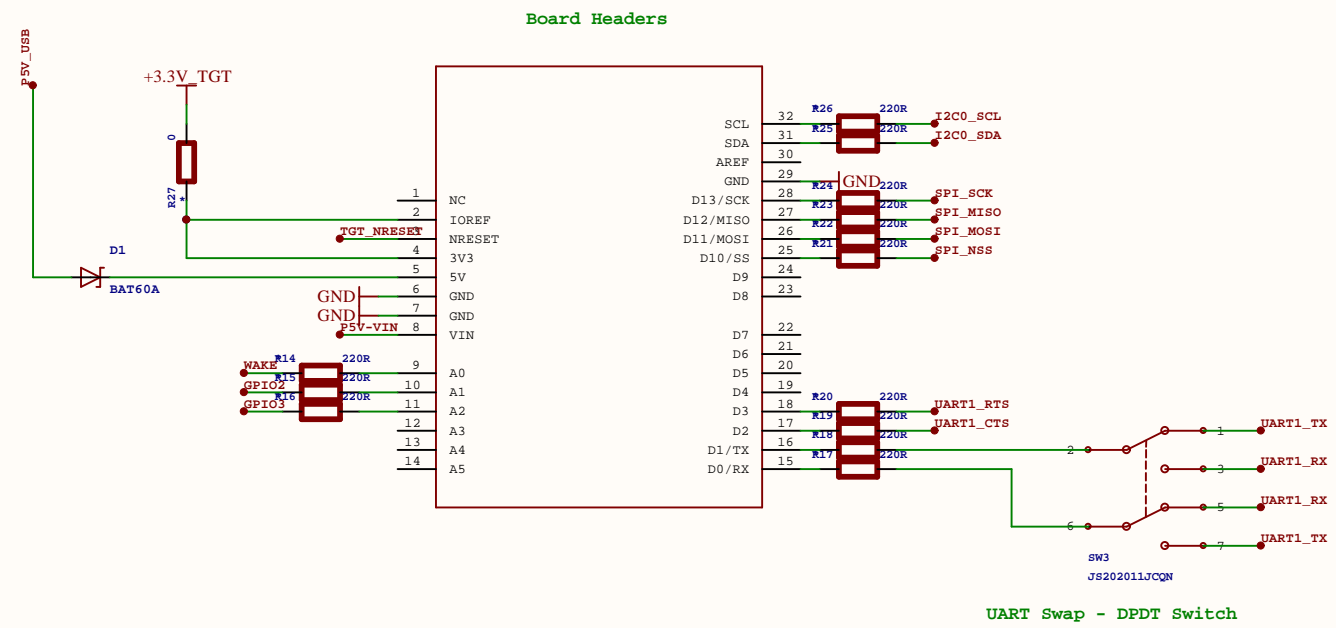
SPI Flash Memory



FF1705	
mbedXdotShield-V_1_2_0_3.SchDoc	Revision: v1.2.1
Drawn By: Marko Stušek	Approved By: Matej Slapšak
Checked By: Rok Sinkovec	Sheet 4 of 5
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BOARD HEADERS



FF1705	
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Drawn By: Marko Stussek	Approved By: Matej Slapšak
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L-Tek FF1705
v1.2.1

4.5V

BATTERY
USB
RESET
DAPLINK

I2C_SCL

I2C_SDA

GND

SPI_SCK

SPI_MISO

SPI_MOSI

SPI_NSS

N
Y

UART_RTS

UART_CTS

UART_TX

UART_RX

SW1

ARM
mbed
enabled

L-TEK

<https://developer.mbed.org/platforms/L-Tek-FF1705/>

3V3

nRESET

3V3

5V

GND

GND

VIN

WAKE

GPIO2

GPIO3

XDOT MCU
FLASH MEMORY
UART SWAP
LORA ANTENNA

LED1

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