Eli Granade's Journal

9/27/2023
We only spent the remaining class time that we had at the time.
We decided to do a load store implementation becuase it is the fastest and most simplest for our purposes.
We decided to have the following registers:
A zero/ground register, a return address register, a stack pointer, two argument/return registers, and three temporary registers. We chose 8 registers so our main add command can be 12 bits for registers and a four bit opcode. We chose a four bit opcode since we're restricted to 16 bit commands.

10/3/23
I helped complete the design document that is due for milestone one, spending an hour after class.
Gave us the ability to add more R types by extending the opcode of the R type instruction, while simultaneously removing dead bits in the instruction type.

I was not assigned any tasks for the next milestone, we will be discussing how we will divide the work when all team members are next able to meet as the whole.