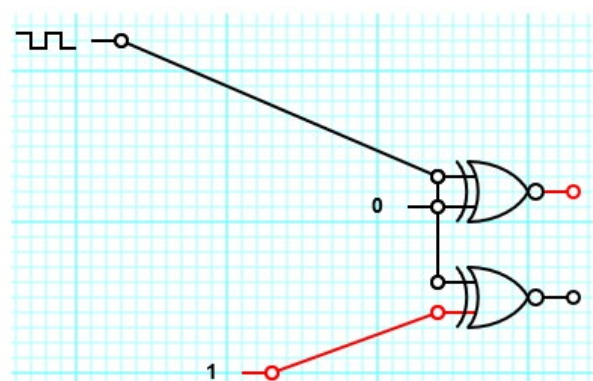
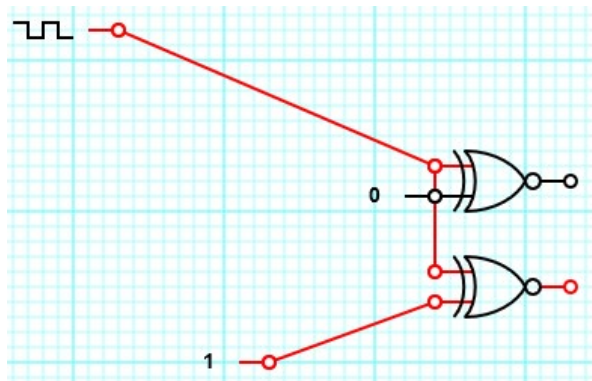


https://sciencedemos.org.uk/logic_gates.php

XNOR Gate



Alternatively, 1 **OR** gate + **NAND** gate