LAB REPORT FOR EXP 2

COURSE TITLE: EEE 416

Name: Md Maisoon Rahman

ID: 1606038 Sec: A2

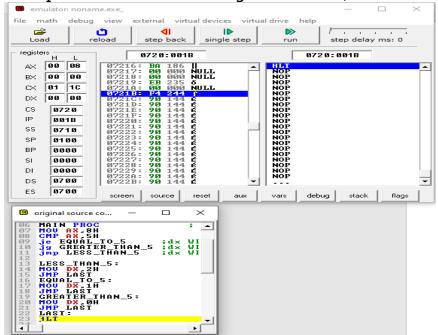
HOMEWORK REPORT 1

CODE:

```
.MODEL SMALL
.STACK 100H
                                      depends on the size of the code
02
                                      ; 100h hexadecimal stack memory
03
    .DATA
04
05
     .CODE
MAIN PROC
                                      ; almost like int main
          MOU AX,3H
CMP AX,5H
je EQUAL_TO_5
jg GREATER_THAN_5
07
98
                                      ;dx WILL BE 1
09
                                     ;dx WILL BE 0
10
           jmp LESS_THAN_5
                                      ;dx WILL BE 2
11
13 LESS_THAN_5:
14 MOU DX,2H
15 JMP LAST
16 EQUAL_TO_5:
17 MOU DX,1H
18 JMP LAST
19 GREATER_THAN_5:
20 MOU DX,0H
21 JMP LAST
21
22
23
    LAST:
          HLT
24
25
   MAIN ENDP
END MAIN;
```

OUTPUT:

For input case 8h which will be greater that 5h, the following result was obtained



Here, as required, 0 is assigned to DX, as the number is greater than 5h.

For input case 5h, the following output was obtained: original source co... emulator: noname.exe_ .MODEL SMALL .STACK 100H .DATA math ; d ▲ debug view external virtual devices virtual drive Load reload step back single step step delay ms: 0 .CODE
MAIN PROC
MOU AX.5H
CMP AX.5H registers 0720:001B 0720:001B je EQUAL_TO_5 jg GREATER_THAN_5 jmp LESS_THAN_5 00 05 WI ΑX 90 90 90 dx NOP NOP BΧ 00 00 0721D: 0721E: 144 144 LESS_THAN_5: MOU DX,2H JMP LAST |01 ||1C 90 144 90 144 CX 0721F: NOP 07220: NOP 00 01 DΧ 07221: 07222: 90 144 90 144 NOP THE LAST
EQUAL_TO_5:
MOU DX.1H
JMP LAST
GREATER_THAN_5:
MOU DX.0H NŎP 90 144 90 144 90 144 90 144 90 144 90 144 07223: 07224: NOP NOP CS 0720 IΡ 07225: 07226: 07227: 07228: 001B NOP NOP MOU DX.0 SS 0710 NOP LAST: NOF SP 0100 90 144 90 144 07229: 0722A: NOP ВР 0000 MAIN ENDP END MAIN ; NOP NOP 0722B: 9Й SI 0000 Ø722C: 144 90 144 90 144 90 144 DI 9999 0722E: 0722F: NOP

debug

stack

flags

Here, as required, 1 is assigned to DX, as the number is equal to 5h

NOP

For input 3h, the following output was obtained:

F4 244

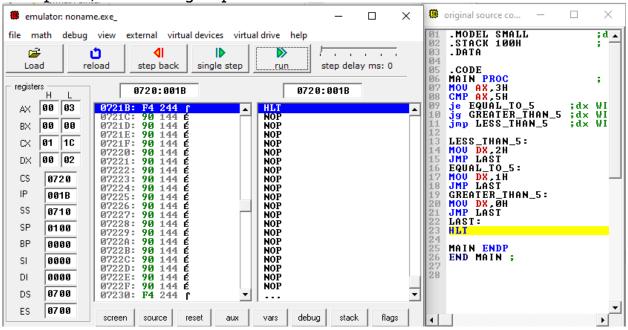
0700

0700

07230:

DS

ES



Here, as required, 2 is assigned to DX, as the number is less than 5h

HOMEWORK REPORT 2

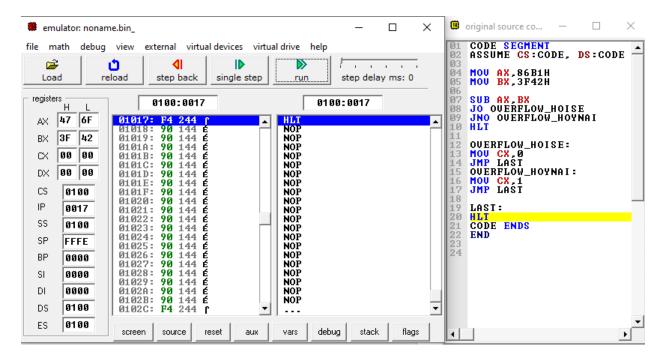
CODE:

```
CODE SEGMENT
ASSUME CS:CODE, DS:CODE
01
03
      MOU AX,86B1H
MOU BX,3F42H
04
05
06
      SUB AX,BX
JO OVERFLOW_HOISE
JNO OVERFLOW_HOYNAI
07
08
09
10
      HLT
11
    OUERFLOW_HOISE:
12
      MOU CX, 0
JMP LAST
13
14
     OUERFLOW_HOYNAI:
15
      MOU CX,1
JMP LAST
17
18
19
    LAST:
20
      HLT
    CODE ENDS
      END
```

OUTPUT:

For 86B1 - 3F42, there will be overflow.

Therefore, for overflow, register CX should be 0. The desired output was observed in our simulation.



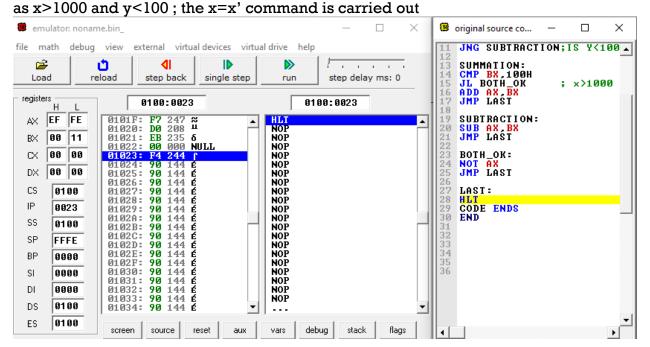
HOMEWORK REPORT 3

CODE:

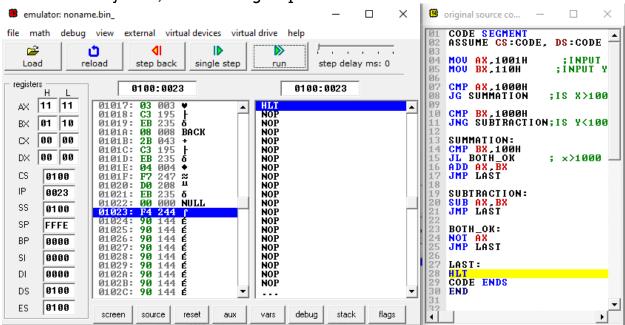
```
CODE SEGMENT
02
     ASSUME CS:CODE, DS:CODE
03
     MOU AX,10H
MOU BX,11H
04
                        ; INPUT X
05
                        INPUT Y
06
07
     CMP AX,1000H
     JG SUMMATION
Ø8
                        ; IS X>1000?
09
     CMP BX,1000H
10
     JNG SUBTRACTION; IS Y<1000?
11
12
13
    SUMMATION:
     CMP BX,100H
14
     JL BOTH_OK
                        ; x>1000 AND Y<100
15
     ADD AX,BX
JMP LAST
16
17
18
19
   SUBTRACTION:
20
     SUB AX,BX
JMP LAST
21
22
23
   BOTH_OK:
24
     NOT AX
JMP LAST
25
26
27
28
   LAST:
   CODE ENDS
29
30
     END
```

OUTPUT:

For output x=1001 and y=11;

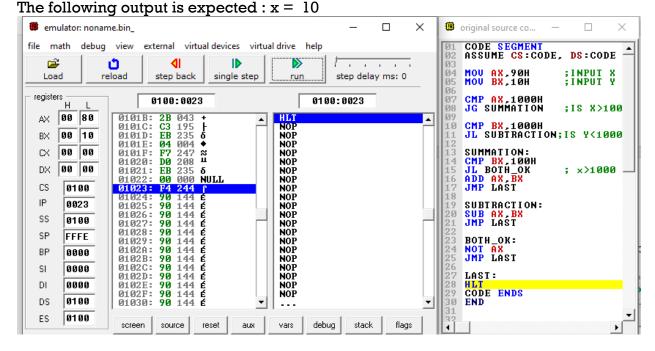


For x=1000 and y=110; as x>1000 and y>100, the following output was obtained.



As expected, x+y command is carried out.

For x=90, y=80; the following command should have been carried out x=x-y



CODE:

```
CODE SEGMENT

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CODE AS, CS; these two lines are here to ensure DS-CS. Remember, MOU DS, CS is illegal

CODE NOU AX, CS; these two lines are here to ensure DS-CS. Remember, MOU DS, CS is illegal

CODE NOU DS, AX; you may ignore these line in EMU. But they are necessary in Hardware

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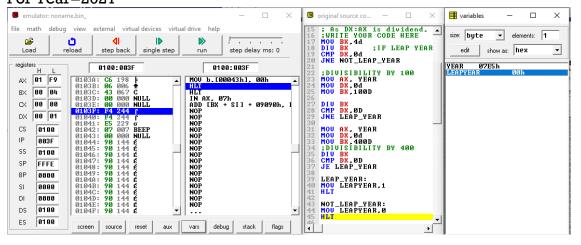
CODE NOU DS, YEAR

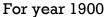
CODE HERE

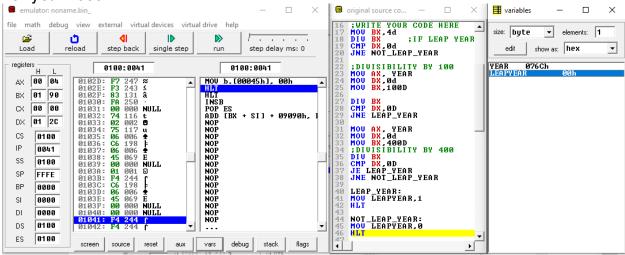
CODE NOU DS, YOU NOU DS, YOU WILL BE TO THE TOTAL THE T
```

OUTPUT:

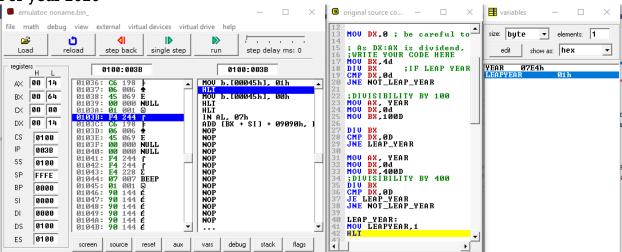
For Year=2021







For year 2020



For year 2000

