

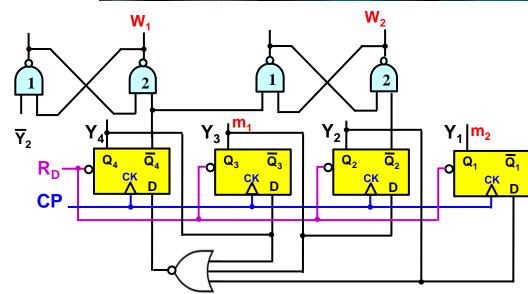
# Unit 9

——Registers and Counters

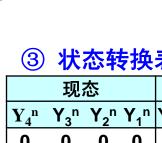
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#### 几种典型的时序逻辑部件 节拍发生器2



1



输入万桯	② 次态方程
$\int D_1 = Y_2$	$\mathbf{Y_1}^{n+1} = \mathbf{Y_2}$
$\int D_2 = Y_3$	$\mathbf{Y_2}^{n+1} = \mathbf{Y_3}$
$D_3 = Y_4$	$\mathbf{Y_3}^{n+1} = \mathbf{Y_4}$
$D_4 = \overline{Y_4 + Y_3 + Y_2}$	$Y_4^{n+1} = \overline{Y_4 + Y_3 + Y_2}$

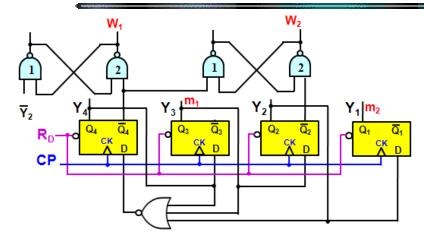
③ 状态转换表								
现态			次态				时钟	
$Y_4^n$	Y <sub>3</sub> n	Y <sub>2</sub> n	Y <sub>1</sub> n	Y <sub>4</sub> n+1	Y <sub>3</sub> n+1	$Y_2^{n+1}$	Y <sub>1</sub> n+1	СР
0	0	0	0	1	0	0	0	<b>↑</b>
1	0	0	0	0	1	0	0	<b>↑</b>
0	1	0	0	0	0	1	0	<b>↑</b>
0	0	1	0	0	0	0	1	<b> </b>
0	0	0	1	1	0	0	0	<b>↑</b>

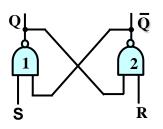
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状态图

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## 几种典型的时序逻辑部件——节拍发生器2





### 结论: 2-节拍发生器

- W<sub>1\_</sub>m<sub>1</sub>: 节拍电位\_节拍脉冲
- W<sub>2</sub>\_m<sub>2</sub>: 节拍电位\_节拍脉冲

## ⑤ 确定输出

R Y <sub>4</sub>	S <sub>Y<sub>2</sub></sub>	$Q_{n+1} \overline{Q}_{n+1}$ $(W_1 = \overline{Q})$		
1	1	Q <sub>n</sub>	$\overline{\mathbf{Q}}_{n}$	
0	1	0	1	
1	0	1	0	
0	0	_	_	

R	S	Q <sub>n+1</sub>	Q <sub>n+1</sub>	
$\overline{Y}_2$	$\overline{Y}_4$	$(W_2 = \overline{Q})$		
1	1	Q <sub>n</sub>	$\overline{\mathbf{Q}}_{n}$	
0	1	0	1	
1	0	1	0	
0	0	_	-	

