

# Fundamentals of Logic Design

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## 几种常用的复合逻辑运算

- > 与非,或非
- > 与或非
- > 异或
- > 同或

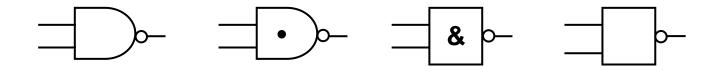




## 1. 与非 (NAND)

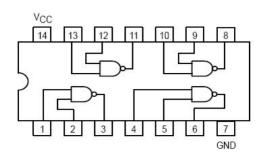
 $\mathbf{F} = \overline{\mathbf{A}} \mathbf{B}$ 

■逻辑符号





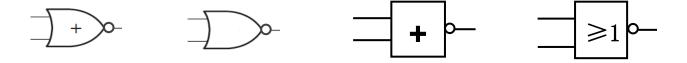




### 2. 或非 (NOR)

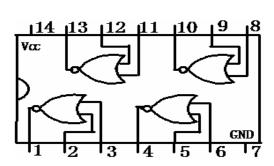
$$F = A + B$$

■逻辑符号



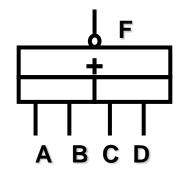


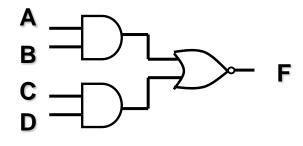




## 3. 与或非(AND-OR-NOT)

■逻辑符号

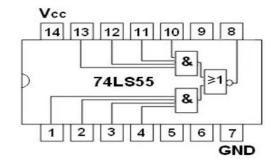




■ 典型芯片: 74LS51,74LS55

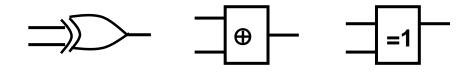






## 4. 异或

■逻辑符号

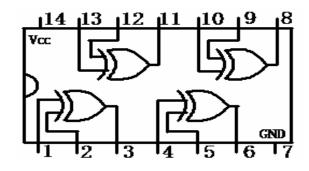


#### 真值表

A	В	F
0	0	0
0	1	1
1	0	1
1	1	0







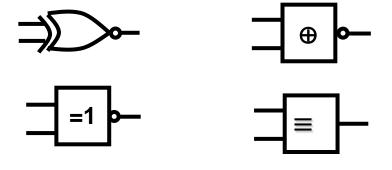
- 应用
- > 全加器 (Full adder)
- > 半加器 (Half-adder)





## 5. 同或

■逻辑符号

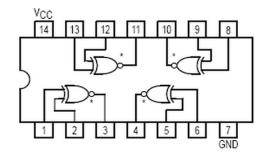


#### 真值表

A	В	F
0	0	1
0	1	0
1	0	0
1	1	1







- 应用
  - > 等值比较器





■ 异或及同或运算的性质

$$A \oplus 1 = \overline{A}$$
  $A \odot 1 = A$   
 $A \oplus 0 = A$   $A \odot 0 = \overline{A}$ 

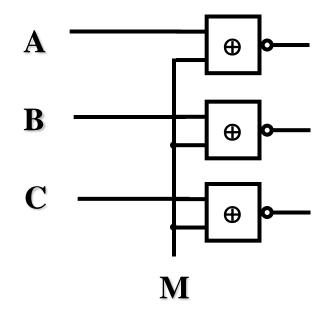
$$\mathbf{A} \oplus \mathbf{A} = \mathbf{0} \qquad \qquad \mathbf{A} \odot \mathbf{A} = \mathbf{1}$$

$$A \oplus \overline{A} = 1$$
  $A \odot \overline{A} = 0$ 





■ 应用







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