## Talos Vulnerability Report

TALOS-2020-1145

## Pixar OpenUSD SDF layer path remote code execution

NOVEMBER 12, 2020

CVE NUMBER

CVE-2020-13531

Summary

A use-after-free vulnerability exists in a way Pixar OpenUSD 20.08 processes reference paths textual USD files. A specially crafted file can trigger the reuse of a freed memory which can result in further memory corruption and arbitrary code execution. To trigger this vulnerability, the victim needs to open an attacker-provided malformed file.

Tested Versions

Pixar OpenUSD 20.08

Apple macOS Apple macOS Catalina 10.15.6

Product URLs

https://openusd.org

CVSSv3 Score

8.8 - CVSS:3.0/AV:N/AC:L/PR:N/UI:R/S:U/C:H/I:H/A:H

CWE

CWE-416 - Use After Free

Details

OpenUSD stands for Open Universal Scene Descriptor and is a software suite by Pixar that facilitates, among other things, interchange of arbitrary 3-D scenes that may be composed of many elemental assets.

Most notably, USD and its backing file format usd are used on Apple iOS and macOS as part of the ModellO framework supporting SceneKit and ARKit for sharing and displaying 3-D scenes in, for example, augmented reality applications. On macOS, these files are automatically rendered to generate thumbnails, while on iOS they can be shared via iMessage and opened with user interaction.

Along with a binary representation, USD 3-D models can come in a text based format for a more human readable representation. A key concept in creating USD models and scenes is layering. Different models are composed into a scene as layers. In short, this can be achieved through references or payloads.

While processing a list of references for a particular prim, the following code is executed as part of scene composition:

The function takes the current prim that is being processed as a node at [1] and then iterates through it's composition arcs (references or payloads) at [3]. Before reaching the for loop at [3], the SDF path of the current node is saved in srcPath at [2]. The vulnerability here lies in the fact that further node processint in the for loop can and will change the node and possibly it's actuall path. When this happens, srcPath becomes a stale reference to a now freed object which can further lead to memory corruption.

The following PoC triggers this vulnerability:

Above, we hit the breakpoint just after the for loop is entered and we can see the contents of srcPath and it's backing primPart->\_poolHandle and \_propPart->\_poolHandle which represent the buffers allocated for this object. Continuing the execution for one loop hits the breakpoint again:

```
Thread 1 "test" hit Breakpoint 3, pxrInternal_v0_20_pxrReserved_::_EvalRefOrPayloadArcs<pxrInternal_v0_20_pxrReserved_::SdfReference, (pxrInternal_v0_20_pxrReserved_::PcpArcType)4> (node-.., indexer=0x7fffffffc7a0, arcs=std::vector of length 2, capacity 2 = {...}, infoVec=std::vector of length 2,
```

Now, pool handles for \_primPart and propPart show erroneous values and indeed, comparing them to values of current node. GetPath() shows that srcPath is a stale reference. The underlying object has been freed. In order to actually cause memory corruption, this stale reference needs to be reused after it has been freed. This is the purpose of scale = 0 property of the /B reference. This comes into play with the following code:

The above code tries to validate scale and offset properties of the reference by calling IsValid and GetInverse().IsValid(). To be valid, scale and offset, and their inverse, need to be valid double values (not NaN or Infinity). Since scale is 0, it's inverse will be set to Infinity which is not considered valid and the body of the if statement it entered. In it, an error record is constructed which actually re-uses the stale srcPath causing memory corruption. Due to reference counting and complex object interactions, what seems like a simple read of a stale reference can in fact result in spurious heap writes and further memory corruption as the use of the stale reference continues. Given the expressiveness and recursive nature of USDA language, it is possible to achieve control over the freed memory which can ultimately result in arbitrary code execution.

```
AddressSanitizer:DEADLYSIGNAL
   ==439715==ERROR: AddressSanitizer: SEGV on unknown address 0x0000087c0008 (pc 0x7f42e95818ad bp 0x7ffcafa88000 sp 0x7ffcafa87cc0 T0) ==439715==The signal is caused by a WRITE memory access. #0 0x7f42e95818ac in pxrInternal_v0_20__pxrReserved__::Sdf_PoolpxrInternal_v0_20_pxrReserved__::Sdf_PoolpxrReserved__:Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrReserved__::Sdf_PoolpxrRese
  16384u>::GetPtr(unsigned int, unsigned int) ./pxr/usd/sdf/pool.h:231
#1 0x7f42e95818ac in pxrInternal_v0_20__pxrReserved_::Sdf_Pool<pxrInternal_v0_20__pxrReserved_::Sdf_PathPrimTag, 24u, 8u, 16384u>::Handle::GetPtr() const ./pxr/usd/sdf/pool.h:107
  #2 0x7f42e95818ac in
pxrInternal_v0_20__pxrReserved__::Sdf_PathNodeHandleImpl<pxrInternal_v0_20__pxrReserved__::Sdf_Pool<pxrInternal_v0_20__pxrReserved__::Sdf_Pool<pxrInternal_v0_20_pxrReserved__::Sdf_PathNode const>::get() const ./pxr/usd/sdf/path.h:148
  #3 0x7f42e95818ac in
pxrInternal_v0_20__pxrReserved__::Sdf_PathNodeHandleImpl<pxrInternal_v0_20__pxrReserved__::Sdf_Pool<pxrInternal_v0_20__pxrReserved__::Sdf_PathNode const ::_AddRef() const ./pxr/usd/sdf/path.h:189
  #5 0x7f42e95818ac in
pxrInternal_v0_20_pxrReserved_::Sdf_PathNodeHandleImpl<pxrInternal_v0_20_pxrReserved_::Sdf_Pool<pxrInternal_v0_20_pxrReserved_::Sdf_PathNode const>::operator=

(pxrInternal_v0_20_pxrReserved_::Sdf_PathNodeHandleImpl<pxrInternal_v0_20_pxrReserved_::Sdf_PathNode const>::operator=

(pxrInternal_v0_20_pxrReserved_::Sdf_PathNodeHandleImpl<pxrInternal_v0_20_pxrReserved_::Sdf_Pool<pxrInternal_v0_20_pxrReserved_::Sdf_PathNode const>::operator=

(pxrInternal_v0_20_pxrReserved_::Sdf_PathNode const> const6) ./pxr/usd/sdf/path.h:121

#6 0x7f42e7d98acc in pxrInternal_v0_20_pxrReserved_::SdfPath::operator=(pxrInternal_v0_20_pxrReserved_::SdfPath const6)
./pxr/usd/sdf/path.h:289

#7 0x7f42e7d98acc in _EvalRefOrPayloadArcs<pxrInternal_v0_20_pxrReserved_::SdfReference, (pxrInternal_v0_20_pxrReserved_::PcpArcType)4>
./pxr/usd/pcp/primIndex.cpp::B18

#8 0x7f42e7d840fa in _EvalNodeReferences ./pxr/usd/pcp/primIndex.cpp::466

#10 0x7f42e7daf87d in pxrInternal_v0_20_pxrReserved_::PcpComputePrimIndex(pxrInternal_v0_20_pxrReserved_::SdfPath const6,
pxrInternal_v0_20_pxrReserved_::PcpTinIndexInternal_v0_20_pxrReserved_::PcpPrimIndexOutputs*,
pxrInternal_v0_20_pxrReserved_::ArResolver*) ./pxr/usd/pcp/primIndex.cpp::430

#11 0x7f42e7b4d605 in
pxrInternal_v0_20_pxrReserved_::PcpCache::_ParallelIndexer::_ComputeIndex(pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*,
     #5 0x7f42e95818ac in
 #11 0x7442Pb46665 in

pxrInternal_v0_20_pxrReserved_::PcpCache::_ParallelIndexer::_ComputeIndex(pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*,
pxrInternal_v0_20_pxrReserved_::SdfPath, bool) ./pxr/usd/pcp/cache.cpp:1286

#12 0x7442Pb35b07 in void std::__invoke_impl<void, void (pxrInternal_v0_20_pxrReserved_::PcpCache::_ParallelIndexer::*6)

(pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*, pxrInternal_v0_20_pxrReserved_::SdfPath, bool),
pxrInternal_v0_20_pxrReserved_::PcpCache::_ParallelIndexer*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6,
pxrInternal_v0_20_pxrReserved_::SdfPath6, bool6>(std::_invoke_memfun_deref, void

(pxrInternal_v0_20_pxrReserved_::PcpCache::_ParallelIndexer::*6)(pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*,
pxrInternal_v0_20_pxrReserved_::SdfPath, bool), pxrInternal_v0_20_pxrReserved_::PcpCache::_ParallelIndexer*6,
pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6,
pxrInternal_v0_20_pxrReserved_::PcpP
pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::SdfPath6, bool6)
/usr/include/c+/9/bits/invoke.h:73
#13 @x7f4zPatb35b97 in std::_invoke_resultvoid (pxrInternal_v0_20_pxrReserved_::PcpCache::ParallelIndexer::*6)
(pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*, pxrInternal_v0_20_pxrReserved_::SdfPath, bool),
pxrInternal_v0_20_pxrReserved_::PcpCache::ParallelIndexer*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6,
pxrInternal_v0_20_pxrReserved_::SdfPath6, bool6>::type_std::_invokevoid (pxrInternal_v0_20_pxrReserved_::PcpCache::ParallelIndexer:**6)(pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6,
pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6,
pxrInternal_v0_20_pxrReserved_::PcpFrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6,
pxrInternal_v0_20_pxrReserved_::PcpCache::ParallelIndexer:**8)(pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6,
pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6,
pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6,
pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6,
pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*6, pxrInternal_v0_20_pxr
  /usr/include/c+*/9/Dits/invoke.n:ys
#14 0xf42e7b35b07 in void std::Bind<void (pxrInternal_v0_20_pxrReserved_::PcpCache::ParallelIndexer::*
(pxrInternal_v0_20_pxrReserved_::PcpCache::ParallelIndexer*, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*,
pxrInternal_v0_20_pxrReserved_::SdfPath, bool))(pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*,
pxrInternal_v0_20_pxrReserved_::SdfPath, bool)>::_call<void, 0ul, 1ul, 2ul, 3ul>(std::tuple<>&6, std::_Index_tuple<0ul, 1ul, 2ul, 3ul>)
/usr/include/c-*/9/functional:400
 /usr/include/c++/9/functional:400
#15 0x7f42e7b35b07 in void std:: Bind<void (pxrInternal_v0_20_pxrReserved_::PcpCache:: ParallelIndexer::*
(pxrInternal_v0_20_pxrReserved_::PcpCache:: ParallelIndexer*, pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*,
pxrInternal_v0_20_pxrReserved_::SdfPath, bool))(pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*,
pxrInternal_v0_20_pxrReserved_::SdfPath, bool))::operator()<, void() / usr/include/c++/9/functional:484
#16 0x7f42e7b35b07 in pxrInternal_v0_20_pxrReserved_::PcpCache:: ParallelIndexer::*(pxrInternal_v0_20_pxrReserved_::PcpCache:: ParallelIndexer::*(pxrInternal_v0_20_pxrReserved_::PcpCache:: ParallelIndexer:*(pxrInternal_v0_20_pxrReserved_::PcpCache:: ParallelIndexer*,
pxrInternal_v0_20_pxrReserved_::PcpPrimIndex const*, pxrInternal_v0_20_pxrReserved_::SdfPath, bool))
//pxr/base/work/dispatcher.h:145
#17 0x7f42ef5f5feat in tbb::internal::custom scheduler:thh::internal::IntelScheduler:Traits::local_wait_for_all(thb::tacke_thb).
 ./pxr/base/work/dispatcher.h:145
#17 0x7f42e5f5fea4 in tbb::internal::custom_scheduler<tbb::internal::IntelSchedulerTraits>::local_wait_for_all(tbb::task6, tbb::task*)
.././src/tbb/custom_scheduler.h:501
#18 0x7f42e5fceb70 in tbb::task::wait_for_all() ./build/include/tbb/task.h:760
#19 0x7f42e5fceb70 in pxrInternal_v020__pxrReserved__::WorkDispatcher::Wait() ./pxr/base/work/dispatcher.cpp:52
#20 0x7f42e5fcaaf0 in void std::__invoke_inpl<void, void (pxrInternal_v0_20__pxrReserved__::WorkDispatcher* const6)(),
pxrInternal_v0_20__pxrReserved__::WorkDispatcher* const6)(),
pxrInternal_v0_20__pxrReserved__::WorkDispatcher*:* const6)(),
pxrInternal_v0_20__pxrReserved__::WorkDispatcher*:* const6)(),
pxrInternal_v0_20__pxrReserved__::WorkDispatcher*:* const6)(),
pxrInternal_v0_20__pxrReserved__::WorkDispatcher*:* const6)(),
pxrInternal_v0_20__pxrReserved__::WorkDispatcher*:* const6)(),
  /wsr/include/c++/9/bits/invoke.h:73
#21 0x7f42e5fcaaf0 in std::_invoke_result<void (pxrInternal_v0_20_pxrReserved_::WorkDispatcher::* const6)(),
pxrInternal_v0_20_pxrReserved_::WorkDispatcher* const6>::type std::_invokevoid (pxrInternal_v0_20_pxrReserved_::WorkDispatcher::* const6)(),
pxrInternal_v0_20_pxrReserved_::WorkDispatcher* const6>(void (pxrInternal_v0_20_pxrReserved_::WorkDispatcher::* const6)(),
pxrInternal_v0_20_pxrReserved_::WorkDispatcher* const6) / usr/include/c++/9/bits/invoke.h:95
#22 0x7f42e5fcaaf0 in void std::Bindvoid (pxrInternal_v0_20_pxrReserved_::WorkDispatcher::*
  (pxrInternal_v0_20__pxrReserved__::WorkDispatcher*))()>::__call_c<void, , @ul>(std::tuple<>66, std::_Index_tuple<@ul>) const /usr/include/c++/9/functional:410
#23 0x7f42e5fcaaf0 in void std::_Bind<void (pxrInternal_v0_20__pxrReserved__::WorkDispatcher::*
  (pxrInternal_v0_20_pxrReserved_::WorkDispatcher*))()::operator()<, void>() const /usr/include/c++/9/functional:495
#24 0x7f42e5fcaaf0 in tbb::interface7::internal::delegated_function<std::Bindvoid (pxrInternal_v0_20_pxrReserved_::WorkDispatcher*))()> consts::operator() const ./build/include/tbb/task_arena.h:62
#25 0x7f42e5f5aeac in tbb::interface7::internal::task_arena_base::internal_execute(tbb::interface7::internal::delegate_base6) const
   ../../src/tbb/arena.cpp:811
#26 0x7f42e5fc7b9a in void tbb::interface7::task_arena::execute<std::_Bind<void (pxrInternal_v0_20__pxrReserved__::WorkDispatcher::*
  #20 8X7f42e7bla4e8 in pxrInternal_v0_20_pxrReserved_::Pcp:1216

#20 8X7f42e7bla4e8 in pxrInternal_v0_20_pxrReserved_::WorkDispatcher*))()> const8)

#20 8X7f42e7bla4e8 in pxrInternal_v0_20_pxrReserved_::WorkDispatcher*)()> const8)

#20 8X7f42e7bla4e8 in pxrInternal_v0_20_pxrReserved_::PcpCache::ParallelIndexer::RunAndWait() ./pxr/usd/pcp/cache.cpp:1216
   #29 0x7f42e7b1a4e8 in
 #29 8x7f42e7bla4e8 in
pxrInternal_v0_20_pxrReserved_::PcpCache::ComputePrimIndexesInParallel(std::vector<pxrInternal_v0_20_pxrReserved_::SdfPath,
std::allocator<pxrInternal_v0_20_pxrReserved_::SdfPath> > const6,
std::vector<std::shared_ptrcpxrInternal_v0_20_pxrReserved_::PcpErrorBase>,
std::allocator<std::shared_ptrcpxrInternal_v0_20_pxrReserved_::PcpErrorBase> > **,
pxrInternal_v0_20_pxrReserved_::PcpCache::UntypedIndexingRayloadPredicate,
pxrInternal_v0_20_pxrReserved_::PcpCache::UntypedIndexingPayloadPredicate, char const*, char const*)./pxr/usd/pcp/cache.cpp:1425
#30 0xf42ea08d8f1 in ComputePrimIndexesInParalleltypxrInternal_v0_20_pxrReserved_::Canonymous namespace)::_NameChildrenPred,
pxrInternal_v0_20_pxrReserved_::UsdStage::IncludePayloadsPredicate>./pxr/usd/pcp/cache.h:340
#31 0xf42ea08d8f1 in
pxrInternal_v0_20_pxrReserved_::UsdStage::ComposePrimIndexesInParallel(std::yector<pxrInternal_v0_20_pxrReserved_::SdfPath.
  pxrInternal_v0_20_pxrReserved_::UsdStage::_ComposePrimIndexesInParallel(std::vector<pxrInternal_v0_20_pxrReserved_::SdfPath, std::allocator<pxrInternal_v0_20_pxrReserved_::SdfPath> > const6, std::_cxx11::basic_string<char, std::char_traits<char>, std::allocator<char> > const6, pxrInternal_v0_20_pxrReserved_::Usd_InstanceChanges*) ./pxr/usd/usd/stage.cpp:4262
   #32 0x7f42ea752d9c in
  pxrInternal_v0_20_pxrReserved_::UsdStage::_InstantiateStage(pxrInternal_v0_20_pxrReserved_::TfRefPtr<pxrInternal_v0_20_pxrReserved_::SdfLayer> const6, pxrInternal_v0_20_pxrReserved_::TfRefPtr<pxrInternal_v0_20_pxrReserved_::SdfLayer> const6,
```

```
pxrInternal_v0_20_pxrReserved_::\text{ArResolverContext const6}, pxrInternal_v0_20_pxrReserved_::\text{UsdStage::InitialLoadSet}) ./pxr/usd/usd/stage.cpp:649
#33 0x7f42ea8f36d in pxrInternal_v0_20_pxrReserved_::\text{UsdStage::InitialLoadSet}) ./pxr/usd/usd/stage.cpp:649
#34 0x7f42ea8f36d in pxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendImplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage:} pxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendImplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage:} pxrInternal_v0_20_pxrReserved_::\text{UsdStage::pintialLoadSet}, pxrInternal_v0_20_pxrReserved_::\text{UsdStage::pintialLoadSet}, pxrInternal_v0_20_pxrReserved_::\text{UsdStage::pintialLoadSet}, pxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendimplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendimplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendimplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendimplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendimplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendimplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendimplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendimplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendimplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage::pendimplcpxrInternal_v0_20_pxrReserved_::\text{UsdStage::InitialLoadSet})./pxr/usd/usd/stage.cpp:854}
#37 0x5629ff68366 in pxrInternal_v0_20_pxrReserved_::\text{UsdStage::InitialLoadSet})./pxr/usd/usd/stage.cpp:854}
#38 0x5629ff68366 in main ./build/test.cpp:21
#39 0x7f42ea8564 in pxrInternal_v0_20_pxrReserved_::\text{UsdStage::InitialLoadSet})./pxr/usd/usd/stage.cpp:854}
#38 0x5629ff68366 in main ./build/test.cpp:21
#39 0x7f42ea85636 in main ./build/test.cpp:21
#39 0x7f42ea85636 in main ./build/test.opp:21
#39 0x7f42ea85636 in main ./build/test.opp
```

Timeline

2020-09-01 - Vendor Disclosure 2020-11-12 - Public Release

CREDIT

Discovered by Aleksandar Nikolic of Cisco Talos.

VULNERABILITY REPORTS PREVIOUS REPORT NEXT REPORT

TALOS-2020-1125 TALOS-2020-1120