Tugas 3
Organisasi dan Arsitektur Komputer
"Interruption Request Priority"



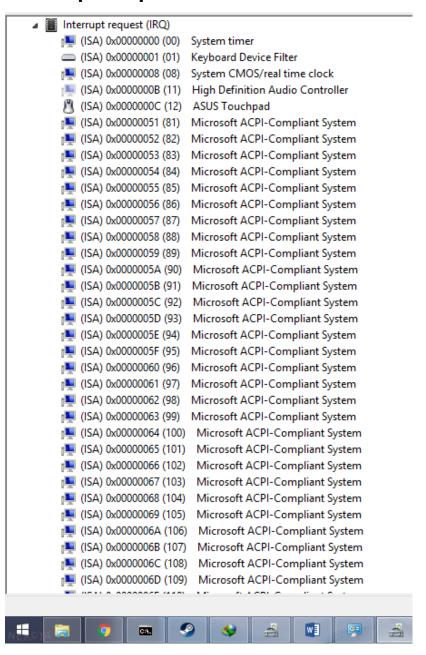
NAMA : Ragil Burhanudin Pamungkas

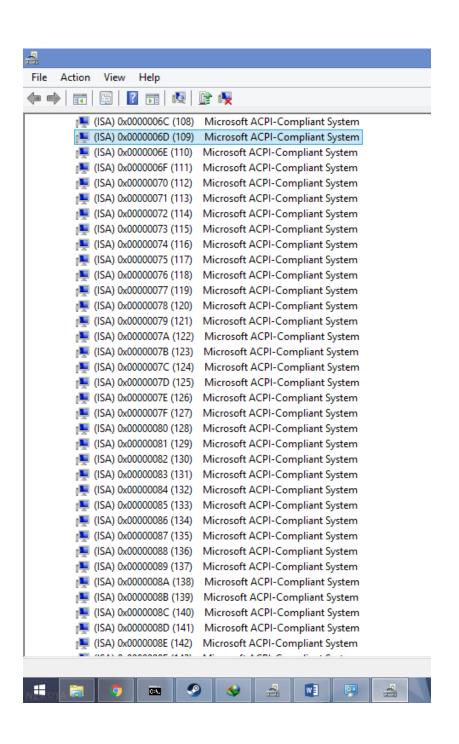
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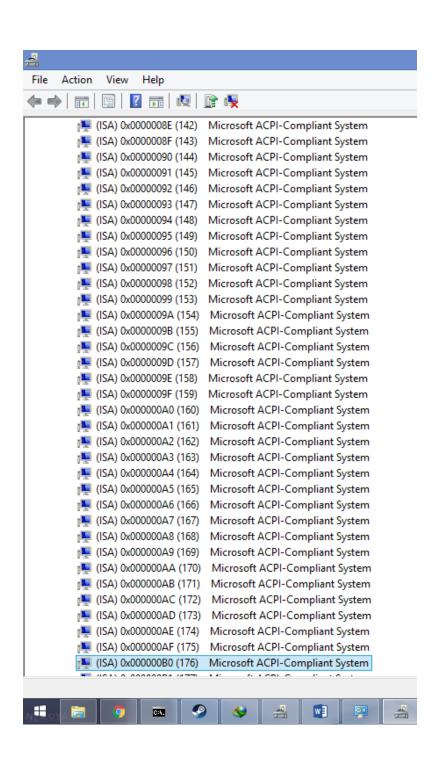
KELAS : A

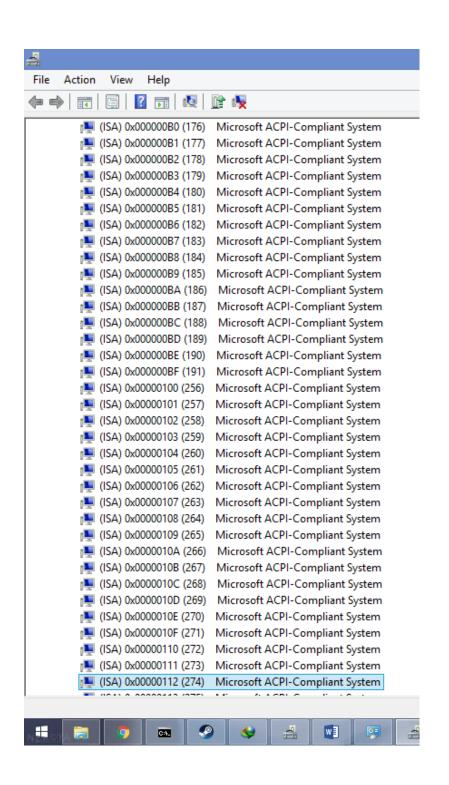
Program Studi Informatika
Fakultas Komunikasi dan Informatika
Universitas Muhammadiyah Surakarta

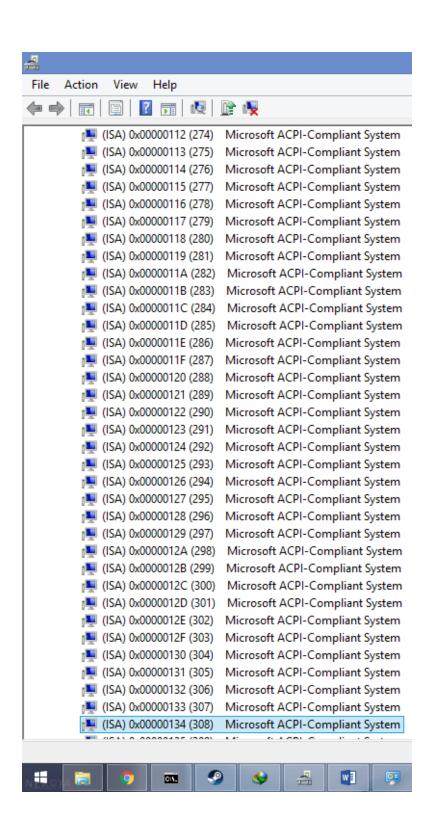
Interrupt Request

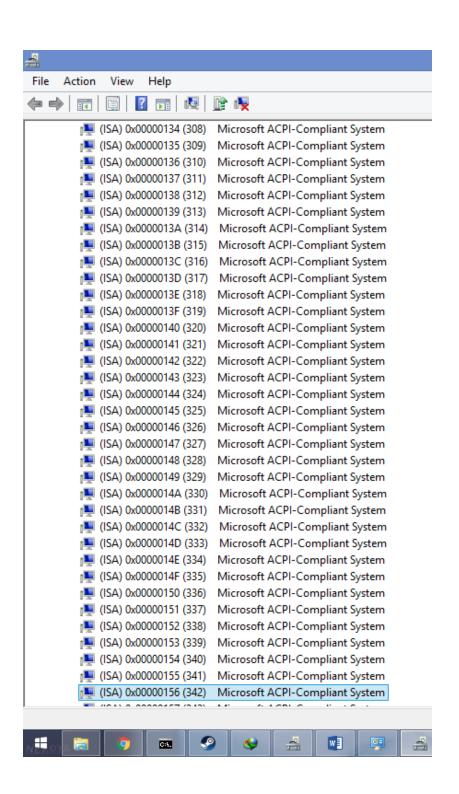


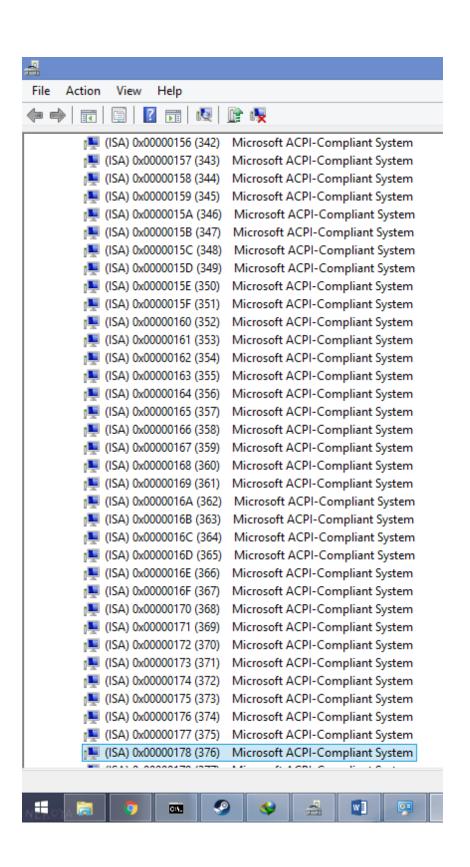


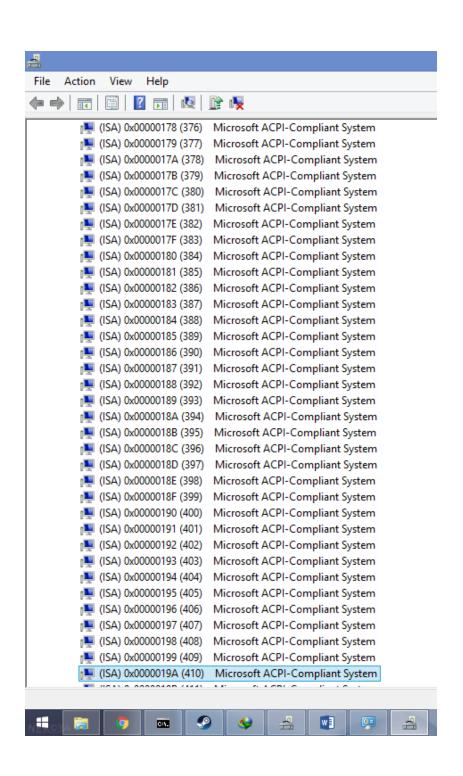


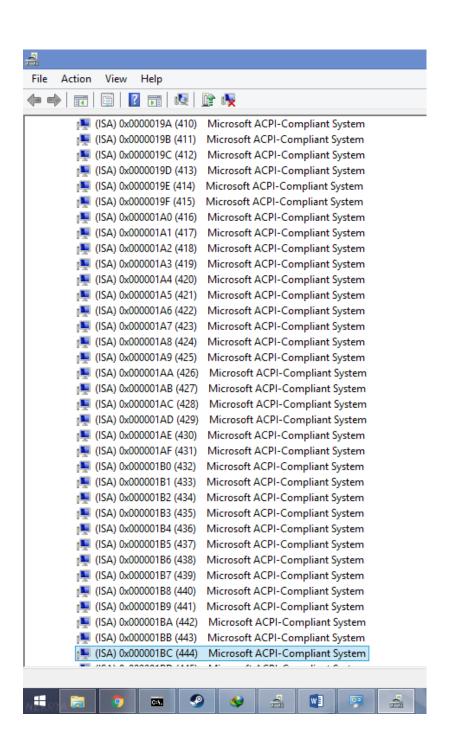


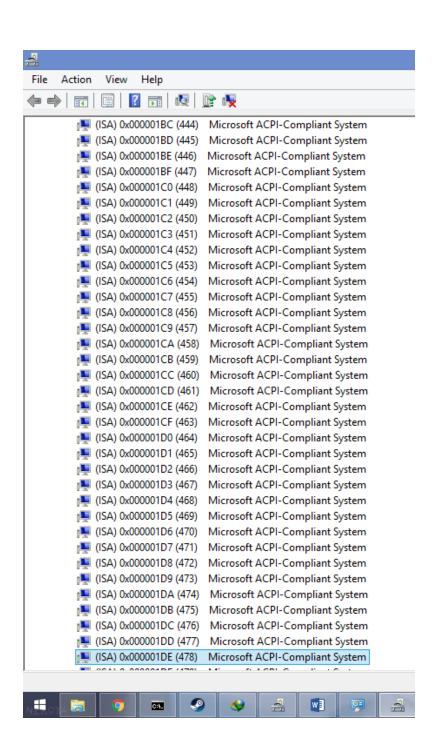




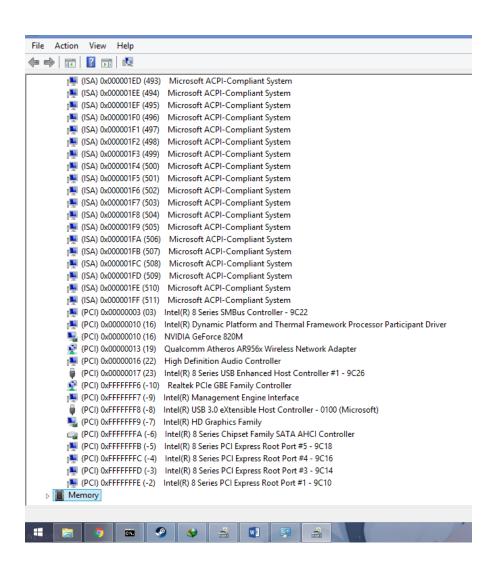




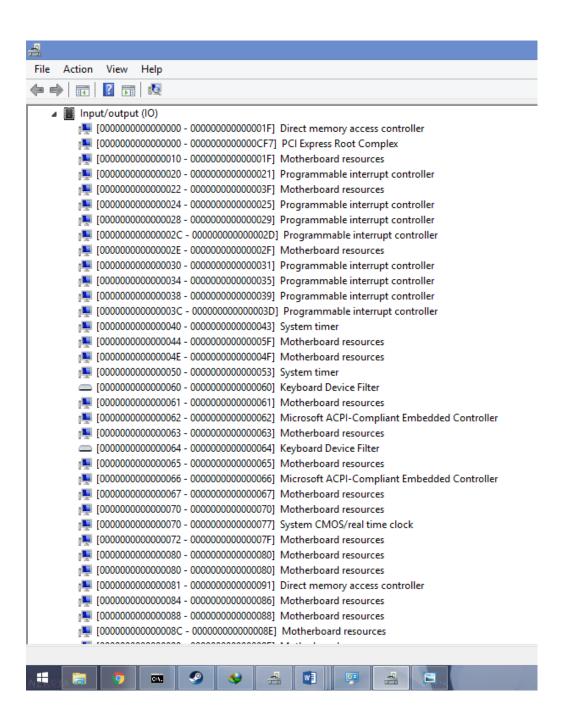


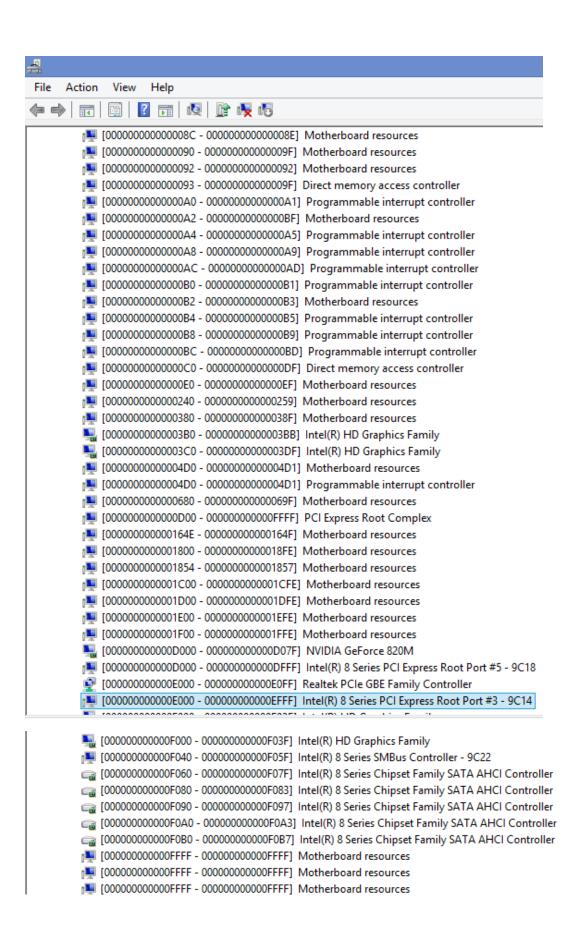






Input Output





Memory

