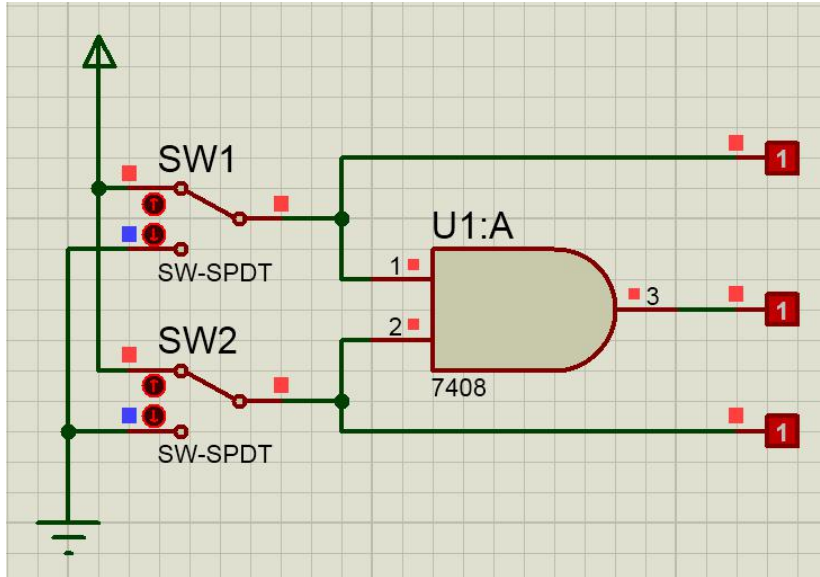
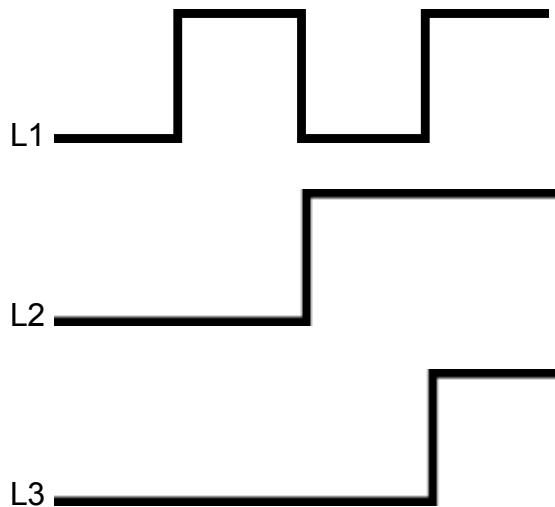


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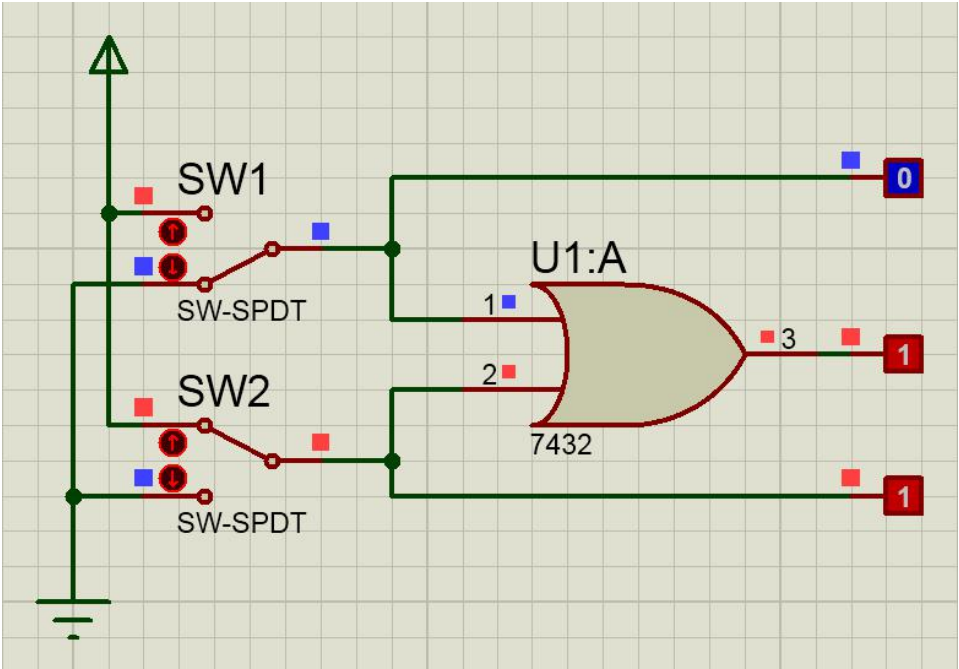
### Percobaan 1. AND



SW 1	SW 2	L1	L2	L3
0	0	0	0	0
1	0	1	0	0
0	1	0	1	0
1	1	1	1	1



Percobaan 2. OR



SW 1	SW 2	L1	L2	L3
0	0	0	0	0
1	0	1	0	1
0	1	0	1	1
1	1	1	1	1

L1



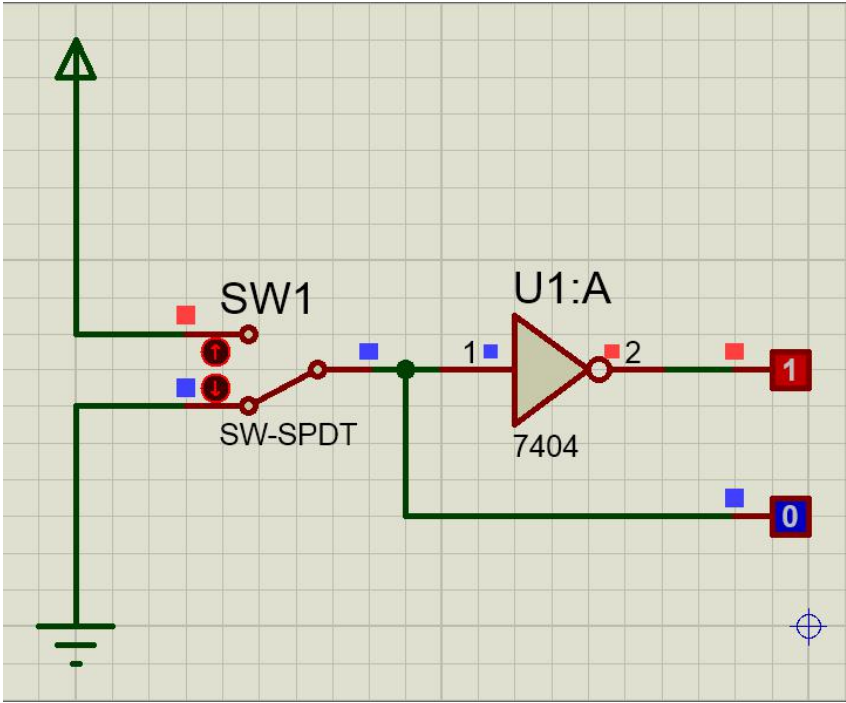
L2



L3



Percobaan 3. NOT



SW 1	L2	L1
0	0	1
1	1	0

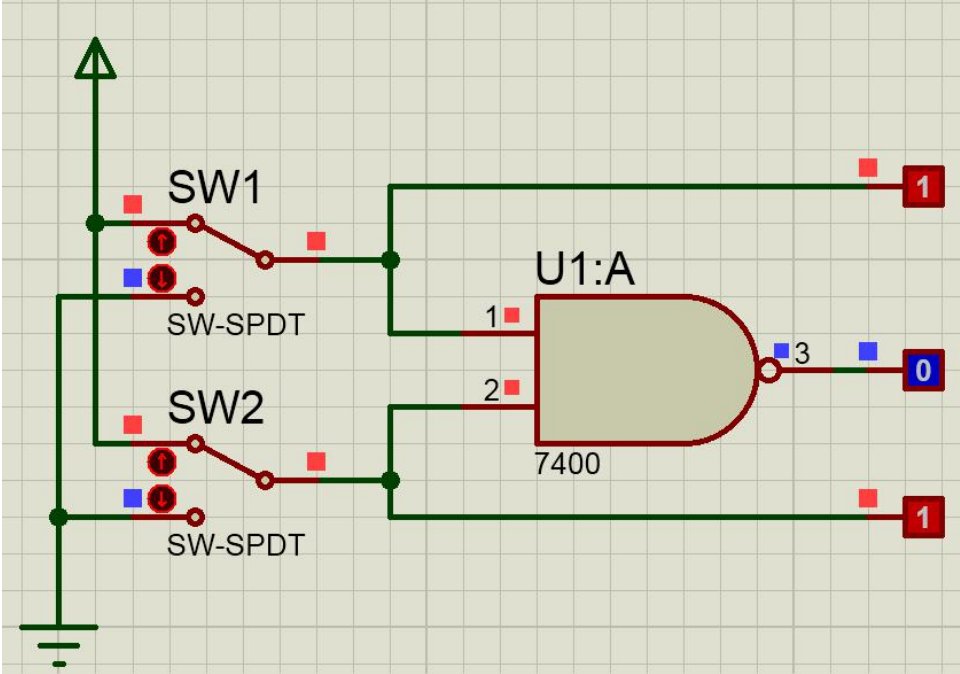
L2



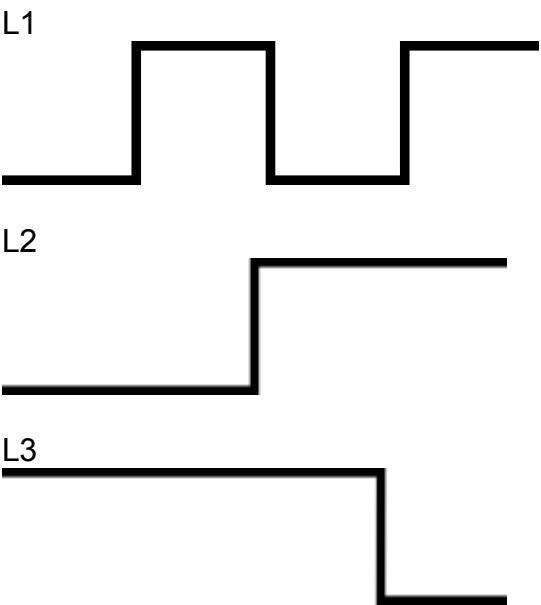
L1



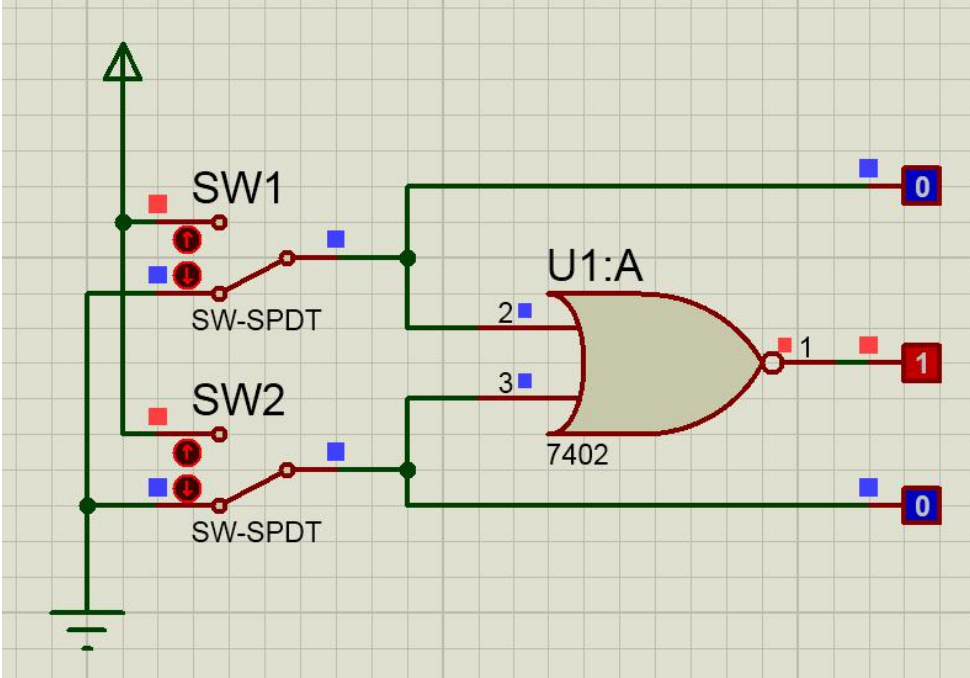
Percobaan 4. NAND



SW 1	SW 2	L1	L2	L3
0	0	0	0	1
1	0	1	0	1
0	1	0	1	1
1	1	1	1	0



Percobaan 5. NOR



SW 1	SW 2	L1	L2	L3
0	0	0	0	1
1	0	1	0	0
0	1	0	1	0
1	1	1	1	0

L1



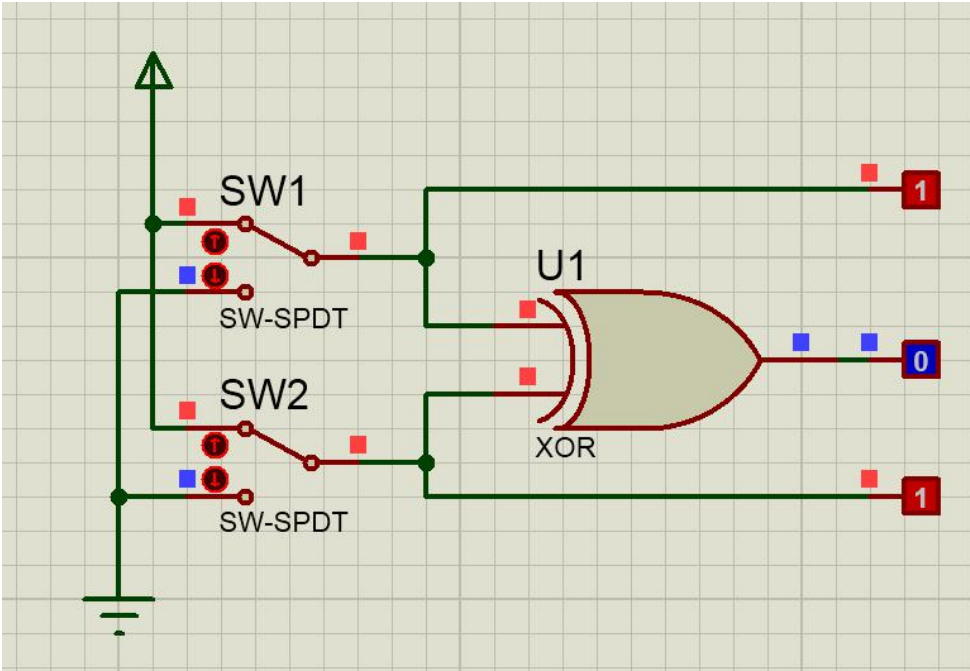
L2



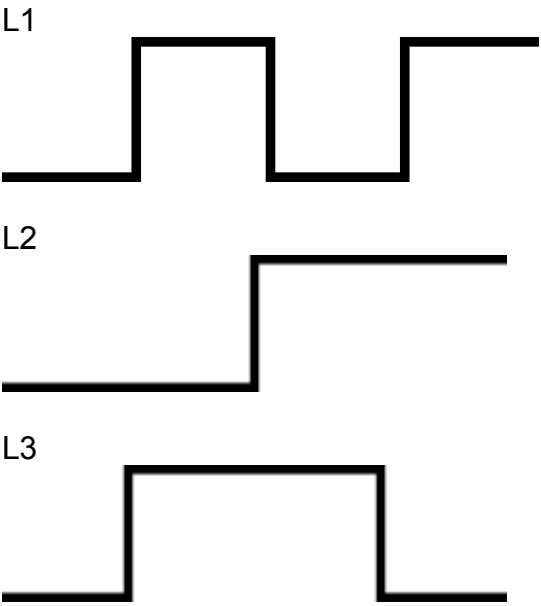
L3



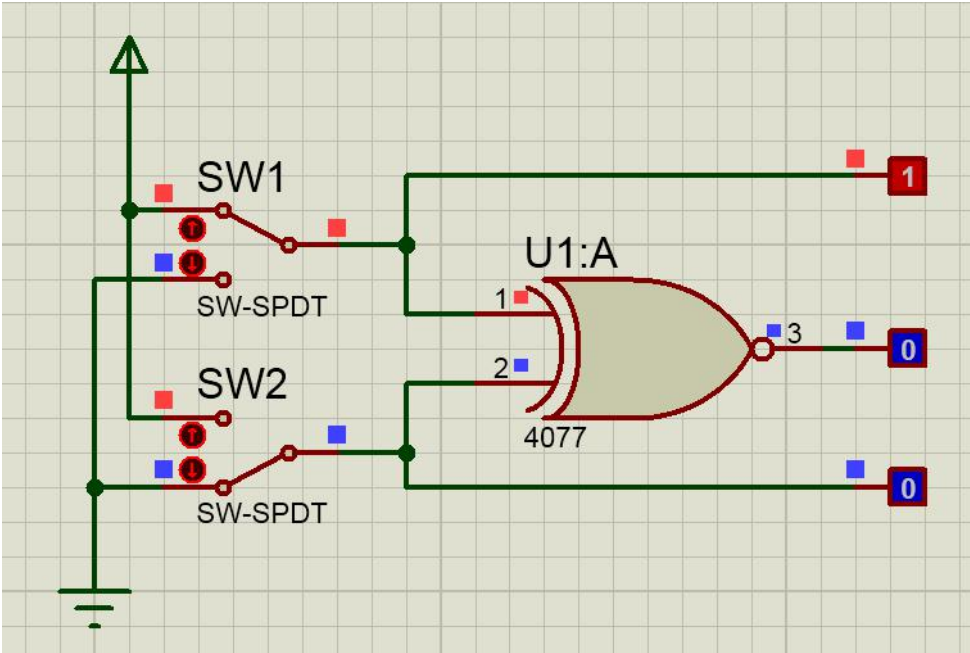
Percobaan 6. XOR



SW 1	SW 2	L1	L2	L3
0	0	0	0	0
1	0	1	0	1
0	1	0	1	1
1	1	1	1	0



Percobaan 7. XNOR



SW 1	SW 2	L1	L2	L3
0	0	0	0	1
1	0	1	0	0
0	1	0	1	0
1	1	1	1	1

L1



L2



L3

