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LAPORAN SISTEM DIGITAL

Modul 4

Percobaan 1

- Tabel kebenaran

SW1	L2	L1
0	0	1
1	1	0

- Diagram waktu

L2	1		
	0		
L1	1		
	0		

Percobaan 2

- Tabel kebenaran

SW1	SW2	L1	L2	L3
0	0	0	0	0
1	0	1	0	1
0	1	0	1	1
1	1	1	1	1

- Diagram waktu

L1	1				
	0				
L2	1				
	0				
L3	1				
	0				

Percobaan 3

- Tabel kebenaran

SW1	SW2	L1	L2	L3
0	0	0	0	0
1	0	1	0	0
0	1	0	1	0

1	1	1	1	1
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- Diagram waktu

L1	1				
	0				
L2	1				
	0				
L3	1				
	0				

Percobaan 4

- Tabel kebenaran

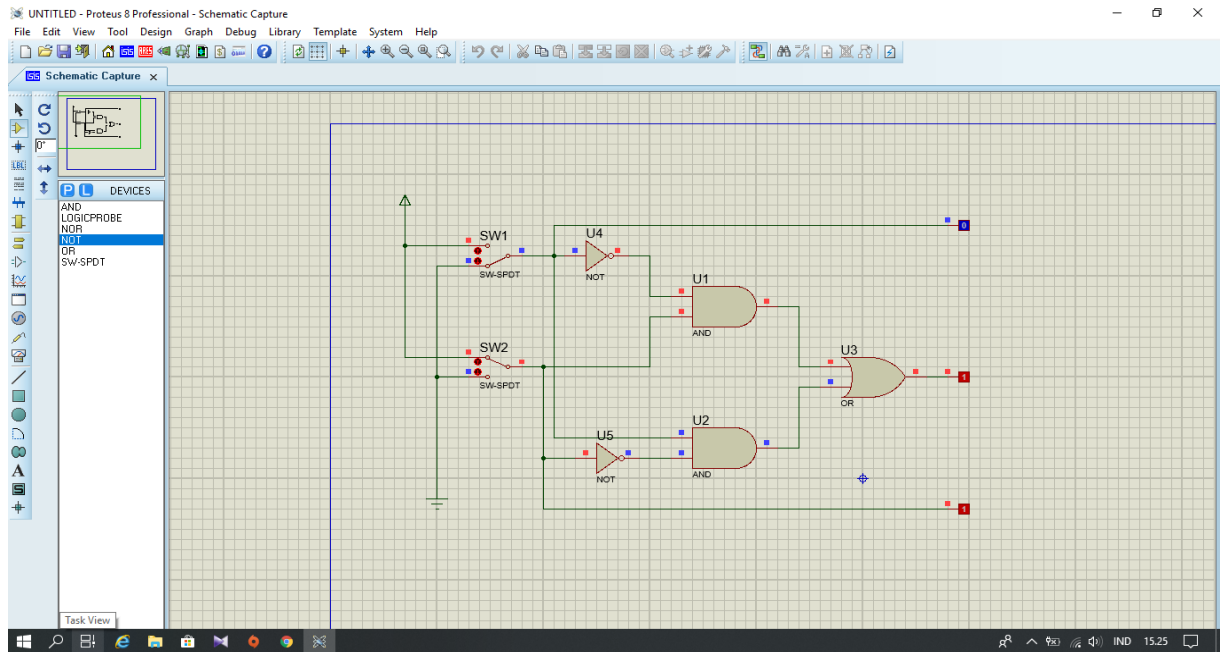
SW1	SW2	L1	L2	L3
0	0	0	0	1
1	0	1	0	0
0	1	0	1	0
1	1	1	1	1

- Diagram waktu

L1	1				
	0				
L2	1				
	0				
L3	1				
	0				

Percobaan 5

- Gambar kombinasi gerbang logika



- Tabel kebenaran

SW1	SW2	L1	L2	L3
0	0	0	0	0
1	0	1	0	1
0	1	0	1	1
1	1	1	1	0

- Diagram waktu

L1	1				
	0				
L2	1				
	0				
L3	1				
	0				