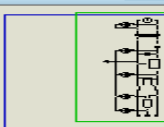


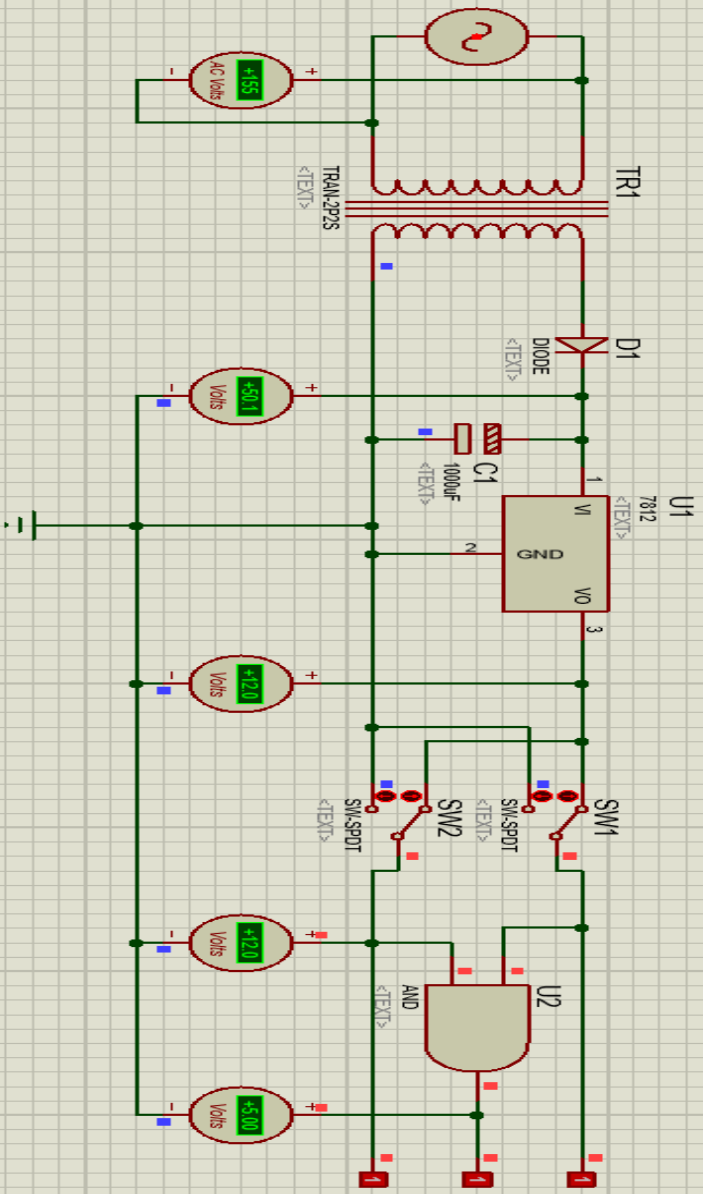


\* Schematic Capture X



DEVICES

- 7805
- 7812
- ALTERNATOR
- AND
- CAP-ELEC
- DIODE
- LOGICPROBE
- SW-SPDT
- TRAN-2P2S



2 Message(s)

Base Design

ANIMATING: 00:00:03.550001 (CPU load 5%)

+360.0

+300.0 th