

DIGITAL SYSTEMS LABORATORY WORK

MODUL 12 : IMPLEMENTASI



By :

NUR ANNIDA I'FFAH SUPARDI

L200183147

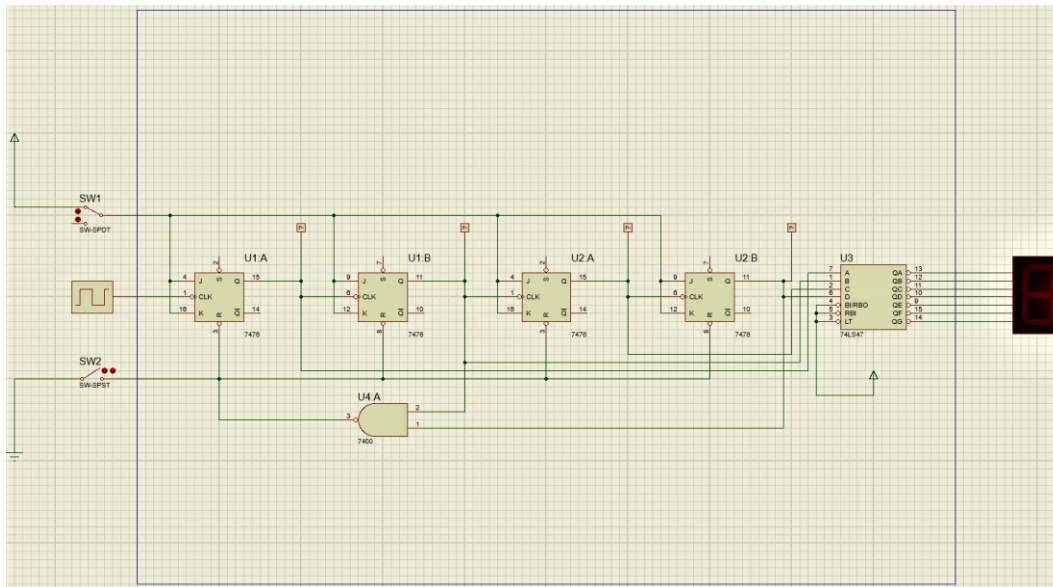
INFORMATION TECHNOLOGY

FACULTY OF COMMUNICATION AND INFORMATICS

UNIVERSITY OF MUHAMMADIYAH SURAKARTA

NIM : L200183147
Name : Nur Annida I'ffah Supardi
Assistant Name : Salsa Sasmita Mukti
Date of Practicum : Friday, June 28 st 2019

Experiment 1

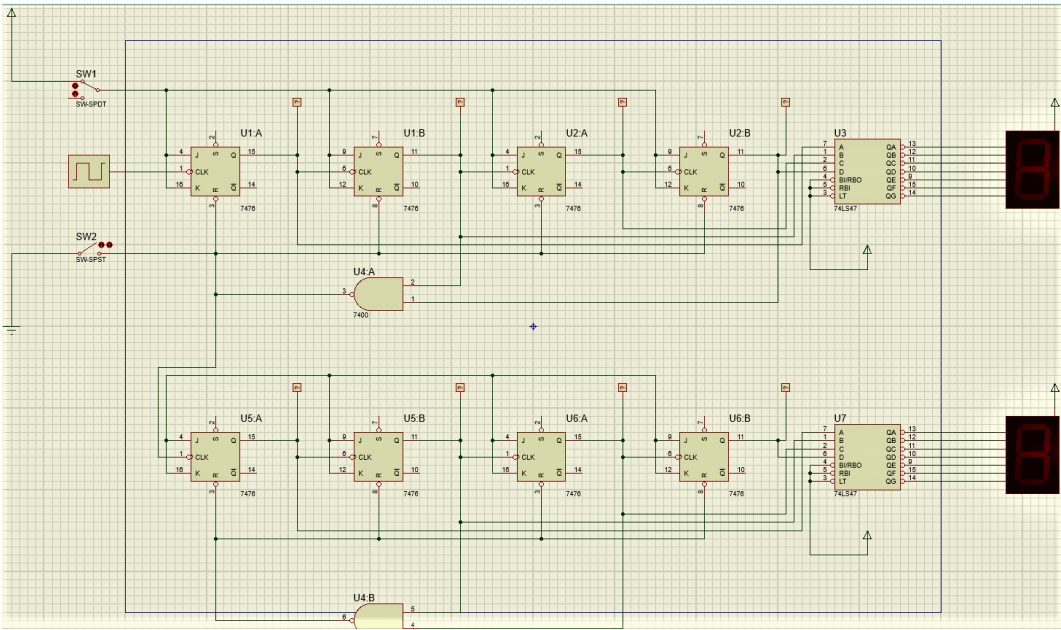


Picture 1.1. Modulus 10 and Decoder circuit

1. What is the output seen in the seven segment?

Answer: The seven segment output is in the form of calculations from 1 to 9.

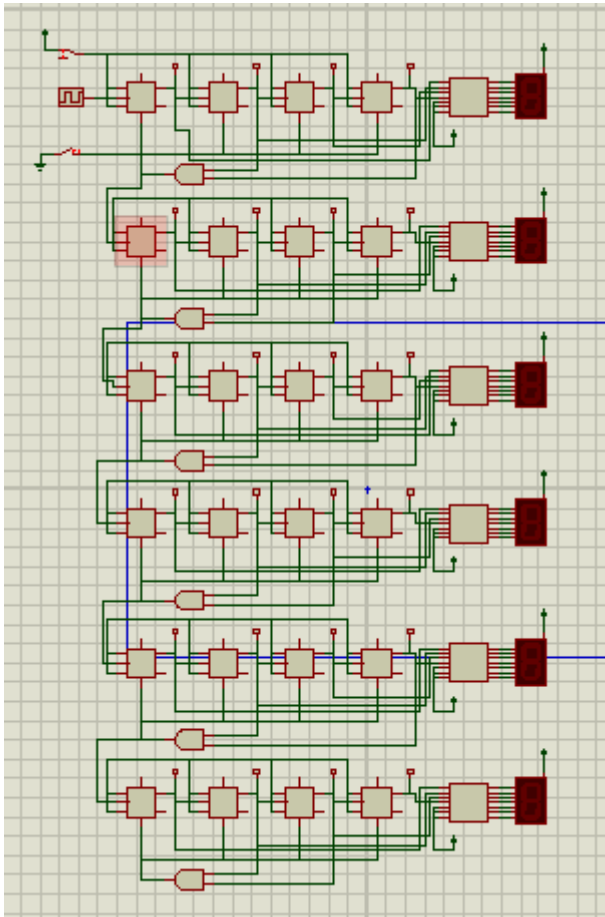
Experiment 2



Picture 2.1. Modulus 6 and Decoder circuit

1. What is the clock function in the series above?

Answer: To determine the speed of calculation.



Picture 12.3. Calculation of cycle time and minute cycle

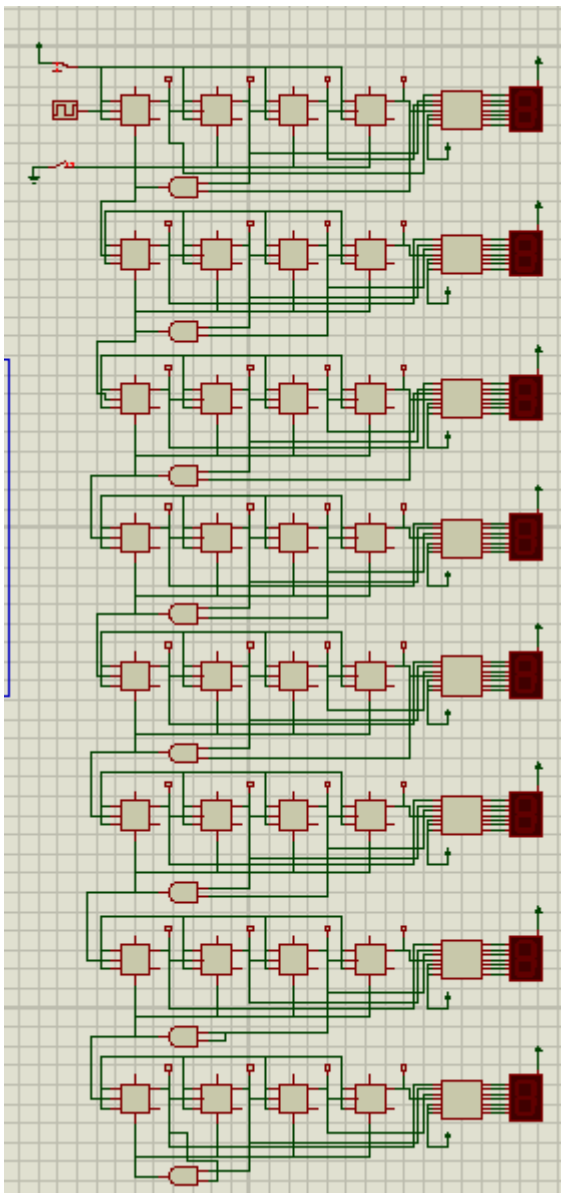
2. Explain the usefulness of the SPDT Switch in the series above!

Answer: To check the value of the decoder.

3. Explain the usefulness of the SPST Switch in the series above!

Answer: To stop counting.

Additional Assignment



Picture 12.5. Digital clock counter