DIGITAL SYSTEMS PRACTICUM 10



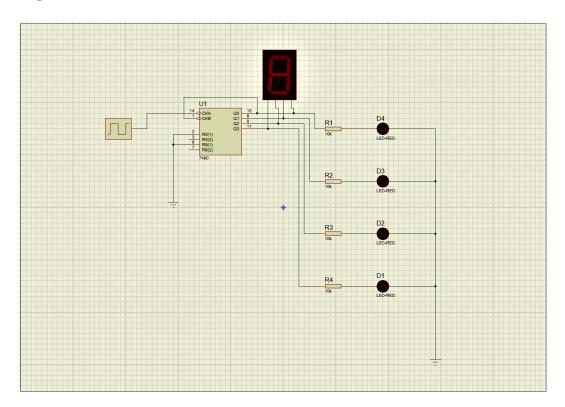
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Experiment 1

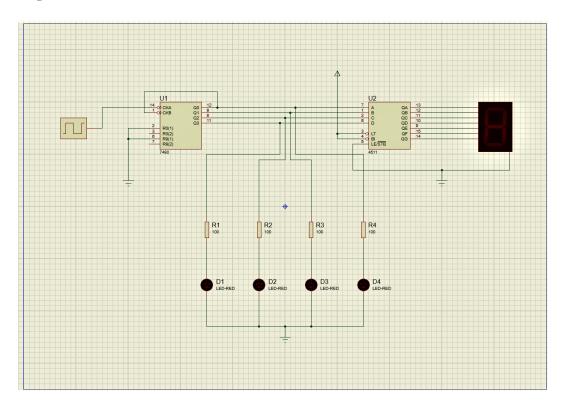


Picture 1.1. Set of clock counter

1. Column table

Input		Output			
Clock	D1	D2	D3	D4	Seven Segment
1	0	0	0	0	0
2	1	0	0	0	1
3	0	1	0	0	2
4	1	1	0	0	3
5	0	0	1	0	4
6	1	0	1	0	5
7	0	1	1	0	6
8	1	1	1	0	7
9	0	0	0	1	8
10	1	0	0	1	9

Experiment 2



Picture 2.1. Addition of a BCD-to-segment decoder

1. Column table

Input		Output			
Clock	D1	D2	D3	D4	Seven Segment
1	0	0	0	0	0
2	1	0	0	0	1
3	0	1	0	0	2
4	1	1	0	0	3
5	0	0	1	0	4
6	1	0	1	0	5
7	0	1	1	0	6
8	1	1	1	0	7
9	0	0	0	1	8
10	1	0	0	1	9

2. Comparison of experiment 1 and experiment 2

In experiment 2, number 6 looks like the letter b and the number 9 looks like the letter q on the 7 segment. but in experiment 1, everything looks normal.

3. Is it true that 7seg-BCD is the same as the BCD-to-7 segment decoder?

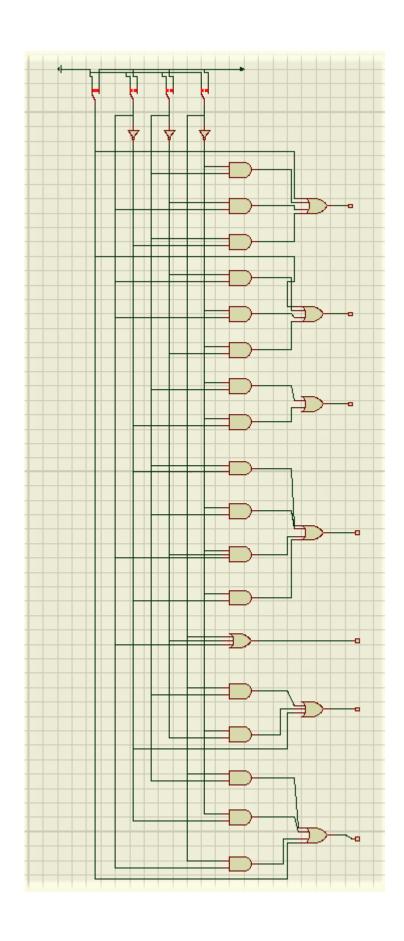
Answer: Yes

Experiment 3

1. Table function of IC 4511

	Input					Output						Displa	
Decim al Digit	L T	D	C	В	A	a	b	c	d	e	f	æ	y Outpu t
0	Н	L	L	L	L	Н	Н	Н	Н	Н	Н	L	0
1	Н	L	L	L	Н	L	Н	Н	L	L	L	L	1
2	Н	L	L	Н	L	Н	Н	L	Н	Н	L	Н	2
3	Н	L	L	Н	Н	Н	Н	Н	Н	L	L	Н	3
4	Н	L	Н	L	L	L	Н	Н	L	L	Н	Н	4
5	Н	L	Н	L	Н	Н	L	Н	Н	L	Н	Н	5
6	Н	L	Н	Н	L	L	L	Н	Н	Н	Н	Н	6
7	Н	L	Н	Н	Н	Н	Н	Н	L	L	L	L	7
8	Н	Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	8
9	Н	Н	L	L	Н	Н	Н	Н	L	L	Н	Н	9
LT	L	X	X	X	X	Н	Н	Н	Н	Н	Н	Н	8

- 2. The output "a" (highlight) in the table shows that LED works in seven common cathode segments
- 3. Each output shows the state of LED from seven segment various conditions
- 4. Each LED is controlled by a combination of logic gates.



Picture 3.1. Complete diagram logic from BCD-to-7segment decoder

5. Comparison truth table with set of BCD-to-7segment

The output results in the BCD-to-7-segment decoder circuit produce a value that exactly matches the truth table