DIGITAL SYSTEMS LABORATORY WORK

MODUL 4 : COMBINATION OF LOGIC GATE

ADDITIONAL ASSIGNMENT
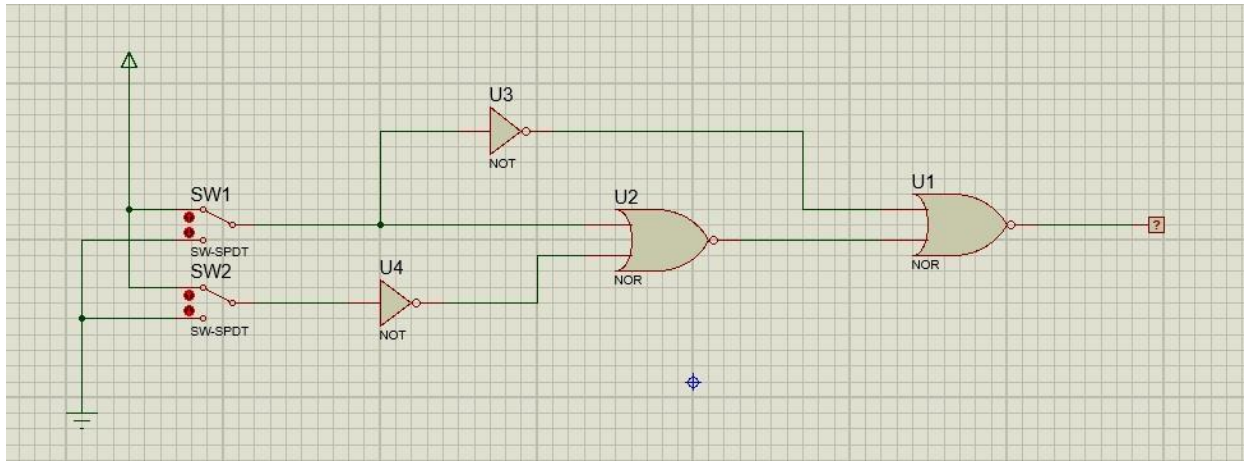

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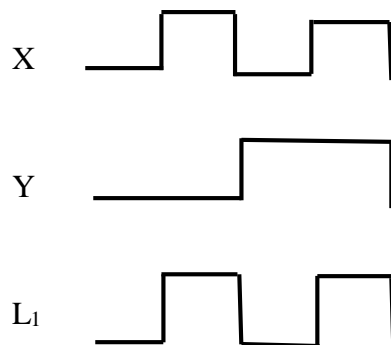Date of Practicum   : Friday, March 22 st 2019

1. Create a logic gate combination as show in figure below !

   a. The truth table

X	Y	L₁
0	0	0
1	0	1
0	1	0
1	1	1

   b. Time diagram

c. Boolean function :

$$\overline{\overline{\overline{X} + \overline{Y}}) + \overline{A}}$$

2. Create the logic gate circuit based on the following Boolean function !

$$F = (A . \overline{B} + C) + (\overline{D} . E)$$