DIGITAL SYSTEM

PRACTICUM REPORT 12: IMPLEMENTATION



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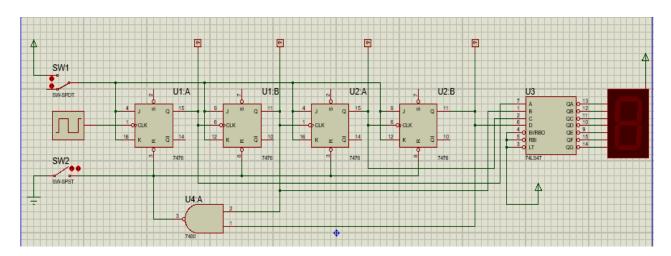
NAME : HAFSHAH FITRI AFIFAH

CLASS : X

ASSISTANT : SALSA SASMITA MUKTI

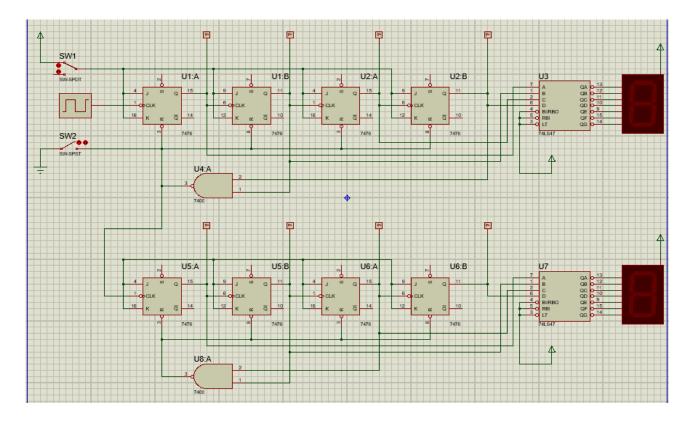
DATE OF PRACTICUM : Friday, June 28th 2019

#Exercise 1 (Circuit of Modulus 10 and Decoder)



Output in seven segmen : forming a series of 0 until 9 digits that appear alternately

#Exercise 2



• The function of clock

To set the speed of appearance of digits on the 7 segment

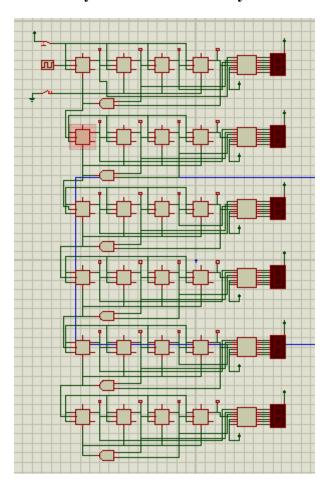
• The function of SW-SPDT

To check the value of the decoder

• The function of SW-SPST

To stop counting

Calculation of cycle time and minute cycle



Additional Assignment (Digital clock counter)

